

<b>Shenzhen P&amp;O Technology Co.,Limited</b>	Rev No	Issued Date.	Page
	A	2021.12.20	1/12

Project Size.	2.4 inch	
Model No.	P024C015	
Samples No.		
Product type.	240xRGBx320 MCU mode	
Signature by customer:		
Prepared	Checked	Approved

## Shenzhen P&O Technology Co.,Limited

Email: [polcd@polcd.com](mailto:polcd@polcd.com)

Mobile: 86-136 0019 7172

## 1.0 GENERAL DESCRIPTION

Item	Specification	Unit
Screen Size	2.4 inch	Diagonal
Number of Pixel	240RGB(H)x320(V)	Pixels
Display area	36.72(H)x48.96(V)	mm
Pixel pitch	51(H)x153(V)	um
Outline Dimension	42.72x60.26x2.30	mm
Pixel arrangement	RGB Vertical Stripe	--
Display mode	Normally White	--
Viewing Direction(eye)	12 O'CLOCK	--
Gray inversion direction	--	
Display Color	262K	--
Luminance(cd/m <sup>2</sup> )	260	nit
Contrast Ratio	250:1	--
Surface treatment	--	--
Interface	MCU 16bit	
Back-light	LED Side-light type	--
Drive IC	ILI9341V	
Operation Temperature	-20~70	°C
Storage Temperature	-30~80	°C
Weight	--	g

### 1.1 Features

- n MCU 16bit interface.

### 1.2 Applications

- n MPOS Device.
- n Personal Navigation Device.
- n Other devices which require high quality displays.

## 2.0 INPUT INTERFACE PIN ASSIGNMENT

FPC connector is used for electronics interface.

PinNo.	Symbol	Function
1-4	DB0-DB3	MCU parallel interface data bus.
5	GND	Ground
6	IOVCC	Power Supply. 1.8V
7	CS	Chip select input pin (active low)
8	RS	Display data/command selection pin in parallel
9	WR	Write enable in 8080 MCU parallel interface.
10	RD	Read enable in 8080 MCU parallel interface.
11	NC	NC
12	XL	Touch the left line
13	YU	Touch the upper circuit
14	XR	Touch the right end line
15	YD	Touch the lower line
16	LEDA	LED back light(Anode)
17-20	LEDK	LED back light(Cathode)
21	FMARK	Frame synchronization signal
22	DB4	MCU parallel interface data bus.
23-30	DB8-DB15	MCU parallel interface data bus.
31	RESET	External reset input.
32	VCC	Power Supply. 2.8V
33	IOVCC	Power Supply. 1.8V
34	GND	Ground
35-37	DB5-DB7	MCU parallel interface data bus.

## 3.0 OPTICAL CHARACTERISTICS

### 3.1 Optical specification

Item	Symbol	Condition	Min	Type	Max	Unit	Note
White luminance (Center)	Lv	$\Theta=0$ Normal Viewing Angle $I_{BL}=80mA$	--	260	--	cd/m <sup>2</sup>	(4)(5)(7)
Response time	Tr+Tf		--	30	--	ms	(3)
Contrast ratio	CR		--	250	--	--	(2)(4)
Color Chromaticity (CIE1931)	white		Wx	0.288	0.308	0.328	
		Wy	0.305	0.325	0.345		
Viewing Angle	Hor	$\Theta_L$	--	45	--		(1)
		$\Theta_R$	--	45	--		
	Ver	$\Theta_U$	--	45	--		
		$\Theta_D$	--	20	--		
Brightness uniformity	Avg	$\Theta=0$	80	90	--	%	(5)
Color Gamut	NTSC	$\Theta=0$	--	56	--	%	(6)
Optima View Direction	12 O'CLOCK						(1)

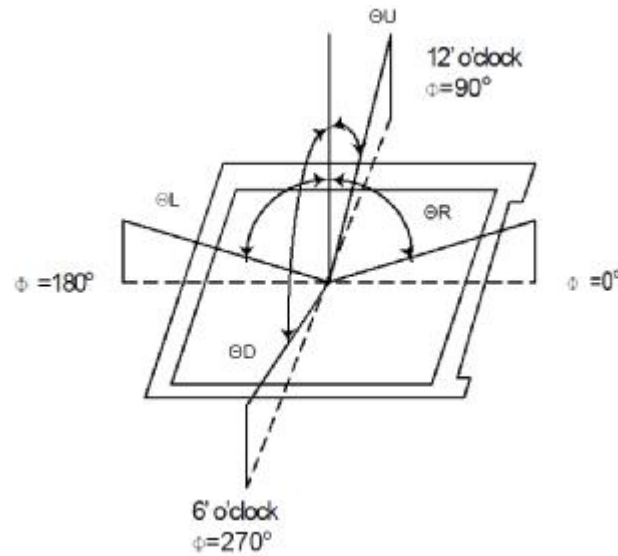
### 3.2 Measuring Condition

- n Measuring surrounding: dark room
- n LED current IL: 80mA
- n Ambient temperature:  $25 \pm 2^\circ C$
- n 15min. warm-up time

### 3.3 Measuring Equipment

- n FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-7 for other optical characteristics.
- n Measuring spot size: 20 ~ 21 mm

## Note (1) Definition of Viewing Angle

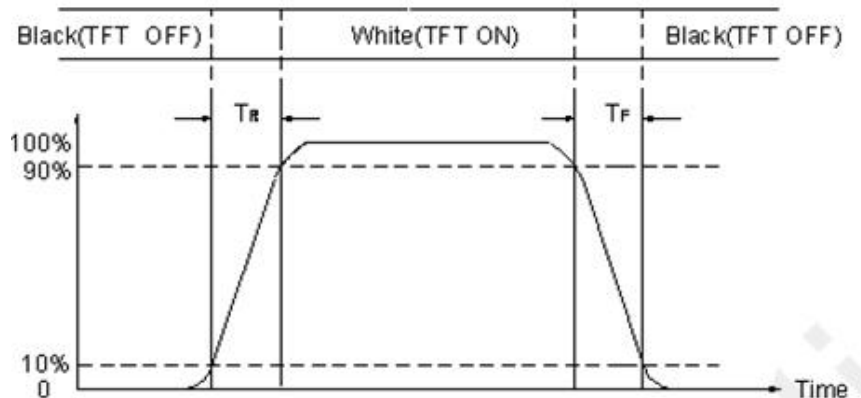


## Note (2) Definition of Contrast Ratio(CR):

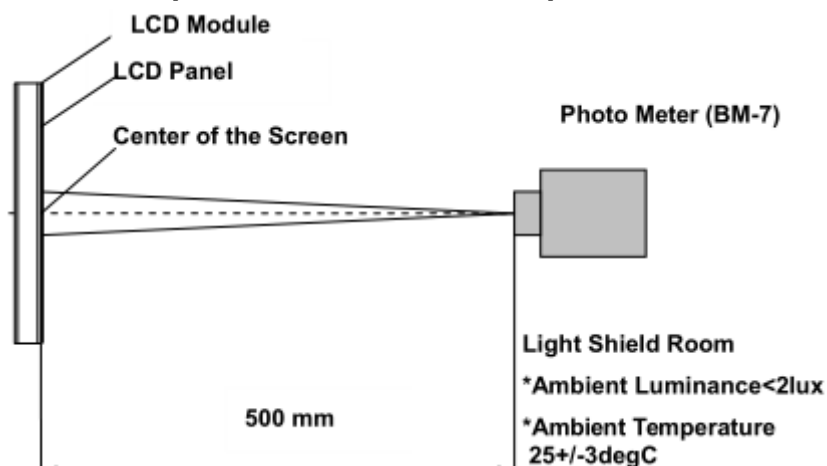
Measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

## Note (3) Definition of Response Time: Sum of TR and TF



## Note (4) Definition of optical measurement setup



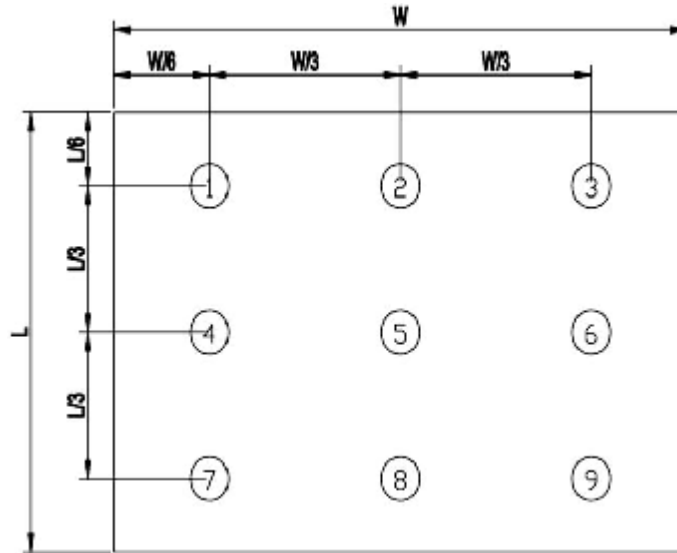
**Note (5) Definition of brightness uniformity**

The luminance uniformity is calculated by using following formula.

$$\Delta B_p = B_p (\text{Min.}) / B_p (\text{Max.}) \times 100 (\%)$$

$B_p (\text{Max.})$  = Maximum brightness in 9 measured spots

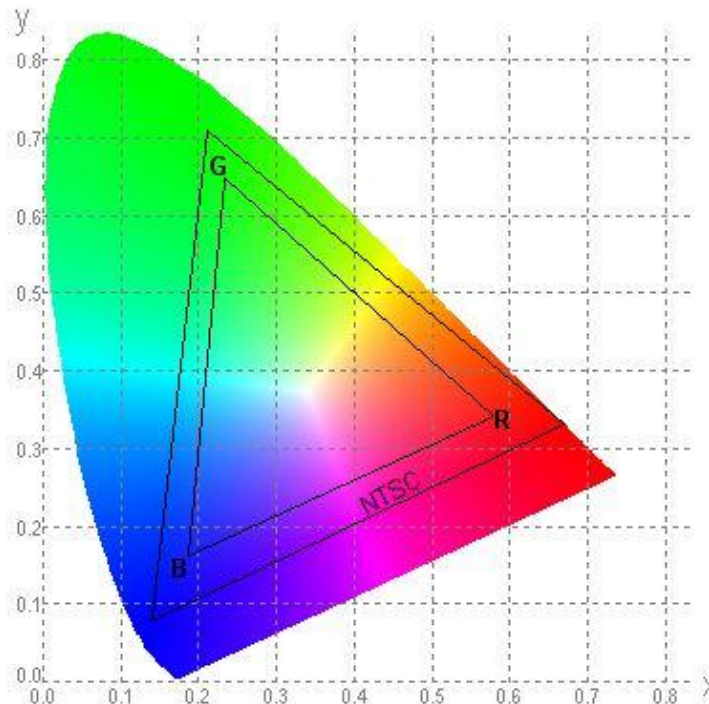
$B_p (\text{Min.})$  = Minimum brightness in 9 measured spots .



**Note (6) Definition of Color of CIE1931 Coordinate and NTSC Ratio.**

Color gamut:

$$S = \frac{\text{Area of RGB triangle}}{\text{Area of NTSC triangle}} \times 100\%$$



**Note (7) Measured the luminance of white state at center point.**

## 4.0 ELECTRICAL CHARACTERISTICS

### 4.1 TFT LCD Module

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Analog supply voltage	VDD	2.4	2.8	3.3	V	
Digital supply voltage	VDDI	1.65	1.8	3.3		
Input signal Voltage	VIH	0.7VDDI	-	VDDI	V	
	VIL	GND	-	0.3VDDI	V	

### 4.2 Back-Light Unit

The backlight system is an edge-lighting type with 4 LED Dies.

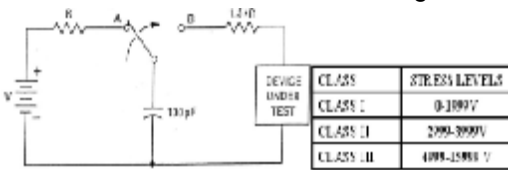
The characteristics of the LED are shown in the following tables.

Item	Symbol	Min	Typ	Max	Unit	Note
LED current	IL	-	60	80	mA	(2)
LED voltage	VL	-	2.8	3.2	V	
Operating LED life time	Hr	-	5000	5500	Hour	(1)(2)

Note (1) LED life time (Hr) can be defined as the time in which it continues to operate under the condition:  $T_a=25\pm 3\text{ }^{\circ}\text{C}$ , typical IL value indicated in the above table until the brightness becomes less than 50%.

Note (2) The "LED life time" is defined as the module brightness decrease to 50% original brightness at  $T_a=25^{\circ}\text{C}$  and  $IL=80\text{mA}$ . The LED lifetime could be decreased if operating IL is larger than 100mA. The constant current driving method is suggested.

## 5.0 Reliability conditions

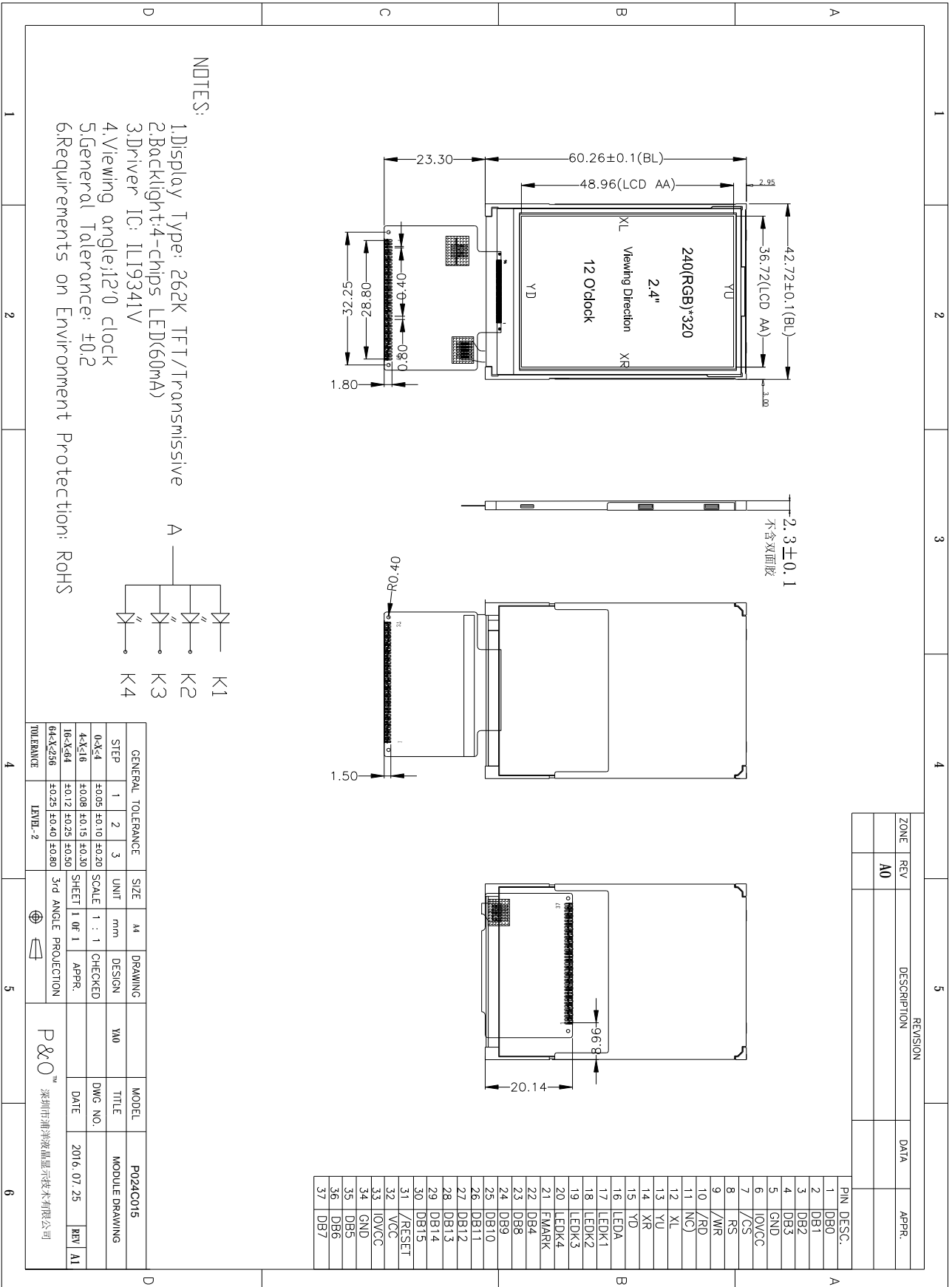
NO	Item	Conditions	Notes												
1	High Temperature Storage	Ta=80°C ± 2°C, 72hrs													
2	Low Temperature Storage	Ta=-30°C ± 2°C, 72hrs													
3	High Temperature Operation	Ta=70°C ± 2°C, 72hrs(Operation state)													
4	Low Temperature Operation	Ta=-20°C ± 2°C, 72hrs(Operation state)													
5	High Temperature and High Humidity (Storage)	Ta=+60°C, 90%RH, 72hrs													
6	Thermal Cycling Test (non operation)	-20°C (30min) → +70°C (30min), 10cycles													
7	Electro static Discharge	<p>Human Body Mode                      100pF ± 10%/1500 Ω ± 1%                      Air ± 8kV / contact ± 6kV                      Consecutive 10times/ Each discharge</p>  <table border="1" data-bbox="981 1131 1236 1243"> <thead> <tr> <th colspan="2">DEVICE UNDER TEST</th> <th>STRESS LEVELS</th> </tr> </thead> <tbody> <tr> <td>CLASS I</td> <td>0-100V</td> <td></td> </tr> <tr> <td>CLASS II</td> <td>200-3000V</td> <td></td> </tr> <tr> <td>CLASS III</td> <td>4000-15000V</td> <td></td> </tr> </tbody> </table>	DEVICE UNDER TEST		STRESS LEVELS	CLASS I	0-100V		CLASS II	200-3000V		CLASS III	4000-15000V		
DEVICE UNDER TEST		STRESS LEVELS													
CLASS I	0-100V														
CLASS II	200-3000V														
CLASS III	4000-15000V														
8	Vibration test(with carton)	<p>Total fixed amplitude:15mm                      Vibration Frequency :10~55Hz                      One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes</p>													
9	Drop (with carton)	<p>Height: 60cm                      1 corner, 3 edges, 6 surfaces</p>													

**Note: There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.**



Rev No	Issued Date.	Page
A	2021.12.20	9/12

## 6.0 OUTLINE DIMENSION



## 7.0 Items and Criteria:

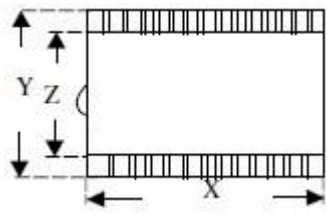
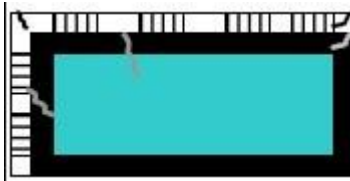
### 7.1 Guarantee

APEX warrants the quality of our products for **1 year** (from the date of delivery). If there are functional defects found during the period of warranty, the defective products would be replaced on a one-to-one basis. Apex would not be responsible for any direct /indirect liabilities consequential to any parties.

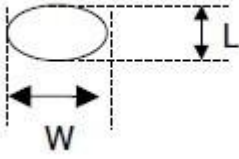
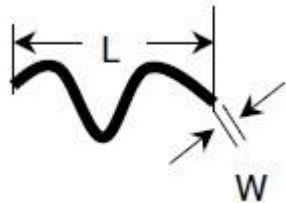
All the products should be stored or used as specified conditions described in these sheets. If module productions are not stored or used as specified conditions, herein, it will be void the **1 year** warranty(guarantee).

### 7.2 Visual inspection criterion in cosmetic

#### (1) Glass defect

Glass defect			
NO	Defect	Criteria	Remark
1	Dimension(Minor)	By engineering diagram	
2	Cracks(Major)	Extensive crack <b>【Reject】</b>	

#### (2) LCM appearance defect

NO	Defect	Criteria		Remark
1	Round type(Minor)	Spec	Permissible Qty	1. $\psi = (L+W)/2$ , L: Length, W: Width 2. Disregard if out of A.A. 
		$\psi \leq 0.10\text{mm}$	Disregard	
		$0.10\text{mm} < \psi \leq 0.20\text{mm}$	3	
		$0.20\text{mm} < \psi$	0	
2	Line type(Minor)	Spec	Permissible Qty	1. L: Length, W: Width 2. Disregard if out of A.A. 
		$W \leq 0.03\text{mm}$	Disregard	
		$L \leq 3.0\text{mm}$ and $0.03\text{mm} < W \leq 0.05\text{mm}$	2	
		$L \leq 3.0\text{mm}$ and $0.05\text{mm} < W \leq 0.10\text{mm}$	1	
		$W > 0.10\text{mm}$ or $L > 3.0\text{mm}$	0	
3	Polarizer defect(Minor)	Spec.	Permissible Qty	1. $\psi = (L+W)/2$ , L: Length, W: Width 2. Disregard if out of A.A.
		$\psi \leq 0.20\text{mm}$	Disregard	
		$0.20\text{mm} < \psi \leq 0.30\text{mm}$	2	
		$0.30\text{mm} < \psi \leq 0.50\text{mm}$	1	

<b>Shenzhen P&amp;O Technology Co., Limited</b>	Rev No	Issued Date.	Page
	A	2021.12.20	11/12

(3) FPC

NO	Defect	Criteria	Remark
1	Copper peeling(Minor)	Copper peeling <b>【Reject】</b>	
2	Golden finger	FPC golden finger broken, dead fold, indentation makes FPC surface broken <b>【Reject】</b> Tin plating layer(or gold plating) scratch, but not hurt circuit <b>【Accept】</b> Except circuit, other position scratch but not expose metal wire <b>【Accept】</b>	
3	Pin	FPC PI layer delamination <b>【Reject】</b> Material and color are inconsistent with sample, FPC burrs <b>【Reject】</b> FPC Pin deformation but not affect function. <b>【Accept】</b> FPC Pin area is dirty <b>【Reject】</b> Other than FPC Pin area is dirty but not affect function <b>【Accept】</b>	
4	Golden finger	Golden finger edge has burrs,foreign material <b>【Reject】</b> Golden finger oxidation (dark), uneven electroplating, pinhole, foreign material <b>【Reject】</b> Golden finger soldering pad crack exceeds 1/3 length of soldering pad, and soldering pad crack exceed 2 Pins <b>【Reject】</b> Golden finger tin plating(or gold plating)scratch, but not hurt circuit <b>【Accept】</b> Other than golden finger area scratch but not expose metal circuit <b>【Accept】</b>	
5	FPC Silk printing	Ghosting, incomplete silk printing, wrong printing <b>【Reject】</b>	
6	FPC Circuit line width	Line width deviation exceed 1/3 line width <b>【Reject】</b>	


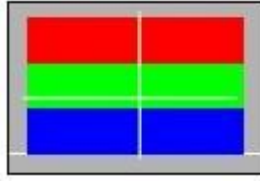

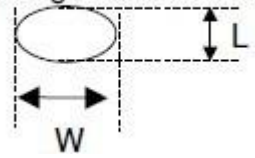
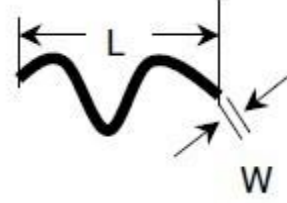
(4) Black tape

NO	Defect	Criteria	Remark
1	Shift(Minor)	IC exposed <b>【Reject】</b>	
2	No black tape(Minor)	No black tape <b>【Reject】</b>	

(5) Silicon

NO	Defect	Criteria	Remark
1	Amount of silicon (Minor)	ITO exposed <b>【Reject】</b>	

### 7.3 Visual inspection criterion in electrical display

NO	Defect	Criteria		Remark
1	No display (Major)	Not allowed		
2	Missing line (Major)	Not allowed		
3	Darker or lighter Line (Major)	Not allowed		
4	Weak line(Major)	By limited sample		
5	Bright / Dark point (Minor)	Spec.	Permissible Qty	1:1sub-pixel: 1R or 1G or1B 2:Point defect area $\geq$ 1/2 sub pixel.
		Bright point	1	
		Dark point	2	
6	Round type (Minor)	Spec	Permissible Qty	1. $\psi=(L+W)/2$ , L: Length, W: Width 2. Disregard if out of A.A. 
		$\psi \leq 0.10\text{mm}$	Disregard	
		$0.10\text{mm} < \psi \leq 0.20\text{mm}$	3	
		$0.20\text{mm} < \psi$	0	
7	Line type (Minor)	Spec.	Permissible Qty	1. L: Length, W: Width 2. Disregard if out of A.A. 
		$W \leq 0.03\text{mm}$	Disregard	
		$L \leq 3.0\text{mm}$ and $0.03\text{mm} < W \leq 0.05\text{mm}$	2	
		$L \leq 3.0\text{mm}$ and $0.05\text{mm} < W \leq 0.10\text{mm}$	1	
		$W > 0.10\text{mm}$ or $L > 3.0\text{mm}$	0	
8	Mura (Minor)	By 5% ND filter invisible		