

3-Terminal 1.5A Positive Adjustable Regulator

DESCRIPTION

This monolithic integrated circuit is an adjustable 3-terminal positive voltage regulator designed to supply more than 1.5A of load current with an output voltage adjustable over a 1.2 to 37V. It employs internal current limiting, thermal shut-down and safe area compensation.

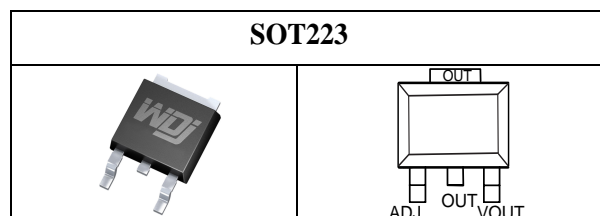
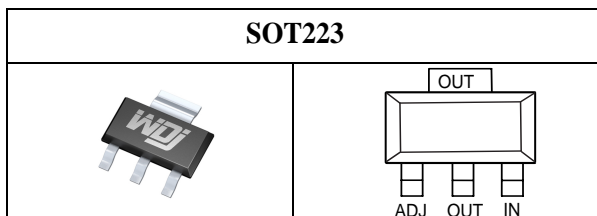
FEATURE

- Internal thermal overload protection
- Internal short circuit current limiting
- Output transistor safe operating area compensation

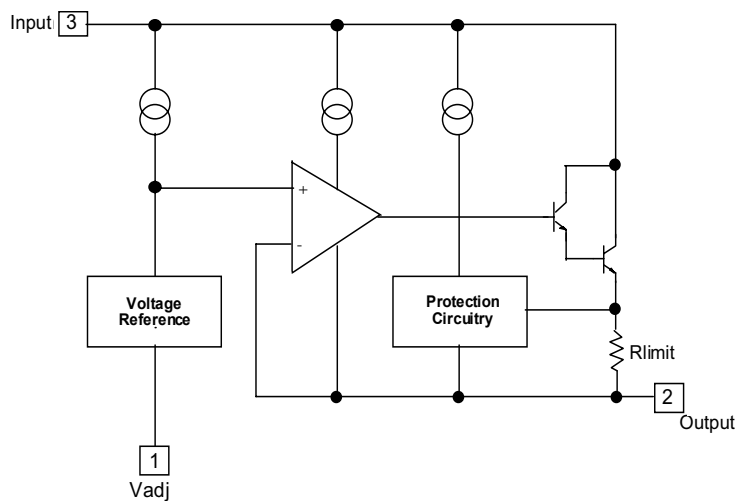
ORDERING INFORMATION

| Device | Package |
|---------------|---------|
| WDJ LM317MDT | TO-252 |
| WDJ LM317DCYR | SOT-223 |

PIN CONFIGURATION



INTERNAL Internal Block Diagram



Absolute Maximum Ratings

| Symbol | Parameter | Value | Units |
|-------------------------|---|--------------------|-------|
| V_I-V_O | Input-Output Voltage Differential | 40 | V |
| T_{LEAD} | Lead Temperature | 230 | °C |
| P_D | Power Dissipation | Internally limited | W |
| T_J | Operating Junction Temperature Range | 0~125 | °C |
| T_{stg} | Storage Temperature Range | -55~125 | |
| $\Delta V_O / \Delta T$ | Temperature Coefficient of Output Voltage | ±0.02 | %/°C |

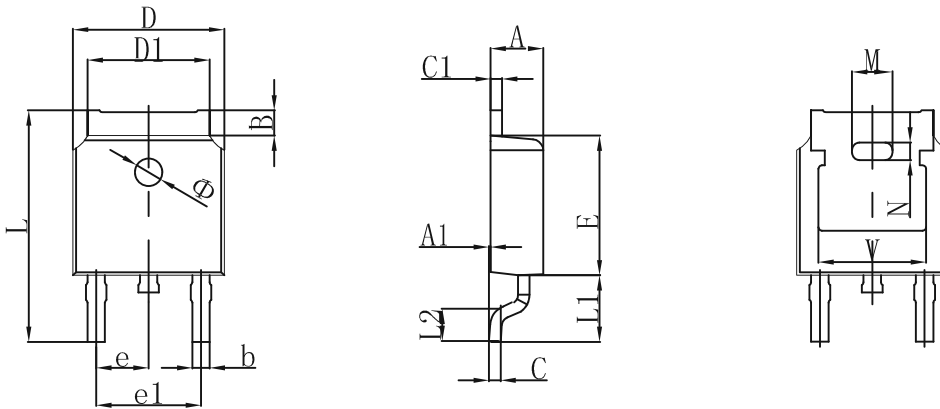
ELECTRICAL CHARACTERISTICS

($V_O-V_I=5V, I_O=0.5A, 0^\circ C \leq T_J \leq +125^\circ C, I_{MAX}=1.5A, P_{DMAX}=20W$, unless otherwise specified)

| Parameter | Symbol | Test conditions | MIN | TYP | MAX | UNIT |
|---|------------------|--|------|------------|-----------|----------|
| Line Regulation(note1) | R_{line} | $T_A=25^\circ C$ $3V \leq V_I-V_O \leq 40V$ | | 0.01 | 0.04 | %V |
| | | $3V \leq V_I-V_O \leq 40V$ | | 0.02 | 0.07 | |
| Load Regulation(note1) | R_{load} | $T_A=25^\circ C, 10mA \leq I_O \leq I_{MAX}$ $V_O < 5V$ $V_O \geq 5V$ | | 18 0.4 | 25 0.5 | mV |
| | | $10mA \leq I_O \leq I_{MAX}$ $V_O < 5V$ $V_O \geq 5V$ | | 40 0.8 | 70 1.5 | % V_O |
| Adjustable Pin Current | I_{ADJ} | - | | 46 | 100 | μA |
| Adjustable Pin Current Change | ΔI_{ADJ} | $3V \leq V_I-V_O \leq 40V$ $10mA \leq I_O \leq I_{MAX}, P_D \leq P_{MAX}$ | | 2.0 | 5 | |
| Reference Voltage | V_{REF} | $3V \leq V_{IN}-V_O \leq 40V$ $10mA \leq I_O \leq I_{MAX}, P_D \leq P_{MAX}$ | 1.20 | 1.25 | 1.30 | V |
| Temperature Stability | ST_T | - | | 0.7 | | %/ V_O |
| Minimum Load Current to Maintain Regulation | $I_{L(MIN)}$ | $V_I-V_O=40V$ | | 3.5 | 12 | mA |
| Maximum Output Current | $I_{O(MAX)}$ | $V_I-V_O \leq 15V, P_D \leq P_{MAX}$ $V_I-V_O \leq 40V, P_D \leq P_{MAX}$ $T_A=25^\circ C$ | 1.0 | 2.2 0.3 | | A |
| RMS Noise,% of V_{OUT} | e_N | $T_A=25^\circ C, 10Hz \leq f \leq 10KHz$ | | 0.003 | 0.01 | %/ V_O |
| Ripple Rejection | RR | $V_O=10V, f=120Hz$ without C_{ADJ} $C_{ADJ}=10\mu F$ (note2) | 66 | 60 75 | | dB |
| Long-Term Stability, $T_J=T_{HIGH}$ | ST | $T_A=25^\circ C$ for end point measurements,1000HR | | 0.3 | 1 | % |
| Thermal Resistance Junction to case | $R_{\theta JC}$ | - | | 5 | | °C/W |

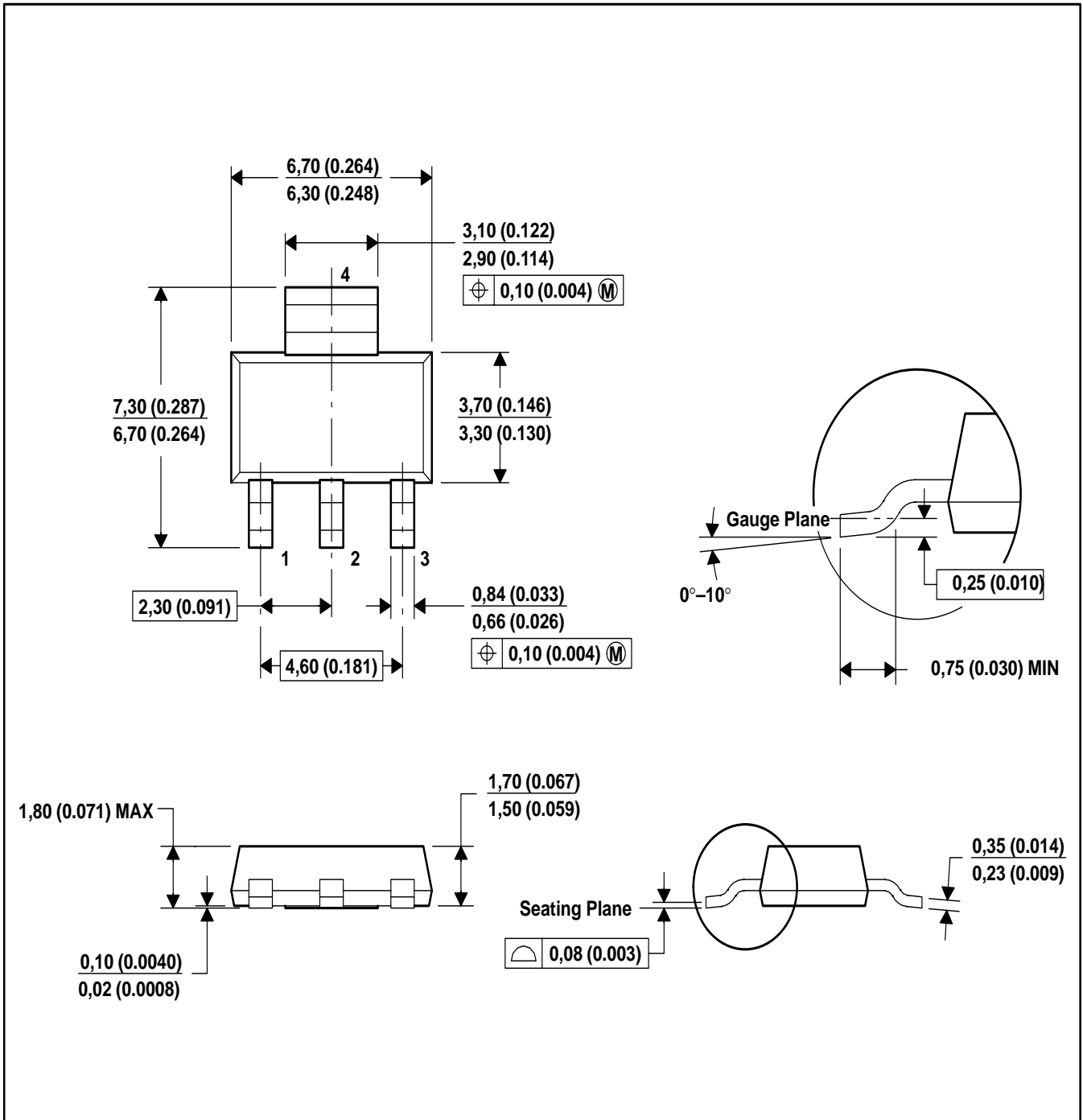
TRANSISTOR OUTLINE

TO-252



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|--------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 2.200 | 2.380 | 0.087 | 0.094 |
| A1 | 0.000 | 0.100 | 0.000 | 0.004 |
| B | 0.800 | 1.400 | 0.031 | 0.055 |
| b | 0.710 | 0.810 | 0.028 | 0.032 |
| c | 0.460 | 0.560 | 0.018 | 0.022 |
| c1 | 0.460 | 0.560 | 0.018 | 0.022 |
| D | 6.500 | 6.700 | 0.256 | 0.264 |
| D1 | 5.130 | 5.460 | 0.202 | 0.215 |
| E | 6.000 | 6.200 | 0.236 | 0.244 |
| e | 2.286 TYP. | | 0.090 TYP. | |
| e1 | 4.327 | 4.727 | 0.170 | 0.186 |
| M | 1.778REF. | | 0.070REF. | |
| N | 0.762REF. | | 0.018REF. | |
| L | 9.800 | 10.400 | 0.386 | 0.409 |
| L1 | 2.9REF. | | 0.114REF. | |
| L2 | 1.400 | 1.700 | 0.055 | 0.067 |
| V | 4.830 REF. | | 0.190 REF. | |
| Φ | 1.100 | 1.300 | 0.043 | 0.051 |

SOT-223



**CAUTION:**

These devices are sensitive to electrostatic discharge;
follow proper IC Handling Procedures.

For additional product information, or full datasheet,
please contact with our Sales Department or Representatives.