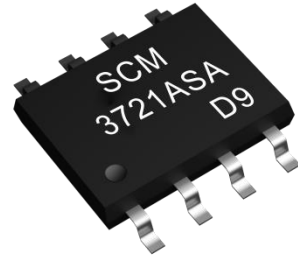


SCM3721ASA/SCM3723ASA Dual-Channel Digital Isolators

Features

- 3 V to 5.5 V level translation
- High common-mode transient immunity: 150 kV/μs typical
- High robustness to radiated and conducted noise
- High ESD rating
- Ultra low power consumption 0.55mA/channel(150Kbps)
- Wide temperature range: -40°C ~ 125°C
- Isolation voltages: AC 3000Vrms
- High data rate:150kbps

Package



Mechanical package: SOP-8
(see "Ordering information" for details).

Applications

- General-purpose multichannel isolation
- Industrial field bus isolation

Functional

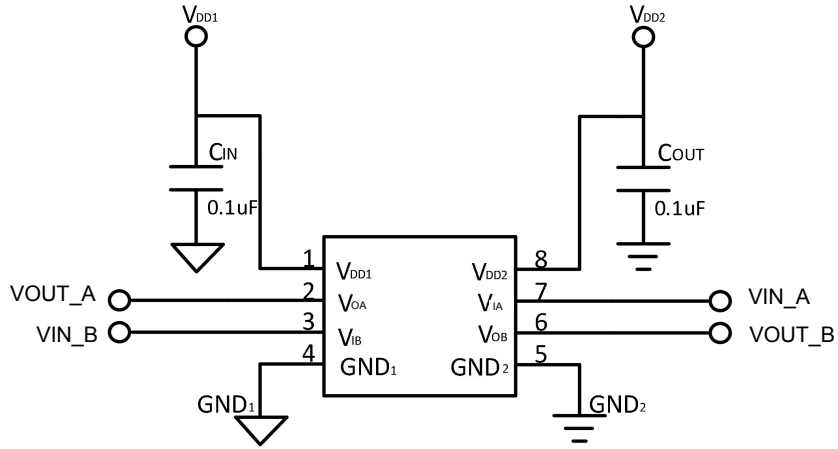
The SCM3721ASA/SCM3723ASA digital isolators by using matured standard semiconductor CMOS technology and Capacitance pulse isolation technology, these isolation components provide outstanding performance characteristics and reliability superior to alternatives such as optocoupler devices and other integrated isolators.

Capacitance pulse isolation technology is a new generation digital isolator technology . It uses the principle of capacitor voltage divider to transmit voltage signal directly cross the isolator capacitor without signal modulation and demodulation.

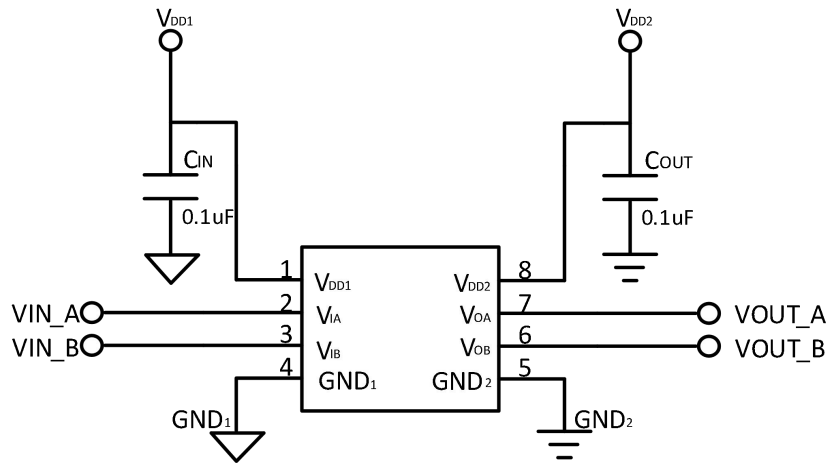
The SCM3721ASA/SCM3723ASA isolator data channels are independent and different models have different transmission directions with a withstand voltage rating of 3 kV rms and the data rate from DC up to 150Kbps . The devices operate with the supply voltage on either side ranging from 3.0 V to 5.5 V, providing compatibility with lower voltage systems as well as enabling voltage translation functionality across the isolation barrier. The fail-safe state is available in which the outputs transition to a preset state when the input power supply is not applied.

Product model

| Model | Power supply range (V) | Data rate | Channel number | Output channel number | Withstand voltage(kV rms) | Default output | Package option | Pin number |
|------------|--------------------------|-----------|----------------|-----------------------|---------------------------|----------------|----------------|------------|
| SCM3721ASA | 3.0~5.5 | 150Kbps | 2 | 1 | 3 | high | SOP | 8 |
| SCM3723ASA | 3.0~5.5 | 150Kbps | 2 | 2 | 3 | high | SOP | 8 |



Typical Circuit: 1 SCM3721ASA Application Diagram

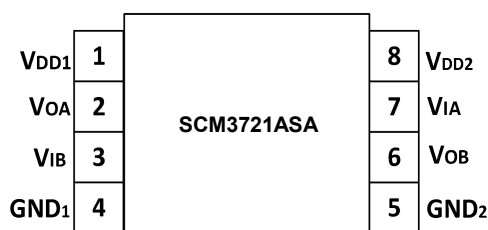


Typical Circuit: 2 SCM3723ASA Application Diagram

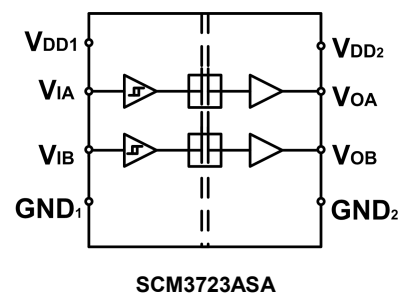
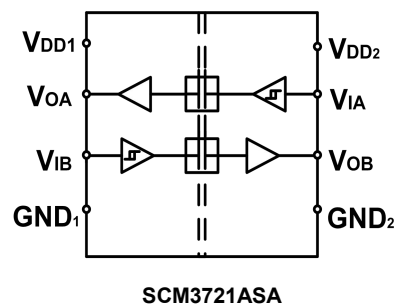
目录

| | | | |
|---------------------------------------|---|---|----|
| Feature and Package | 1 | Electrical Characteristics..... | 5 |
| Applications..... | 1 | Insulation and Safety Related Specifications..... | 6 |
| Functional Description..... | 1 | Package Characteristics..... | 6 |
| Product model..... | 1 | Regulatory Information..... | 7 |
| Typical Application Circuit..... | 2 | Insulation Characteristics..... | 7 |
| Pin Connection..... | 3 | Typical Performance Curves..... | 8 |
| Inner Block Diagram..... | 3 | Applications Information..... | 10 |
| Function Table..... | 3 | Order Information..... | 11 |
| Pin Descriptions..... | 4 | Package Information..... | 11 |
| Absolute Maximum Rating..... | 4 | Tape & Reel Information..... | 12 |
| Recommended Operating Conditions..... | 4 | | |

Pin Connection



Internal Block



Function table

| V _{Ix} Input ¹ | V _{DDI} State ¹ | V _{DDO} State ¹ | Default Low | Default High | Test Conditions /Comments |
|------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|-------------------------------------|-----------------------------|
| | | | VO _x Output ¹ | VO _x Output ¹ | |
| Low | Powered ² | Powered ² | Low | Low | Normal operation |
| High | Powered ² | Powered ² | High | High | Normal operation |
| Open | Powered ² | Powered ² | Low | High | Default output |
| Don't Care ⁴ | Unpowered ³ | Powered ² | Low | High | Default output ⁵ |
| Don't Care ⁴ | Powered ² | Unpowered ³ | High Impedance | High Impedance | |

- Notes:
1. V_{Ix}/V_{Ox} are the input/output signals of a given channel (A or B). V_{DDI}/V_{DDO} are the supply voltages on the input/output signal sides of this given channel.
 2. Powered means V_{DDx} ≥ 2.9 V
 3. Unpowered means V_{DDx} < 2.3V
 4. Input signal (V_{Ix}) must be in a low state to avoid powering the given V_{DDI}1 through its ESD protection circuitry.
 5. If the V_{DDI} goes into unpowered status, the channel outputs the default logic signal after around 1μs. If the V_{DDI} goes into powered status, the channel outputs the input status logic signal after around 3μs.

Pin descriptions

| SCM3721ASA | | |
|------------|------|---|
| Pin No. | Name | Description |
| 1 | VDD1 | Supply Voltage for Isolator Side 1. |
| 2 | VOA | Logic Output A. |
| 3 | VIB | Logic Input B. |
| 4 | GND1 | Ground 1. This pin is the ground reference for Isolator Side 1. |
| 5 | GND2 | Ground 2. This pin is the ground reference for Isolator Side 2. |
| 6 | VOB | Logic Output B. |
| 7 | VIA | Logic Input A. |
| 8 | VDD2 | Supply Voltage for Isolator Side 2. |

| SCM3721ASA | | |
|------------|------|---|
| Pin No. | Name | Description |
| 1 | VDD1 | Supply Voltage for Isolator Side 1. |
| 2 | VIA | Logic Input A. |
| 3 | VIB | Logic Input B. |
| 4 | GND1 | Ground 1. This pin is the ground reference for Isolator Side 1. |
| 5 | GND2 | Ground 2. This pin is the ground reference for Isolator Side 2. |
| 6 | VOB | Logic Output B. |
| 7 | VOA | Logic Output A. |
| 8 | VDD2 | Supply Voltage for Isolator Side 2. |

Absolute Maximum Ratings

$T_A = 25^\circ\text{C}$, unless otherwise noted.

| Parameter | SYMBOL | RANGE | UNIT |
|---|------------------------------|--|-------------------|
| Supply Voltages | $V_{DD1-GND1}, V_{DD2-GND2}$ | -0.5 V to +7.0 V | V |
| Input Voltages | V_{IA}, V_{IB} | -0.5 V to $V_{DDx} + 0.5$ V | V |
| Output Voltages | V_{OA}, V_{OB} | -0.5 V to $V_{DDx} + 0.5$ V | V |
| Average Output Current per Pin ² Side 1 Output Current | I_{O1} | -10 mA to +10 mA | mA |
| Average Output Current per Pin Side 2 Output Current | I_{O2} | -10 mA to +10 mA | mA |
| Common-Mode Transients Immunity | | -150 kV/ μs to +150 kV/ μs | kV/ μs |
| Storage Temperature Range | T_{ST} | -65°C to +150°C | °C |
| Ambient Operating Temperature Range | T_A | -40°C to +125°C | °C |

Notes:

1 VDDx is the side voltage power supply VDD, where x = 1 or 2.

2 See Figure1 for the maximum rated current values for various temperatures.

3 See Figure9 for Common-mode transient immunity (CMTI) measurement.

4 Stresses at or above those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Operation beyond the maximum operating conditions for extended periods may affect product reliability.

Recommended Operating Conditions

| Parameter | Symbol | Min | Typ | Max | Unit |
|---------------------------------|-------------|-----------------------|-----|-----------------------|------|
| Supply Voltage | V_{DDx}^1 | 3 | | 5.5 | V |
| High Level Input Signal Voltage | V_{IH} | $0.7 \cdot V_{DDx}^1$ | | V_{DDx}^1 | V |
| Low Level Input Signal Voltage | V_{IL} | 0 | | $0.3 \cdot V_{DDx}^1$ | V |
| High Level Output Current | I_{OH} | -6 | | | mA |
| Low Level Output Current | I_{OL} | | | 6 | mA |
| Maximum Data Rate | | 0 | | 150 | Kbps |
| Junction Temperature | T_J | -40 | | 150 | °C |
| Ambient Operating Temperature | T_A | -40 | | 125 | °C |

Notes:

1 VDDx is the side voltage power supply VDD, where x = 1 or 2.

| Switching Specifications | | | | | | |
|--|--------------------------------|--|------------------------|------------------------|----------------------|-------|
| Parameter | Symbol | Test Conditions/Comments | Min | Typ | Max | Unit |
| Minimum Pulse Width | PW | Within pulse width distortion (PWD) limit | | | 6.5 | us |
| Maximum Data Rate | | Within PWD limit | 150 | | | Kbps |
| Propagation Delay Time | tpHL, tpLH | The different time between 50% input signal to 50% output signal 50% @ 5VDC supply | | 3.0 | 4.5 | us |
| | | @ 3.3VDC supply | | 3.2 | 4.8 | us |
| Pulse Width Distortion | PWD | The max different time between tpHL and tpLH@ 5VDC supply. And The value is tpHL - tpLH | 0 | 0.02 | 0.2 | us |
| | | @ 3.3VDC supply | 0 | 0.02 | 0.2 | us |
| Part to Part Propagation Delay Skew | tPSK | The max different propagation delay time between any two devices at the same temperature, load and voltage @ 5VDC supply | | | 0.3 | us |
| | | @ 3.3VDC supply | | | 0.3 | us |
| Channel to Channel Propagation Delay Skew | tCSK | The max amount propagation delay time differs between any two output channels in the single device @ 5VDC supply. | | 0 | 0.2 | us |
| | | @ 3.3VDC supply | | 0 | 0.2 | us |
| Output Signal Rise/Fall Time | t _r /t _f | 10% to 90% signal terminated 50 , See figure15 | | 1.5 | | us |
| Common-Mode Transient Immunity | CMTI | V _{IN} = V _{DDx} ² or 0V, V _{CM} = 1000 V. | 100 | 150 | | kV/μs |
| ESD (HBM - Human body model) | ESD | All pins | | ±8 | | kV |
| DC Specifications | | | | | | |
| Parameter | Symbol | Test Conditions/Comments | Min | Typ | Max | Unit |
| Rising Input Signal Voltage Threshold | V _{IT+} | | | 0.6*V _{DDx} | 0.7*V _{DDx} | V |
| Falling Input Signal Voltage Threshold | V _{IT-} | | 0.3* V _{DDx} | 0.4* V _{DDx} | | V |
| High Level Output Voltage | VOH | -20 μA output current | V _{DDx} - 0.1 | V _{DDx} | | V |
| | | -2 mA output current | V _{DDx} - 0.2 | V _{DDx} - 0.1 | | V |
| Low Level Output Voltage | VOL | 20 μA output current | | 0 | 0.1 | V |
| | | 2 mA output current | | 0.1 | 0.2 | V |
| Input Current per Signal Channel | I _{IN} | 0 V ≤ Signal voltage ≤ V _{DDx} | -10 | 0.5 | 10 | μA |
| V _{DDx} ¹ Undervoltage Rising Threshold | VDDxUV+ | | 2.45 | 2.65 | 2.9 | V |
| V _{DDx} ¹ Undervoltage Falling Threshold | VDDxUV- | | 2.3 | 2.5 | 2.75 | V |
| V _{DDx} ¹ Hysteresis | VDDxUVH | | | 0.15 | | V |
| Quiescent Supply Current | | | | | | |
| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit |
| SCM3721ASA Quiescent Supply Current @ 5VDC Supply | IDD1 (Q) | 0V Input signal | 431 | 539 | 701 | μA |
| | IDD2 (Q) | 0V Input signal | 431 | 539 | 701 | μA |
| | IDD1 (Q) | 5V Input signal | 414 | 518 | 673 | μA |
| | IDD2 (Q) | 5V Input signal | 414 | 518 | 673 | μA |
| SCM3721ASA Quiescent Supply Current @ 3.3VDC Supply | IDD1 (Q) | 0V Input signal | 405 | 506 | 658 | μA |
| | IDD2 (Q) | 0V Input signal | 405 | 506 | 658 | μA |
| | IDD1 (Q) | 3.3V Input signal | 410 | 512 | 666 | μA |
| | IDD2 (Q) | 3.3V Input signal | 410 | 512 | 666 | μA |
| SCM3723ASA Quiescent Supply Current @ 5VDC Supply | IDD1 (Q) | 0V Input signal | 154 | 192 | 250 | μA |
| | IDD2 (Q) | 0V Input signal | 708 | 885 | 1151 | μA |
| | IDD1 (Q) | 5V Input signal | 61 | 76 | 99 | μA |
| | IDD2 (Q) | 5V Input signal | 767 | 959 | 1247 | μA |
| SCM3723ASA Quiescent Supply Current @ 3.3VDC Supply | IDD1 (Q) | 0V Input signal | 113 | 141 | 183 | μA |
| | IDD2 (Q) | 0V Input signal | 696 | 870 | 1131 | μA |
| | IDD1 (Q) | 3.3V Input signal | 60 | 75 | 98 | μA |
| | IDD2 (Q) | 3.3V Input signal | 758 | 948 | 1232 | μA |
| Total Supply Current vs. Data Throughput (CL = 0 pF) | | | | | | |
| V _{DD1} - V _{GND1} = V _{DD2} - V _{GND2} = 3.3VDC±10% or 5VDC±10%, T _A =25°C, CL = 0 pF, unless otherwise noted. | | | | | | |

| Parameter | Symbol | 2 Kbps | | | 50Kbps | | | 150Kbps | | | Unit |
|------------------------------------|--------|--------|------|------|--------|------|------|---------|------|------|------|
| | | Min | Typ | Max | Min | Typ | Max | Min | Typ | Max | |
| SCM3721ASA Supply Current @ 5VDC | IDD1 | | 0.53 | 0.80 | | 0.53 | 0.80 | | 0.54 | 0.81 | mA |
| | IDD2 | | 0.53 | 0.80 | | 0.53 | 0.80 | | 0.54 | 0.81 | mA |
| SCM3721ASA Supply Current @ 3.3VDC | IDD1 | | 0.51 | 0.77 | | 0.51 | 0.77 | | 0.51 | 0.77 | mA |
| | IDD2 | | 0.51 | 0.77 | | 0.51 | 0.77 | | 0.51 | 0.77 | mA |
| SCM3723ASA Supply Current @ 5VDC | IDD1 | | 0.13 | 0.20 | | 0.13 | 0.20 | | 0.13 | 0.20 | mA |
| | IDD2 | | 0.92 | 1.38 | | 0.93 | 1.40 | | 0.94 | 1.41 | mA |
| SCM3723ASA Supply Current @ 3.3VDC | IDD1 | | 0.10 | 0.15 | | 0.10 | 0.15 | | 0.10 | 0.15 | mA |
| | IDD2 | | 0.91 | 1.37 | | 0.91 | 1.37 | | 0.92 | 1.38 | mA |

INSULATION AND SAFETY RELATED SPECIFICATIONS

| Insulation Specifications | | | | | |
|--|---------|-----------------------|--|--------|--|
| Parameter | Symbol | Value | | Unit | Test Conditions/Comments |
| | | SCM3721ASA/SCM3723ASA | | | |
| Rated Dielectric Insulation Voltage | | 3000 | | V rms | 1-minute duration |
| Minimum External Air Gap (Clearance) | L (CLR) | 4 | | mm min | Measured from input terminals to output terminals, shortest distance through air |
| Minimum External Tracking (Creepage) | L (CRP) | 4 | | mm min | Measured from input terminals to output terminals, shortest distance path along body |
| Minimum Internal Gap (Internal Clearance) | | 11 | | µm min | Insulation distance through insulation |
| Tracking Resistance (Comparative Tracking Index) | CTI | >400 | | V | DIN IEC 112/VDE 0303 Part 1 |
| Material Group | | II | | | Material Group (DIN VDE 0110, 1/89, Table 1) |

PACKAGE CHARACTERISTICS

| Package Characteristics | | | | | |
|---|-----------------|-----------------------|--|------|---|
| Parameter | Symbol | Typical Value | | Unit | Test Conditions/Comments |
| | | SCM3721ASA/SCM3723ASA | | | |
| Resistance (Input to Output) | RI-O | 10 ¹¹ | | Ω | |
| Capacitance (Input to Output) | CI-O | 0.6 | | pF | @1MHz |
| Input Capacitance | C _i | 3 | | pF | @1MHz |
| IC Junction to Ambient Thermal Resistance | θ _{JA} | 100 | | °C/W | Thermocouple located at center of package underside |

REGULATORY INFORMATION

See the Table and the Insulation Lifetime section for details regarding recommended maximum working voltages for specific cross isolation waveforms and insulation levels.

| Regulatory | SCM3721ASA/SCM3723ASA |
|------------|--|
| UL | Recognized under UL 1577 Component Recognition Program ¹ Single Protection, 3000 V rms Isolation Voltage |
| CSA | Approved under CSA Component Acceptance Notice 5A CSA 60950-1-07+A1+A2 and IEC 60950-1, second edition, +A1+A2: Basic insulation at 500 V rms (707 V peak) Reinforced insulation at 250 V rms (353 V peak) |
| VDE | DIN V VDE V 0884-10 (VDE V 0884-10):2006-12 ² Basic insulation, V _{IORM} = 707 V peak, V _{IOSM} = 4615 V peak |
| CQC | Certified under CQC11-471543-2012 GB4943.1-2011 Basic insulation at 500 V rms (707 V peak) working voltage Reinforced insulation at 250 V rms (353 V peak) |

INSULATION CHARACTERISTICS

| Description | Test Conditions/Comments | Symbol | Characteristic | Unit |
|--|---|---------|--------------------------|--------------------|
| | | | SCM3721ASA SCM3723ASA | |
| Installation Classification per DIN VDE 0110 | | | | |
| For Rated Mains Voltage ≤ 150 V rms | | | I to IV | |
| For Rated Mains Voltage ≤ 300 V rms | | | I to III | |
| For Rated Mains Voltage ≤ 400 V rms | | | I to III | |
| Climatic Classification | | | 40/105/21 | |
| Pollution Degree per DIN VDE 0110, Table 1 | | | 2 | |
| Maximum Working Insulation Voltage | | VIORM | 707 | V peak |
| Input to Output Test Voltage, Method B1 | $V_{IORM} \times 1.875 = V_{pd(m)}$, 100% production test, $t_{ini} = t_m = 1$ sec, partial discharge < 5 pC | Vpd (m) | 1326 | V peak |
| After Environmental Tests Subgroup 1 | $V_{IORM} \times 1.5 = V_{pd(m)}$, $t_{ini} = 60$ sec, $t_m = 10$ sec, partial discharge < 5 pC | Vpd (m) | 1061 | V peak |
| After Input and/or Safety Test Subgroup 2 and Subgroup 3 | $V_{IORM} \times 1.2 = V_{pd(m)}$, $t_{ini} = 60$ sec, $t_m = 10$ sec, partial discharge < 5 pC | | 849 | V peak |
| Highest Allowable Overvoltage | | VIOTM | 4200 | V peak |
| Surge Isolation Voltage Basic | Basic insulation, $1.2 \mu\text{s}$ rise time, $50 \mu\text{s}$, 50% fall time | VIOSM | 4615 | V peak |
| Safety Limiting Values | Maximum value allowed in the event of a failure (see Figure 3) | | | |
| Maximum Junction Temperature | | T_s | 150 | $^{\circ}\text{C}$ |
| Total Power Dissipation at 25°C | | P_s | 1.56 | W |
| Insulation Resistance at T_s | $V_{IO} = 800$ V | R_s | $>10^9$ | Ω |

Typical Performance Curves

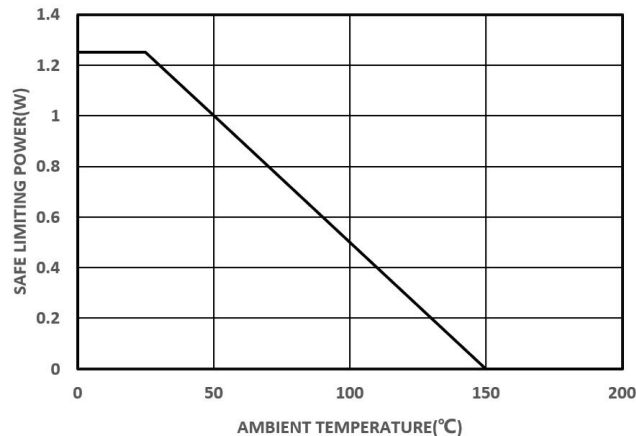
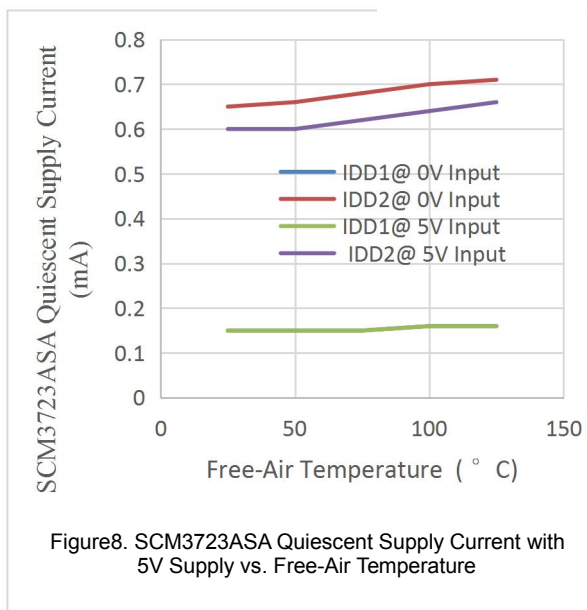
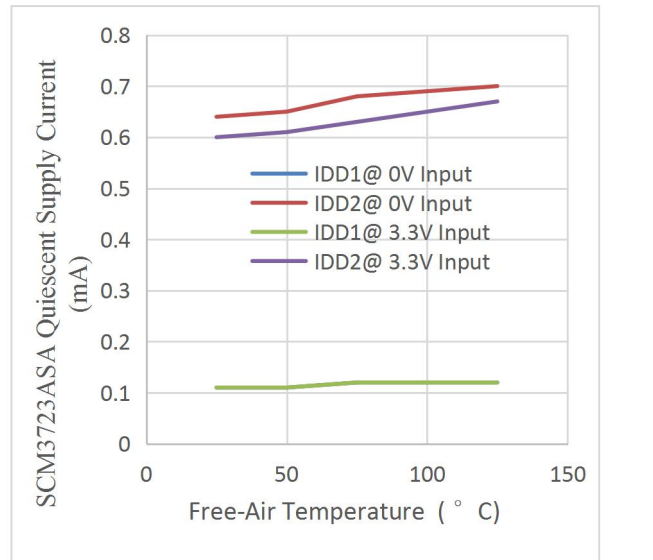
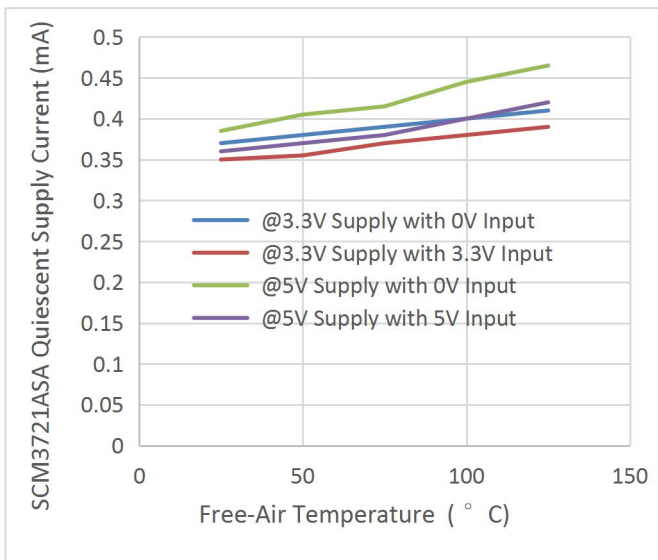
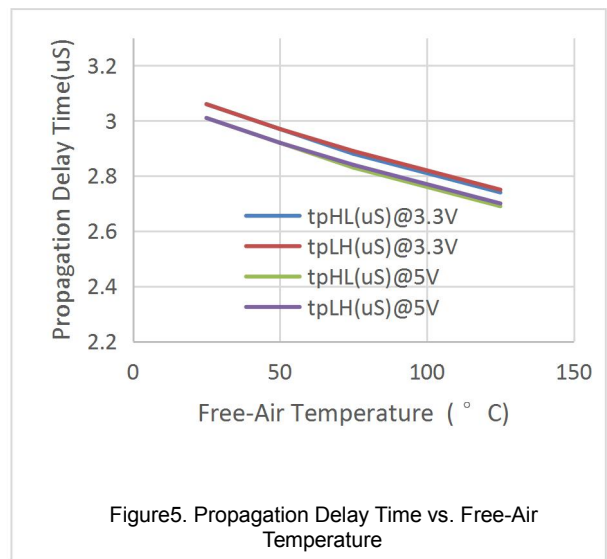
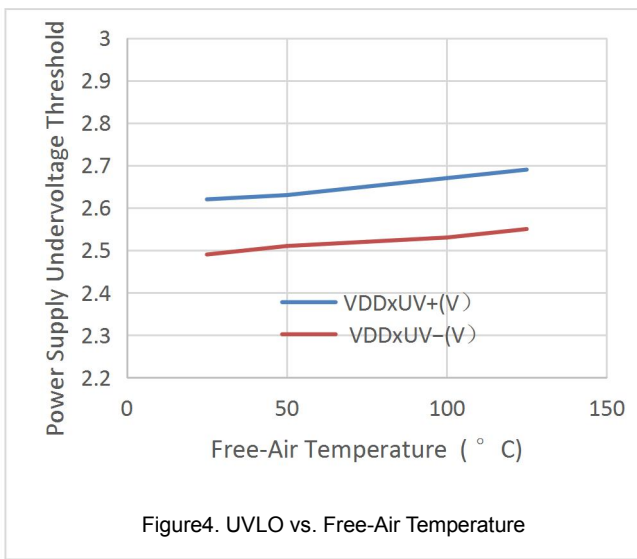


Figure3. Thermal Derating Curve, Dependence of Safety Limiting Values with Ambient Temperature per VDE



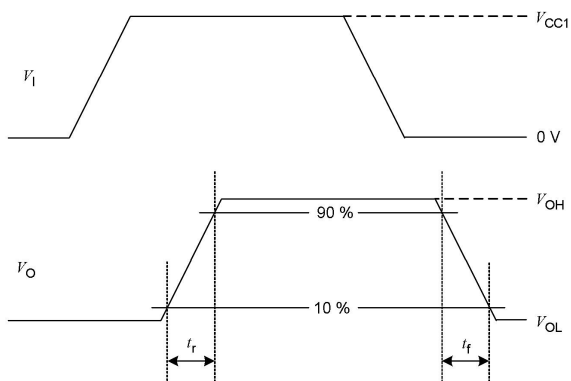


Figure9. Transition time waveform measurement

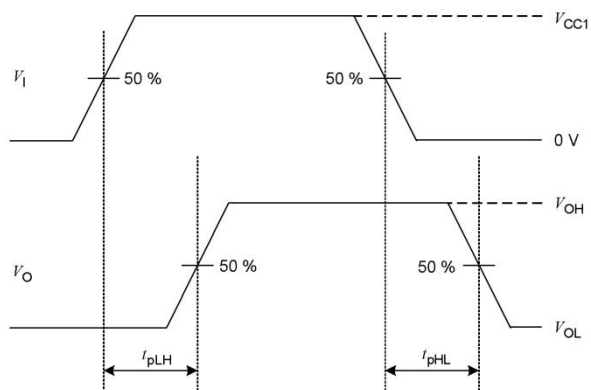


Figure10. Propagation delay time waveform measurement

APPLICATIONS INFORMATION

OVERVIEW

The SCM3721ASA/SCM3723ASA are digital isolators product family based on unique Capacitance pulse isolation technology. Capacitance pulse isolation technology is a new generation digital isolator technology. It uses the principle of capacitor voltage divider to transmit signal directly cross the isolator capacitor without signal modulation and demodulation. Compare to the traditional Opto-couple technology, icoupler technology, OOK technology, Capacitance pulse isolation technology is a more essential and concise isolation signal transmit technology which leads to greatly simplification on circuit design and therefore significantly improves device performance, such as lower power consumption, faster speed, enhanced anti-interference ability, lower noise.

By using matured standard semiconductor CMOS technology and the innovative Capacitance pulse isolation technology design, these isolation components provide outstanding performance characteristics and reliability superior to alternatives such as optocoupler devices and other integrated isolators. The SCM3721ASA/SCM3723ASA isolator data channels are independent and are available in a variety of configurations with a withstand voltage rating of 1.5 kV rms to 3.0 kV rms and the data rate from DC up to 10Mbps (see the Ordering Guide).

The SCM3721ASA/SCM3723ASA are the outstanding 150Kbps dual-channel digital isolators with the enhanced ESD capability. the devices transmit data across an isolation barrier by layers of silicon dioxide isolation.

The devices operate with the supply voltage on either side ranging from 3.0 V to 5.5 V, offering voltage translation of 3.3 V and 5 V logic.

The SCM3721ASA/SCM3723ASA have very low propagation delay and high speed. The input/output design techniques allow logic and supply voltages over a wide range from 3.0 V to 5.5 V, offering voltage translation of 3.3 V and 5 V logic. The architecture is designed for high common-mode transient immunity and high immunity to electrical noise and magnetic interference.

PCB LAYOUT

The low-ESR ceramic bypass capacitors must be connected between V_{DD1} and GND_1 and between V_{DD2} and GND_2 . The bypass capacitors are placed on the PCB as close to the isolator device as possible. The recommended bypass capacitor value is between 0.1 μ F and 10 μ F. To enhance the robustness of a design, the user may also include resistors (50–300 Ω) in series with the inputs and outputs if the system is excessively noisy.

Avoid reducing the isolation capability, Keep the space underneath the isolator device free from metal such as planes, pads, traces and vias.

To minimize the impedance of the signal return loop, keep the solid ground plane directly underneath the high-speed signal path, the closer the better. The return path will couple between the nearest ground plane to the signal path. Keep suitable trace width for controlled impedance transmission lines interconnect.

To reduce the rise time degradation, keep the length of input/output signal traces as short as possible, and route low inductance loop for the signal path and It's return path.

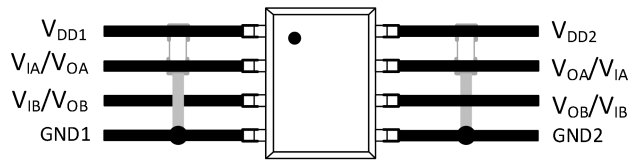


Figure 8. Recommended Printed Circuit Board Layout

CMTI MEASUREMENT

To measure the Common-Mode Transient Immunity (CMTI) of SCM3721ASA/SCM3723ASA isolator under specified common-mode pulse magnitude (V_{CM}) and specified slew rate of the common-mode pulse (dV_{CM}/dt) and other specified test or ambient conditions, The common-mode pulse generator (G_1) will be capable of providing fast rising and falling pulses of specified magnitude and duration of the common-mode pulse (V_{CM}) and the maximum common-mode slew rates (dV_{CM}/dt) can be applied to SCM3721ASA/SCM3723ASA isolator coupler under measurement. The common-mode pulse is applied between one side ground GND1 and the other side ground GND2 of SCM3721ASA/SCM3723ASA isolator and shall be capable of providing positive transients as well as negative transients.

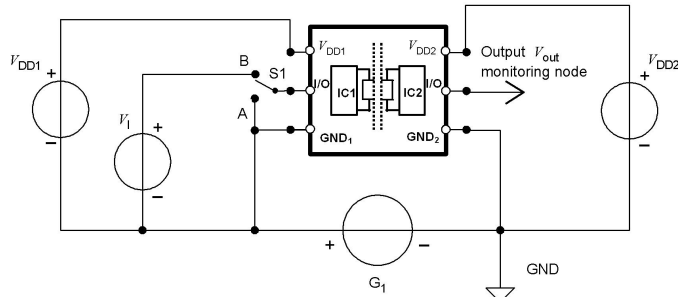


Figure 9. Common-mode transient immunity (CMTI) measurement

Ordering Information

| Part Number | Temperature Range | Withstand Voltage Rating (kV rms) | Package | Number of pins | Product Marking | Tape & Reel |
|-------------|-------------------|-----------------------------------|---------|----------------|------------------|-------------|
| SCM3721ASA | -40°C ~ +125°C | 3 | SOP | 8 | SCM3721ASA YM | 4K/REEL |
| SCM3723ASA | -40°C ~ +125°C | 3 | SOP | 8 | SCM3723ASA YM | 4K/REEL |

Product marking and date code

SCM3401XYZ:

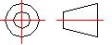
(1) SCM3721 = Product designation.

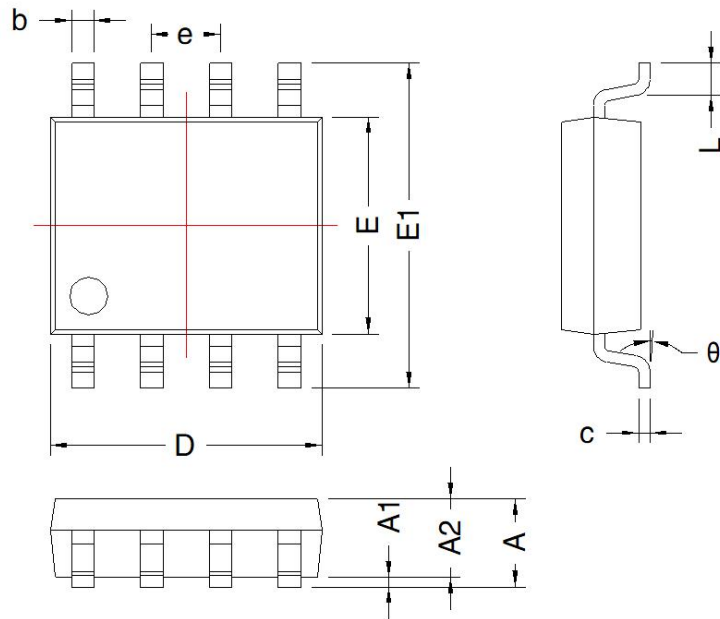
(2) X = Version code information (A-Z).

(3) Y = Packaging definition code; S for SOP package,

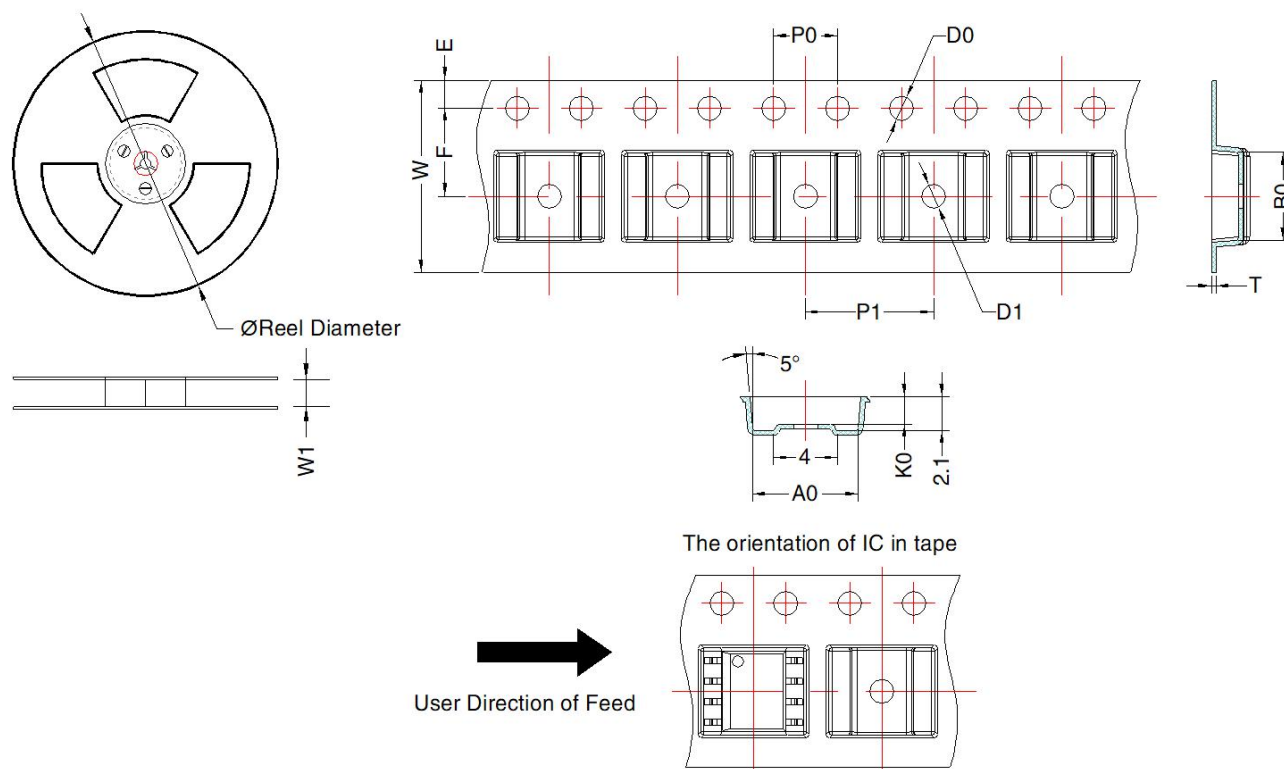
(4) Z = Operating temperature range (C = 0°C to +70°C, I = -40°C to +85°C, A = -40°C to +125°C, M = -55°C to +125°C).

(5) YM = Date code for product traceability; Y = code for production year; M = code for production month.

THIRD ANGLE PROJECTION 



| SOIC-8 | | | | |
|----------|---------------|------|-----------------|-------|
| Mark | Dimension(mm) | | Dimension(inch) | |
| | Min | Max | Min | Max |
| A | 1.35 | 1.75 | 0.053 | 0.069 |
| A1 | 0.1 | 0.25 | 0.004 | 0.010 |
| A2 | 1.3 | 1.5 | 0.005 | 0.059 |
| D | 4.8 | 5.0 | 0.189 | 0.197 |
| E | 3.8 | 4.0 | 0.150 | 0.157 |
| E1 | 5.8 | 6.2 | 0.228 | 0.244 |
| L | 0.45 | 0.8 | 0.018 | 0.031 |
| b | 0.38 | 0.47 | 0.015 | 0.018 |
| e | 1.27TYP | | 0.05TYP | |
| c | 0.17 | 0.25 | 0.007 | 0.001 |
| θ | 0° | 8° | 0° | 8° |



| Device | Package Type | MPQ | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | T (mm) | W (mm) | E (mm) | F (mm) | P1 (mm) | P0 (mm) | D0 (mm) | D1 (mm) |
|------------|--------------|------|--------------------|--------------------|---------|---------|---------|----------|----------|----------|---------|---------|---------|---------|---------|
| SCM3721ASA | SOIC-8 | 4000 | 330.0 | 12.4 | 6.6±0.1 | 5.5±0.1 | 1.7±0.1 | 0.3±0.05 | 12.0±0.3 | 1.75±0.1 | 5.5±0.1 | 8±0.1 | 4±0.1 | 1.5±0.1 | 1.5±0.1 |

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