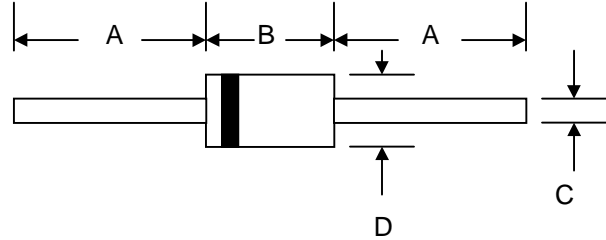


Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Current Capability
- Low Power Loss, High Efficiency
- High Surge Current Capability
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications

3.0A SCHOTTKY BARRIER RECTIFIER



Mechanical Data

- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 1.2 grams (approx.)
- Mounting Position: Any
- Marking: Type Number

DO-201AD		
Dim	Min	Max
A	24.5	—
B	7.20	9.50
C	1.10	1.30
D	5.00	5.60
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	1N5820	1N5821	1N5822	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	20	30	40	V
RMS Reverse Voltage	$V_{R(RMS)}$	14	21	28	V
Average Rectified Output Current (Note 1) @ $T_L = 90^\circ\text{C}$	I_o	3.0			A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) @ $T_L = 75^\circ\text{C}$	I_{FSM}	80			A
Forward Voltage @ $I_F = 3.0\text{A}$	V_{FM}	0.475	0.50	0.525	V
Peak Reverse Current @ $T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage @ $T_A = 100^\circ\text{C}$	I_{RM}	2.0 20			mA
Typical Junction Capacitance (Note 2)	C_j	250			pF
Typical Thermal Resistance Junction to Ambient	$R_{\theta JA}$	20			K/W
Operating and Storage Temperature Range	T_j, T_{STG}	-65 to +150			$^\circ\text{C}$

Note: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

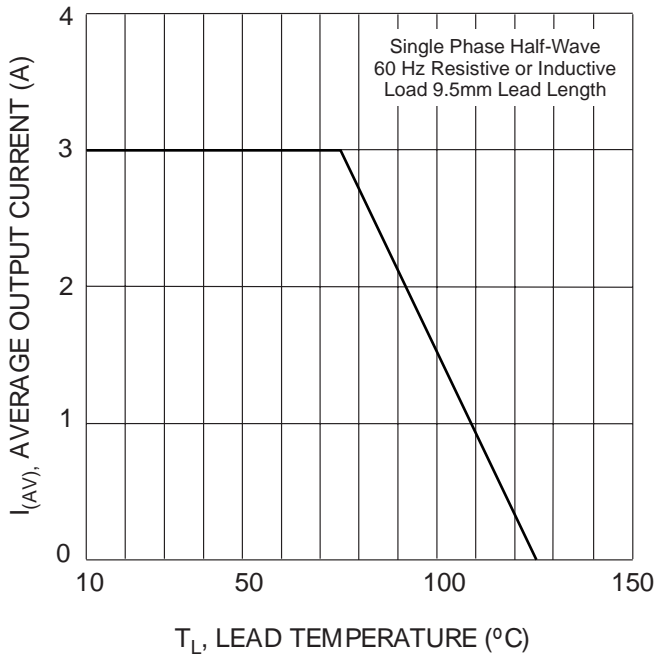


Fig. 1 Forward Current Derating Curve

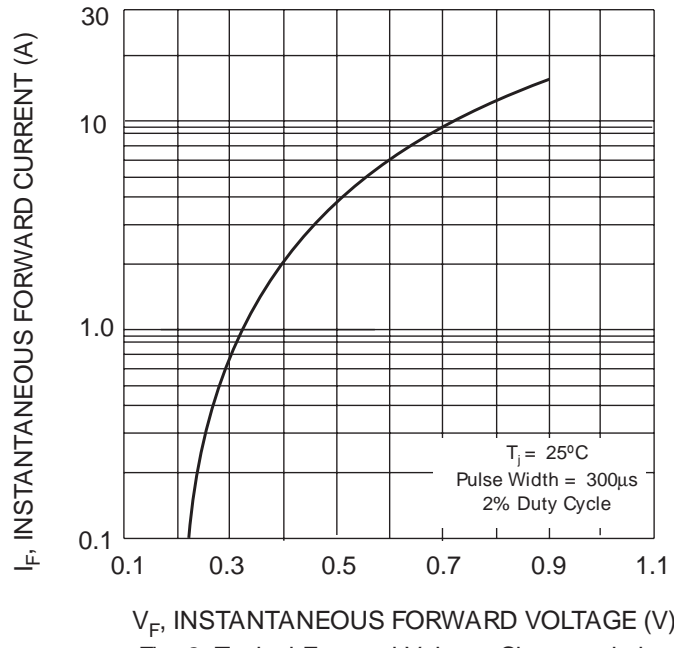


Fig. 2 Typical Forward Voltage Characteristics

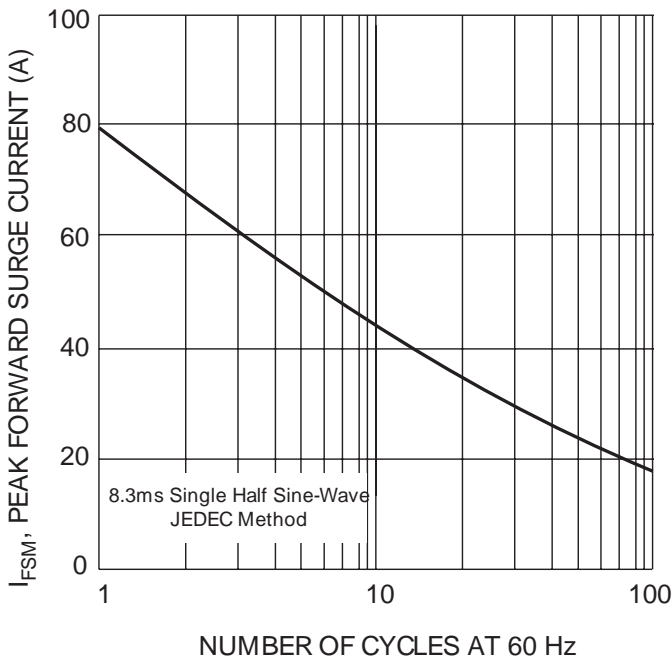


Fig. 3 Peak Forward Surge Current

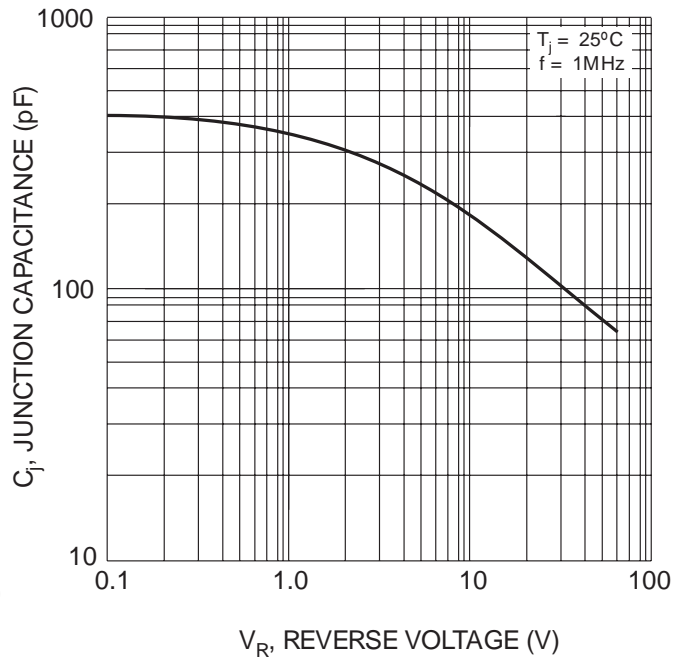


Fig. 4 Typical Junction Capacitance