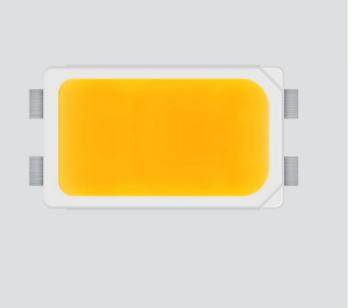
Middle Power LED Series 5630

LM561B Plus



Improved efficacy and performance of LM561B to provide better solution







Features & Benefits

- 0.3 W class middle power LED
- Mold resin for high reliability
- Standard form factor for design flexibility (5.6 × 3.0 mm)



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1. Characteristics

a) Absolute Maximum Rating

ltem	Symbol	Rating	Unit	Condition
Ambient / Operating Temperature	Ta	-40 ~ +85	°C	-
Storage Temperature	T_{stg}	-40 ~ +120	°C	-
LED Junction Temperature	Tj	110	°C	-
Forward Current	l _F	180	mA	-
Peak Pulsed Forward Current	l _{fp}	300	mA	Duty 1/10, pulse width 10 ms
Assembly Process Temperature	_	260 <10	°C s	-
ESD (HBM)	-	±5	kV	-



b) Electro-optical Characteristics (I_F = 65 mA, T_s = 25 °C)

ltem	Unit	CRI (R₃) Min.	Nominal CCT (K)	Rank	Bin	Min.	Тур.	Max.
					AZ	2.7	-	2.8
					A1	2.8	-	2.9
Forward Voltage (V _F)	V			WA	A2	2.9	-	3.0
					A3	3.0	_	3.1
					A4	3.1	-	3.2
			2700		S4	30.0		32.0
					S5	32.0		34.0
			3000		S4	30.5		32.5
					S5	32.5		34.5
			3500		S4	31.0	-	33.0
	lm				S5	33.0	-	35.0
Luminous Flux (Φ _ν)					S4	32.0	-	34.0
, , , , , , , , , , , , , , , , , , ,					S5	34.0	-	36.0
					S4	33.0	-	35.0
				S5	35.0	-	37.0	
			5700		S4	32.5	-	34.5
					S5	34.5	-	36.5
			6500		S4	32.0	-	34.0
					S5	34.0	-	36.0
Reverse Voltage (@ 5 mA)	V					0.7	-	1.2
Color Rendering Index (Ra)	-			5		80	-	-
Special CRI (R9)	-					0	-	-
Thermal Resistance (junction to solder point)	°C/W					-	15	-
Beam Angle	0					-	120	-

Note:

Samsung maintains measurement tolerance of: forward voltage = ± 0.1 V, luminous flux = ± 5 %, CRI = ± 3 , R9 = ± 6.5



2. Product Code Information

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
S	Р	M	w	н	т	5	4	1	М	Р	5	w	K	R	K	S	4

Digit	PKG Information	Code		Specification			
1 2 3	Samsung Package Middle Power	SPM					
4 5	Color	WH	White				
6	Product Version	т					
7 8 9	Form Factor	541	5.6 x 3.0 x 0.7 mm;	4 pads;			
10	Sorting Current (mA)	М	65 mA				
11	Chromaticity Coordinates	Р					
12	CRI	5	Min. 80				
13 14	Forward Voltage (V)	WA WK	2.7~3.2V (2,500 pcs/Reel) 2.7~3.2V (10,000 pcs/Reel)				
15 16	CCT (K)	W☆ V☆ U☆ T☆ R☆ Q☆ P☆	2700 3000 3500 4000 Bin Code: 5000 5700 6500 ☆: Warm white:	W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG V1, V2, V3, V4, V5, V6, V7, V8, V9, VA, VB, VC, VD, VE, VF, VG U1, U2, U3, U4, U5, U6, U7, U8, U9, UA, UB, UC, UD, UE, UF, UG T1, T2, T3, T4, T5, T6, T7, T8, T9, TA, TB, TC, TD, TE, TF, TG R1, R2, R3, R4, R5, R6, R7, R8, R9, RA, RB, RC, RD, RE, RF, RG Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9, QA, QB, QC, QD, QE, QF, QG P1, P2, P3, P4, P5, P6, P7, P8, P9, PA, PB, PC, PD, PE, PF, PG "0" (Whole bin) "M" (Quarter bin) "N"(Quarter cross kitting) "R"(Outer bin) or "K" (Kitting bin)			
17 18	Luminous Flux	S4 S5	Bin Code:	\$4, \$5			



a) Luminous Flux Bins ($I_F = 65$ mA, $T_s = 25$ °C)

CRI (R₃) Min.	Nominal CCT (K)	Product Code	Flux Bin	Flux Range (Φ _v , lm)
	2700	SPMWHT541MP5W ◆ W☆S4	S4	30.0 ~ 32.0
	2700	SPMWHT541MP5W ♦ W☆S5	S5	32.0 ~ 34.0
	3000	SPMWHT541MP5W ◆ V☆S4	S4	30.5 ~ 32.5
	3000	SPMWHT541MP5W ◆ V☆S5	S5	32.5 ~ 34.5
	0500	SPMWHT541MP5W ♦ U☆S4	S4	31.0 ~ 33.0
	3500	SPMWHT541MP5W ♦ U☆S5	S 5	33.0 ~ 35.0
80	4000	SPMWHT541MP5W ∳ T☆S4	S4	32.0 ~34.0
60	4000	SPMWHT541MP5W ∳ T☆S5	S 5	34.0 ~36.0
	5000	SPMWHT541MP5W ◆ R☆S4	S4	33.0 ~35.0
	5000	SPMWHT541MP5W ◆ R☆S5	S 5	35.0 ~37.0
	F700	SPMWHT541MP5W ◆ Q☆S4	S4	32.5 ~34.5
	5700	SPMWHT541MP5W ◆ Q☆S5	S5	34.5 ~36.5
	6500	SPMWHT541MP5W ◆ P☆S4	S4	32.0 ~34.0
	6500	SPMWHT541MP5W ◆ P☆S5	S5	34.0 ~36.0

Note:



[&]quot;lack" can be "A" (2,500pcs) or "K" (10,000pcs) of reel taping

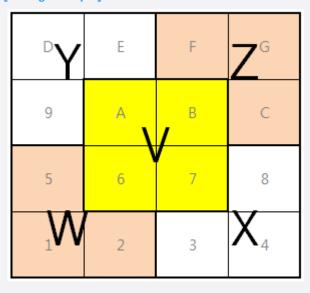
[&]quot;☆" can be "0" (Whole bin), "M" (Quarter bin), "N"(Quarter cross kitting bin), "R"(Outer bin) or "K" (Kitting bin) of the color binning

b) Kitting rule

1) Kitting bin Concept

- 1. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin (VF, Color, Im).
- 2. A forward voltage (VF) of kitting bin is combined by a pair of same VF rank such as (A1+A1), (A2+A2), (A3+A3), (A4+A4) or (AZ+AZ).
- 3. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation)

[Kitting example]



[Binning Information]

	Bin #1	Bin #2
	AZ	AZ
	A1	A1
VF	A2	A2
	A3	A3
	A4	A4
	W (1, 2, 5 bin)	Z (C, F, G bin)
CIE	V (6, 7, A, B bin)	V (6, 7, A, B bin)
	X (3, 4, 8 bin)	Y (9, D, E bin)
13.7	S4	S4
IV	S 5	S 5

※ Each of V,W,X,Y and Z can be one bin without details division.



b) Kitting rule (Quarter cross kitting)

2) Quarter cross kitting bin Concept

- 1. Under agreement between customer and SAMSUNG ELECTRONICS, SAMSUNG can supply kitting bin (VF, Color, Im).
- 2. A forward voltage (VF) of kitting bin is combined by a pair of same VF rank such as (A1+A1), (A2+A2), (A3+A3), (A4+A4) or (AZ+AZ).
- 3. A Chromaticity Coordinates of kitting bin is mixed by kitting procedure.(below kitting simulation)

4.

[Kitting example]

D	E	F	G
9	А	В	С
5	6	7	8
1	2	3	4

[Binning Information]

	Bin #1	Bin #2
	AZ	AZ
	A1	A1
VF	A2	A2
	A3	A3
	A4	A4
OIE	6 bin	B bin
CIE	7 bin	A bin
N /	S4	S4
IV	S5	S5



c) Color Bins (I_F = 65 mA, T_s = 25 °C)

CRI (R₃) Min.	Nominal CCT (K)	Product Code	Color Rank	Chromaticity Bins	
		SPMWHT541MP5W ♦ W0S☆	W0 (Whole bin)	W1, W2, W3, W4, W5, W6, W7, W8, W9, WA, WB, WC, WD, WE, WF, WG	
		SPMWHT541MP5W ◆ WMS☆	WM (Quarter bin)	W6, W7, WA, WB	
	2700	SPMWHT541MP5W ◆ WKS☆	WK (Kitting bin)	WV, WW, WX, WY, WZ	
		SPMWHT541MP5W♦WNS☆	WN (Quarter cross kitting bin)	W6, W7, WA, WB	
		SPMWHT541MP5W◆W0S☆	WR (Outer bin)	W1, W2, W3, W4, W5, W8, W9, WC, WD, WE, WF, WG	
		SPMWHT541MP5W ♦ V0S☆	V0 (Whole bin)	V1, V2, V3, V4, V5, V6, V7, V8, V9, VA, VB, VC, VD, VE, VF, VG	
		SPMWHT541MP5W ◆ VMS☆	VM (Quarter bin)	V6, V7, VA, VB	
	3000	SPMWHT541MP5W ◆ VKS☆	VK (Kitting bin)	VV, VW, VX, VY, VZ	
		SPMWHT541MP5W◆VNS☆	VN (Quarter cross kitting bin)	V6, V7, VA, VB	
		SPMWHT541MP5W ◆ V0S☆	VR (Outer bin)	V1, V2, V3, V4, V5, V8, V9, VC, VD, VE, VF, VG	
			SPMWHT541MP5W ♦ U0S☆	U0 (Whole bin)	U1, U2, U3, U4, U5, U6, U7, U8, U9, UA, UB, UC, UD, UE, UF, UG
80		SPMWHT541MP5W◆UMS☆	UM (Quarter bin)	U6, U7, UA, UB	
80	3500	SPMWHT541MP5W ♦ UKS☆	UK (Kitting bin)	UV, UW, UX, UY, UZ	
	<u></u>	SPMWHT541MP5W◆UNS☆	UN (Quarter cross kitting bin)	U6, U7, UA, UB	
		SPMWHT541MP5W ♦ U0S☆	UR (Outer bin)	U1, U2, U3, U4, U5, U8, U9, UC, UD, UE, UF, UG	
		SPMWHT541MP5W ◆ T0S☆	T0 (Whole bin)	T1, T2, T3, T4, T5, T6, T7, T8, T9, TA, TB, TC, TD, TE, TF, TG	
		SPMWHT541MP5W ◆ TMS☆	TM (Quarter bin)	T6, T7, TA, TB	
	4000	SPMWHT541MP5W ♦ TKS☆	TK (Kitting bin)	TV, TW, TX, TY, TZ	
		SPMWHT541MP5W ◆ TNS☆	TN (Quarter cross kitting bin)	T6, T7, TA, TB	
		SPMWHT541MP5W ◆ T0S☆	TR (Outer bin)	T1, T2, T3, T4, T5, T8, T9, TC, TD, TE, TF, TG	
		SPMWHT541MP5W ◆ R0S☆	R0 (Whole bin)	R1, R2, R3, R4, R5, R6, R7, R8, R9, RA, RB, RC, RD, RE, RF, RG	
	5000	SPMWHT541MP5W◆RMS☆	RM (Quarter bin)	R6, R7, RA, RB	
	5000	SPMWHT541MP5W◆RKS☆	RK (Kitting bin)	RV, RW, RX, RY, RZ	
		SPMWHT541MP5W◆RNS☆	RN (Quarter cross kitting bin)	R6, R7, RA, RB	



	SPMWHT541MP5W◆R0S☆	RR (Outer bin)	R1, R2, R3, R4, R5, R8, R9, RC, RD, RE, RF, RG
	SPMWHT541MP5W ◆ Q0S☆	Q0 (Whole bin)	Q1, Q2, Q3, Q4, Q5, Q6, Q7, Q8, Q9, QA, QB, QC, QD, QE, QF, QC
	SPMWHT541MP5W ◆ QMS☆	QM (Quarter bin)	Q6, Q7, QA, QB
5700	SPMWHT541MP5W ◆ QKS☆	QK (Kitting bin)	QV, QW, QX, QY, QZ
	SPMWHT541MP5W ◆ QNS☆	QN (Quarter cross kitting bin)	Q6, Q7, QA, QB
	SPMWHT541MP5W ◆ Q0S☆	QR (Outer bin)	Q1, Q2, Q3, Q4, Q5, Q8, Q9, QC, QD, QE, QF, QG
	SPMWHT541MP5W ◆ P0S☆	P0 (Whole bin)	P1, P2, P3, P4, P5, P6, P7, P8, P9, PA, PB, PC, PD, PE, PF, PG
	SPMWHT541MP5W◆PMS☆	PM (Quarter bin)	P6, P7, PA, PB
6500	SPMWHT541MP5W◆PKS☆	PK (Kitting bin)	PV, PW, PX, PY, PZ
	SPMWHT541MP5W ◆ PNS☆	PN (Quarter cross kitting bin)	P6, P7, PA, PB
***	SPMWHT541MP5W ◆ P0S☆	PR (Outer bin)	P1, P2, P3, P4, P5, P8, P9, PC, PD, PE, PF, PG

Note:



[&]quot; \blacklozenge " can be "A" (2,500pcs) or "K" (10,000pcs) of reel taping

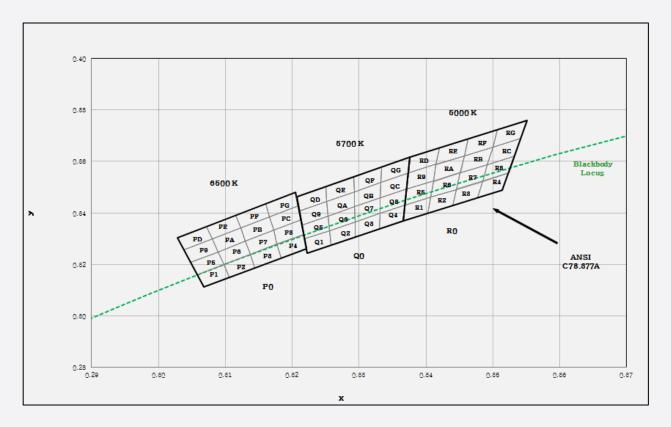
[&]quot; \updownarrow " can be "S4" or "S5" of luminous flux bin

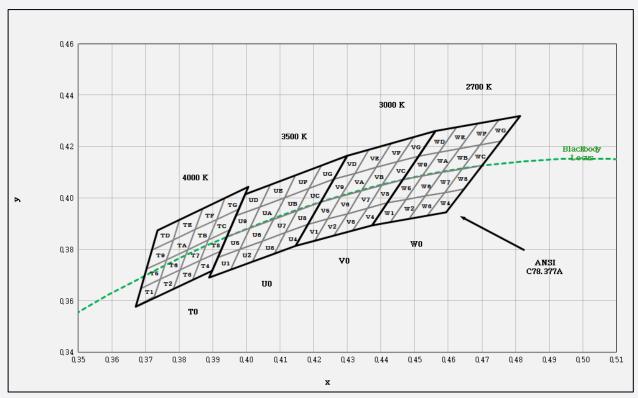
d) Voltage Bins (I_F = 65 mA, T_s = 25 °C)

CRI (R _a) Min.	Nominal CCT (K)	Product Code	Voltage Rank	Voltage Bin	Voltage Range (V)
				AZ	2.7 ~ 2.8
				A1	2.8 ~ 2.9
-	-	-	WA (WK)	A2	2.9 ~ 3.0
				A3	3.0 ~ 3.1
				A4	3.1 ~ 3.2



e) Chromaticity Region & Coordinates (IF = 65 mA, T_s = 25 °C)







e) Chromaticity Region & Coordinates (IF = 65 mA, T_s = 25 °C)

Region	CIEx	CIE y	Region	CIE x	CIE y
		W rank	(2700 K)		
	0.4373	0.3893		0.4465	0.4071
10/4	0.4418	0.3981	1440	0.4513	0.4164
W1	0.4475	0.3994	W9	0.4573	0.4178
	0.4428	0.3906		0.4523	0.4085
	0.4428	0.3906		0.4523	0.4085
W2	0.4475	0.3994	WA	0.4573	0.4178
VVZ	0.4532	0.4008	VVA	0.4634	0.4193
	0.4483	0.3919		0.4582	0.4099
	0.4483	0.3919		0.4582	0.4099
W3	0.4532	0.4008	WB	0.4634	0.4193
VV3	0.4589	0.4021	VVD	0.4695	0.4207
	0.4538	0.3931		0.4641	0.4112
	0.4538	0.3931		0.4641	0.4112
10/4	0.4589	0.4021	W/O	0.4695	0.4207
W4	0.4646	0.4034	WC	0.4756	0.4221
	0.4593	0.3944		0.4700	0.4126
	0.4418	0.3981		0.4513	0.4164
14/5	0.4465	0.4071	145	0.4562	0.4260
W5	0.4523	0.4085	WD	0.4624	0.4274
	0.4475	0.3994		0.4573	0.4178
	0.4475	0.3994		0.4573	0.4178
14/0	0.4523	0.4085		0.4624	0.4274
W6	0.4582	0.4099	WE	0.4687	0.4289
	0.4532	0.4008		0.4634	0.4193
	0.4532	0.4008		0.4634	0.4193
\A/=	0.4582	0.4099	14.5	0.4687	0.4289
W7	0.4641	0.4112	WF	0.4750	0.4304
	0.4589	0.4021		0.4695	0.4207
	0.4589	0.4021		0.4695	0.4207
14/0	0.4641	0.4112	14/0	0.4750	0.4304
W8	0.4700	0.4126	WG	0.4813	0.4319
	0.4646	0.4034		0.4756	0.4221

Region	CIEx	CIE y	Region	CIEx	CIE y		
V rank (3000 K)							
	0.4147	0.3814		0.4221	0.3984		
V1	0.4183	0.3898	V/0	0.4259	0.4073		
VI	0.4242	0.3919	V9	0.4322	0.4096		
	0.4203	0.3833		0.4281	0.4006		
	0.4203	0.3833		0.4281	0.4006		
V2	0.4242	0.3919	VA	0.4322	0.4096		
V2	0.4300	0.3939	VA	0.4385	0.4119		
	0.4259	0.3853		0.4342	0.4028		
	0.4259	0.3853		0.4342	0.4028		
Va	0.4300	0.3939	VD	0.4385	0.4119		
V3	0.4359	0.3960	VB	0.4449	0.4141		
	0.4316	0.3873		0.4403	0.4049		
	0.4316	0.3873		0.4403	0.4049		
V4	0.4359	0.3960	VO	0.4449	0.4141		
V4	0.4418	0.3981	VC	0.4513	0.4164		
	0.4373	0.3893		0.4465	0.4071		
	0.4183	0.3898		0.4259	0.4073		
VE	0.4221	0.3984	VD	0.4299	0.4165		
V5	0.4281	0.4006	VD	0.4364	0.4188		
	0.4242	0.3919		0.4322	0.4096		
	0.4242	0.3919		0.4322	0.4096		
Ve	0.4281	0.4006	VE	0.4364	0.4188		
V6	0.4342	0.4028	VE	0.4430	0.4212		
	0.4300	0.3939		0.4385	0.4119		
	0.4300	0.3939		0.4385	0.4119		
\/7	0.4342	0.4028	VF	0.4430	0.4212		
V7	0.4403	0.4049	VF	0.4496	0.4236		
	0.4359	0.3960		0.4449	0.4141		
	0.4359	0.3960		0.4449	0.4141		
\/0	0.4403	0.4049		0.4496	0.4236		
V8	0.4465	0.4071	VG	0.4562	0.4260		
	0.4418	0.3981		0.4513	0.4164		



e) Chromaticity Region & Coordinates

Region	CIE x	CIE y	Region	CIE x	CIE y
		U rank	(3500 K)		
	0.3889	0.3690		0.3941	0.3848
1.14	0.3915	0.3768	LIO	0.3968	0.3930
U1	0.3981	0.3800	U9	0.4040	0.3966
	0.3953	0.3720		0.4010	0.3882
	0.3953	0.3720		0.4010	0.3882
110	0.3981	0.3800	110	0.4040	0.3966
U2	0.4048	0.3832	UA	0.4113	0.4001
	0.4017	0.3751		0.4080	0.3916
	0.4017	0.3751		0.4080	0.3916
110	0.4048	0.3832		0.4113	0.4001
U3	0.4116	0.3865	UB	0.4186	0.4037
	0.4082	0.3782		0.4150	0.3950
	0.4082	0.3782		0.4150	0.3950
114	0.4116	0.3865	110	0.4186	0.4037
U4	0.4183	0.3898	UC	0.4259	0.4073
	0.4147	0.3814		0.4221	0.3984
	0.3915	0.3768		0.3968	0.3930
	0.3941	0.3848		0.3996	0.4015
U5	0.4010	0.3882	UD	0.4071	0.4052
	0.3981	0.3800		0.4040	0.3966
	0.3981	0.3800		0.4040	0.3966
110	0.4010	0.3882		0.4071	0.4052
U6	0.4080	0.3916	UE	0.4146	0.4089
	0.4048	0.3832		0.4113	0.4001
	0.4048	0.3832		0.4113	0.4001
117	0.4080	0.3916		0.4146	0.4089
U7	0.4150	0.3950	UF	0.4222	0.4127
	0.4116	0.3865		0.4186	0.4037
	0.4116	0.3865		0.4186	0.4037
	0.4150	0.3950		0.4222	0.4127
U8	0.4221	0.3984	UG	0.4299	0.4165
	0.4183	0.3898		0.4259	0.4073

Region	CIEx	CIE y	Region	CIE x	CIE y
		T rank	(4000 K)		
	0.3670	0.3578		0.3702	0.3722
	0.3726	0.3612	T0	0.3763	0.3760
T1	0.3744	0.3685	T9	0.3782	0.3837
	0.3686	0.3649		0.3719	0.3797
	0.3726	0.3612		0.3763	0.3760
Т2	0.3783	0.3646	ΤΛ	0.3825	0.3798
T2	0.3804	0.3721	TA	0.3847	0.3877
	0.3744	0.3685		0.3782	0.3837
	0.3783	0.3646		0.3825	0.3798
Т2	0.3840	0.3681	TD	0.3887	0.3836
T3	0.3863	0.3758	ТВ	0.3912	0.3917
	0.3804	0.3721		0.3847	0.3877
	0.3840	0.3681		0.3887	0.3836
	0.3898	0.3716	TC	0.3950	0.3875
T4	0.3924	0.3794		0.3978	0.3958
	0.3863	0.3758		0.3912	0.3917
	0.3686	0.3649		0.3719	0.3797
T-F	0.3744	0.3685	TD	0.3782	0.3837
T5	0.3763	0.3760	TD	0.3802	0.3916
	0.3702	0.3722		0.3736	0.3874
	0.3744	0.3685		0.3782	0.3837
Te	0.3804	0.3721	T F	0.3847	0.3877
T6	0.3825	0.3798	TE	0.3869	0.3958
	0.3763	0.3760		0.3802	0.3916
	0.3804	0.3721		0.3847	0.3877
T7	0.3863	0.3758	TF	0.3912	0.3917
17	0.3887	0.3836	ΙF	0.3937	0.4001
	0.3825	0.3798		0.3869	0.3958
	0.3863	0.3758		0.3912	0.3917
To	0.3924	0.3794	TO	0.3978	0.3958
Т8	0.3950	0.3875	TG	0.4006	0.4044
	0.3887	0.3836		0.3937	0.4001



e) Chromaticity Region & Coordinates

Region	CIEx	CIE y	Region	CIE x	CIE y
		R rank	(5000 K)		
	0.3366	0.3369		0.3374	0.3554
	0.3369	0.3431	-	0.3371	0.3493
R1	0.3407	0.3460	R9	0.3411	0.3522
	0.3403	0.3398		0.3415	0.3587
	0.3403	0.3398		0.3415	0.3587
Do	0.3407	0.3460		0.3411	0.3522
R2	0.3446	0.3491	RA	0.3451	0.3554
	0.3440	0.3427		0.3457	0.3621
	0.3446	0.3491		0.3451	0.3554
Do	0.3440	0.3427	55	0.3457	0.3621
R3	0.3477	0.3458	RB	0.3500	0.3655
	0.3485	0.3522		0.3492	0.3587
	0.3485	0.3522		0.3492	0.3587
D4	0.3477	0.3458	D0	0.3500	0.3655
R4	0.3514	0.3487	RC	0.3542	0.3690
	0.3524	0.3554		0.3533	0.3620
	0.3371	0.3493		0.3376	0.3616
DE	0.3369	0.3431	DD	0.3374	0.3554
R5	0.3407	0.3460	RD	0.3415	0.3587
	0.3411	0.3522		0.3420	0.3652
	0.3407	0.3460		0.3415	0.3587
DC	0.3411	0.3522	DE	0.3420	0.3652
R6	0.3451	0.3554	RE	0.3463	0.3687
	0.3446	0.3491		0.3457	0.3621
	0.3446	0.3491		0.3457	0.3621
D7	0.3451	0.3554	PГ	0.3463	0.3687
R7	0.3492	0.3587	RF	0.3507	0.3724
	0.3485	0.3522		0.3500	0.3655
	0.3485	0.3522		0.3500	0.3655
Do	0.3492	0.3587	DC.	0.3507	0.3724
R8	0.3533	0.3620	RG	0.3551	0.3760
	0.3524	0.3554		0.3542	0.3690

Region	CIEx	CIE y	Region	CIEx	CIE y
		Q rank	(5700 K)		
	0.3218	0.3298		0.3211	0.3407
04	0.3222	0.3243	00	0.3215	0.3353
Q1	0.3258	0.3275	Q9	0.3254	0.3388
	0.3256	0.3331		0.3252	0.3444
	0.3256	0.3331		0.3252	0.3444
02	0.3258	0.3275	0.4	0.3254	0.3388
Q2	0.3294	0.3306	QA	0.3293	0.3423
	0.3294	0.3364		0.3293	0.3481
	0.3294	0.3364		0.3293	0.3481
00	0.3294	0.3306	0.0	0.3293	0.3423
Q3	0.3330	0.3338	QB	0.3332	0.3458
	0.3331	0.3398		0.3333	0.3518
	0.3331	0.3398		0.3333	0.3518
04	0.3330	0.3338	00	0.3332	0.3458
Q4	0.3366	0.3369		0.3371	0.3493
	0.3369	0.3431		0.3374	0.3554
	0.3215	0.3353		0.3207	0.3462
05	0.3218	0.3298	0.0	0.3211	0.3407
Q5	0.3256	0.3331	QD	0.3252	0.3444
	0.3254	0.3388	Q9 QA QA QD QD	0.3250	0.3501
	0.3254	0.3388		0.3250	0.3501
00	0.3256	0.3331		0.3252	0.3444
Q6	0.3294	0.3364	QE	0.3293	0.3481
	0.3293	0.3423		0.3292	0.3539
	0.3293	0.3423		0.3292	0.3539
07	0.3294	0.3364	0.5	0.3293	0.3481
Q7	0.3331	0.3398	QF	0.3333	0.3518
	0.3332	0.3458		0.3334	0.3578
	0.3332	0.3458		0.3334	0.3578
0-	0.3331	0.3398		0.3333	0.3518
Q8	0.3369	0.3431	QG	0.3374	0.3554
	0.3371	0.3493		0.3376	0.3616



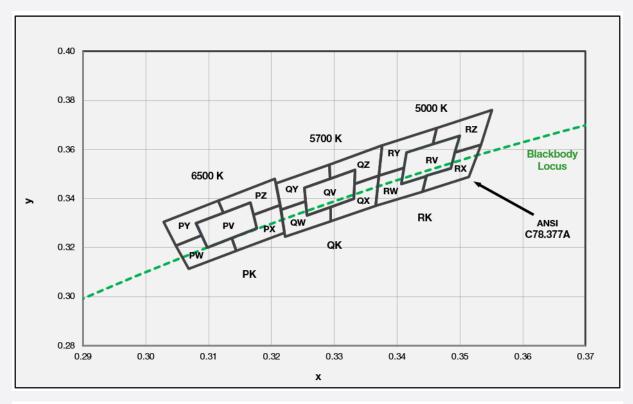
e) Chromaticity Region & Coordinates

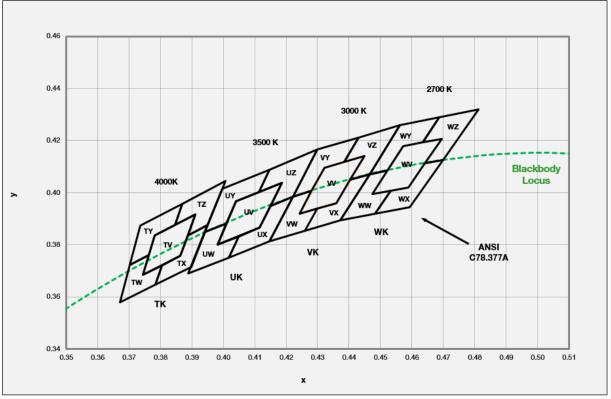
Region	CIE x	CIE y	Region	CIE x	CIE y
		P rank	(6500 K)		
	0.3068	0.3113		0.3048	0.3207
D4	0.3106	0.3150	DO	0.3089	0.3249
P1	0.3098	0.3199	P9	0.3080	0.3298
	0.3058	0.3160		0.3048	0.3256
	0.3106	0.3150		0.3089	0.3249
DO	0.3144	0.3186	DA	0.3130	0.3290
P2	0.3137	0.3238	PA	0.3123	0.3341
	0.3098	0.3199		0.3080	0.3298
	0.3144	0.3186		0.3130	0.3290
Do	0.3183	0.3224	55	0.3172	0.3332
P3	0.3177	0.3278	РВ	0.3166	0.3384
	0.3137	0.3238	- PB	0.3123	0.3341
	0.3183	0.3224		0.3172	0.3332
	0.3221	0.3261	50	0.3214	0.3373
P4	0.3218	0.3317	PC	0.3210	0.3427
	0.3177	0.3278	PC	0.3166	0.3384
	0.3058	0.3160		0.3038	0.3256
5-5	0.3098	0.3199	55	0.3080	0.3298
P5	0.3089	0.3249	PD	0.3072	0.3348
	0.3048	0.3207	PC PC	0.3028	0.3304
	0.3098	0.3199		0.3080	0.3298
Do	0.3137	0.3238	5-	0.3123	0.3341
P6	0.3130	0.3290	PE	0.3115	0.3391
	0.3089	0.3249	PC	0.3072	0.3348
	0.3137	0.3238		0.3123	0.3341
D7	0.3177	0.3278	DE	0.3166	0.3384
P7	0.3172	0.3332	PF	0.3160	0.3436
	0.3130	0.3290		0.3115	0.3391
	0.3177	0.3278		0.3166	0.3384
P8	0.3218	0.3317	DC.	0.3210	0.3427
P8	0.3214	0.3373	PG	0.3206	0.3481
	0.3172	0.3332		0.3160	0.3436

Samsung maintains measurement tolerance of: Cx, $Cy = \pm 0.005$



f) Kitting Chromaticity Region & Coordinates (IF = 65 mA, $T_{\rm s}$ = 25 °C)







f) Kitting Chromaticity Region & Coordinates (I_F = 65 mA, T_s = 25 °C)

Region	CIEx	CIE y	Region	CIE x	CIE y
		W rank	(2700 K)		
	0.4475	0.3994			
WV	0.4589	0.4021			
VVV	0.4695	0.4207			
	0.4573	0.4178			
	0.4373	0.3893		0.4465	0.4071
	0.4483	0.3919		0.4523	0.4085
WW	0.4532	0.4008	WY	0.4573	0.4178
VVVV	0.4475	0.3994	VVT	0.4634	0.4193
	0.4523	0.4085		0.4687	0.4289
	0.4465	0.4071		0.4562	0.4260
	0.4483	0.3919		0.4641	0.4112
	0.4593	0.3944		0.4700	0.4126
WX	0.4700	0.4126	WZ	0.4813	0.4319
VVA	0.4641	0.4112	VVZ	0.4687	0.4289
	0.4589	0.4021		0.4634	0.4193
	0.4532	0.4008		0.4695	0.4207

Region	CIEx	CIE y	Region	CIE x	CIE y
		V rank	(3000 K)		
	0.4242	0.3919			
10/	0.4359	0.3960			
W	0.4449	0.4141			
	0.4322	0.4096			
	0.4147	0.3814		0.4221	0.3984
	0.4259	0.3853		0.4281	0.4006
VW	0.4300	0.3939	VY	0.4322	0.4096
VVV	0.4242	0.3919	VY	0.4385	0.4119
	0.4281	0.4006		0.4430	0.4212
	0.4221	0.3984		0.4299	0.4165
	0.4259	0.3853		0.4403	0.4049
	0.4373	0.3893		0.4465	0.4071
VX	0.4465	0.4071	VZ	0.4562	0.4260
VX	0.4403	0.4049	VZ	0.4430	0.4212
	0.4359	0.3960		0.4385	0.4119
	0.4300	0.3939		0.4449	0.4141



f) Kitting Chromaticity Region & Coordinates

Region	CIEx	CIE y	Region	CIE x	CIE y
		U rank	(3500 K)		
	0.3981	0.3800			
UV	0.4116	0.3865			
UV	0.4186	0.4037			
	0.4040	0.3966			
	0.3889	0.3690		0.3941	0.3848
	0.4017	0.3751		0.4010	0.3882
UW	0.4048	0.3832		0.4040	0.3966
UVV	0.3981	0.3800	UY	0.4113	0.4001
	0.4010	0.3882		0.4146	0.4089
	0.3941	0.3848		0.3996	0.4015
	0.4017	0.3751		0.4150	0.3950
	0.4147	0.3814		0.4221	0.3984
UX	0.4221	0.3984	UZ	0.4299	0.4165
UA	0.4150	0.3950	UZ	0.4146	0.4089
	0.4116	0.3865		0.4113	0.4001
	0.4048	0.3832		0.4186	0.4037

Region	CIEx	CIEy	Region	CIEx	CIE y
		T rank	(4000 K)		
	0.3744	0.3685			
TV	0.3863	0.3758	-		
IV	0.3912	0.3917			
	0.3782	0.3837			
	0.3670	0.3578		0.3702	0.3722
	0.3783	0.3646	-	0.3763	0.3760
TW	0.3804	0.3721	TY	0.3782	0.3837
IVV	0.3744	0.3685	T I I	0.3847	0.3877
	0.3763	0.3760		0.3869	0.3958
	0.3702	0.3722		0.3736	0.3874
	0.3783	0.3646		0.3887	0.3837
	0.3898	0.3716		0.3950	0.3875
TX	0.3950	0.3875	TZ	0.4006	0.4044
IA	0.3887	0.3837	14	0.3869	0.3958
	0.3863	0.3758		0.3847	0.3877
	0.3804	0.3721		0.3912	0.3917



f) Kitting Chromaticity Region & Coordinates

Region	CIEx	CIE y	Region	CIE x	CIE y				
	R rank (5000 K)								
	0.3407	0.3460							
RV	0.3485	0.3524							
HV	0.3500	0.3655							
	0.3415	0.3588							
	0.3366	0.3369		0.3371	0.3493				
	0.3440	0.3427		0.3411	0.3525				
RW	0.3446	0.3491	DV	0.3415	0.3588				
HVV	0.3407	0.3460	RY	0.3457	0.3621				
	0.3411	0.3525		0.3463	0.3687				
	0.3371	0.3493		0.3376	0.3616				
	0.3440	0.3428		0.3457	0.3621				
	0.3514	0.3487		0.3500	0.3655				
DV	0.3533	0.3620	D.7	0.3492	0.3587				
RX	0.3492	0.3587	RZ	0.3533	0.3620				
	0.3485	0.3522		0.3551	0.3760				
	0.3446	0.3493		0.3463	0.3687				

Region	CIEx	CIE y	Region	CIE x	CIE y
	Q rank (5700 K)				
	0.3256	0.3331			
QV	0.3331	0.3398			
QV	0.3333	0.3518			
	0.3252	0.3444			
	0.3222	0.3243		0.3215	0.3353
	0.3294	0.3306	QY	0.3254	0.3388
QW	0.3294	0.3364		0.3252	0.3444
QVV	0.3256	0.3331		0.3293	0.3481
	0.3254	0.3388		0.3292	0.3539
	0.3215	0.3353		0.3207	0.3462
	0.3294	0.3306		0.3293	0.3481
	0.3366	0.3369		0.3333	0.3518
ΩX	0.3371	0.3493		0.3332	0.3458
ŲΧ	0.3332	0.3458		0.3371	0.3493
	0.3331	0.3398		0.3376	0.3616
	0.3294	0.3364		0.3292	0.3539



f) Kitting Chromaticity Region & Coordinates

Region	CIE x	CIE y	Region	CIE x	CIE y
		P rank	(6500 K)	•	
	0.3098	0.3199			
PV	0.3177	0.3278			
PV	0.3166	0.3384			
	0.3080	0.3298			
	0.3068	3 0.3113		0.3048	0.3207
	0.3144	0.3186	PY	0.3089	0.3249
PW	0.3137	0.3238		0.308	0.3298
PVV	0.3098	0.3199		0.3123	0.3341
	0.3089	0.3249		0.3115	0.3391
	0.3048	0.3207		0.3028	0.3304
	0.3144	0.3186	PZ	0.3123	0.3341
	0.3221	0.3261		0.3166	0.3384
PX	0.3213	0.3373		0.3172	0.3332
	0.3172	0.3332		0.3213	0.3373
	0.3177	0.3278		0.3205	0.3481
	0.3137	0.3238		0.3115	0.3391

Note:

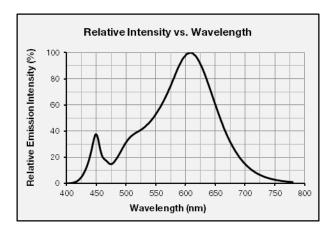
Samsung maintains measurement tolerance of: Cx, $Cy = \pm 0.005$



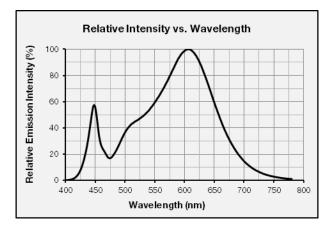
3. Typical Characteristics Graphs

a) Spectrum Distribution (I_F = 65 mA, T_s = 25 °C)

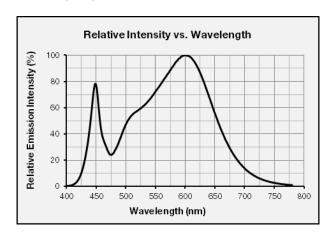
CCT: 2700 K (80 CRI)



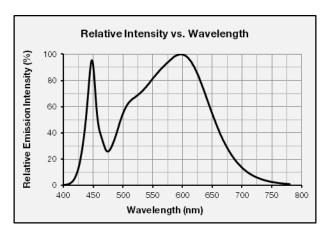
CCT: 3000 K (80 CRI)



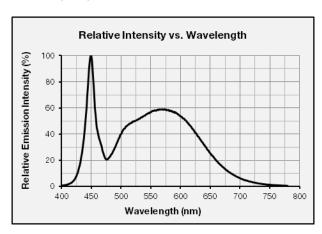
CCT: 3500 K (80 CRI)



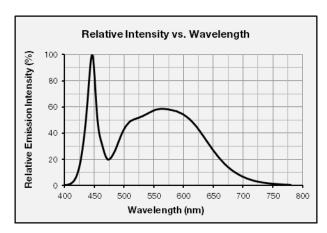
CCT: 4000 K (80 CRI)



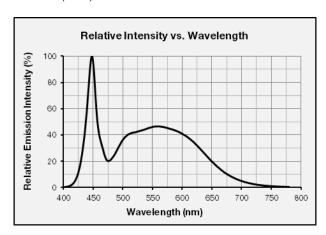
CCT: 5000 K (80 CRI)



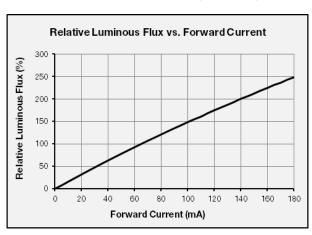
CCT: 5700 K (80 CRI)

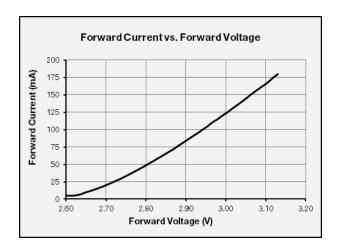


CCT: 6500 K (80 CRI)

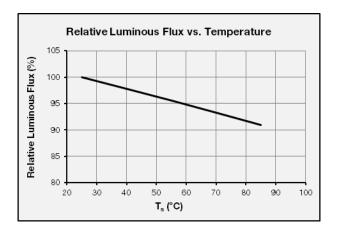


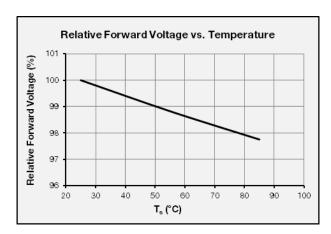
b) Forward Current Characteristics (T_s = 25 °C)





c) Temperature Characteristics (I_F = 65 mA)

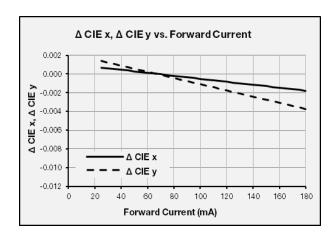


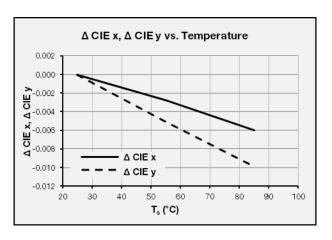


d) Color Shift Characteristics

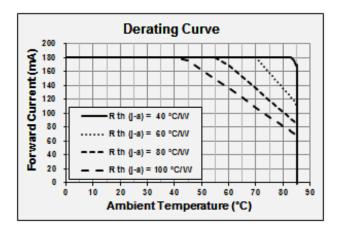




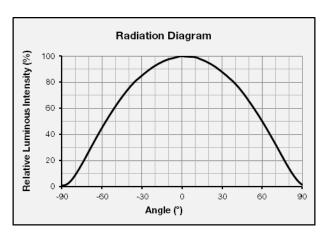




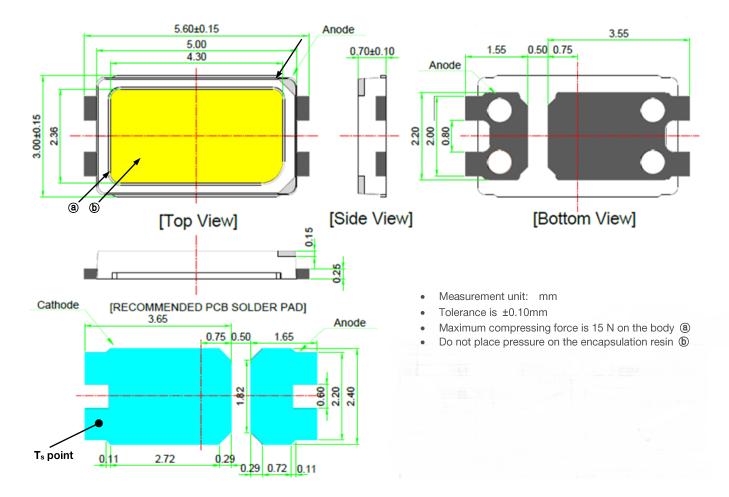
e) Derating Curve



f) Beam Angle Characteristics (I_F = 65 mA, T_s = 25 °C)



4. Outline Drawing & Dimension



Notes:

- 1) This LED has built-in ESD protection device(s) connected in parallel to LED chip(s).
- 2) Ts point and measurement method:
 - (1) Measure one point at the cathode pad, if necessary remove PSR of PCB to reach Ts point.
 - 2 All pads must be soldered to the PCB to dissipate heat properly, otherwise the LED can be damaged.

Precautions:

- 1) Pressure on the LEDs will influence to the reliability of the LEDs. Precautions should be taken to avoid strong pressure on the LEDs. Do not put stress on the LEDs during heating.
- 2) Re-soldering should not be done after the LEDs have been soldered. If re-soldering is unavoidable, LED's characteristics should be carefully checked before and after such repair.
- 3) Do not stack assembled PCBs together. Since materials of LEDs is soft, abrasion between two PCB assembled with LED might cause catastrophic failure of the LEDs.

5. Reliability Test Items & Conditions

a) Test Items

Test Item	Test Condition	Test Hour / Cycle	Sample No.
Room Temperature Life Test	25 °C, DC 180 mA	1000 h	22
High Temperature Life Test	85°C, DC 180 mA	1000 h	22
High Temperature Humidity Life Test	85 °C, 85 % RH, DC 180 mA	1000 h	22
Low Temperature Life Test	-40 °C, DC 180 mA	1000 h	22
Powered Temperature Cycle Test	-45 °C / 20 min ↔ 85 °C / 20 min, sweep 100 min cycle on/off: each 5 min, DC 180 mA	100 cycles	22
Thermal Cycle	-45 °C / 15 min ↔ 125 °C / 15 min → Hot plate 180 °C	500 cycles	100
High Temperature Storage	120 °C	1000 h	11
Low Temperature Storage	-40 °C	1000 h	11
ESD (HBM)	R₁: 10 MΩ R₂: 1.5 kΩ C: 100 pF V: ±5 kV	5 times	30
ESD (MM)	R ₁ : 10 MΩ R ₂ : 0 C: 200 pF V: ±0.5 kV	5 times	30
Vibration Test	20~2000~20 Hz, 200 m/s², sweep 4 min X, Y, Z 3 direction, each 1 cycle	4 cycles	11
Mechanical Shock Test	1500 g, 0.5 ms 3 shocks each X-Y-Z axis	5 cycles	11

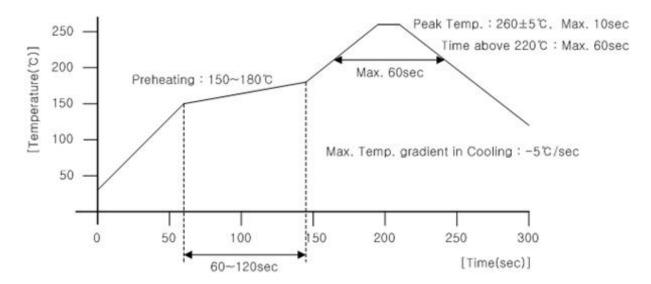
b) Criteria for Judging the Damage

ltem	Symbol	Test Condition	Lim	Limit		
item	Зуптоог	$(T_s = 25 ^{\circ}C)$	Min	Max		
Forward Voltage	V_{F}	$I_F = 65 \text{ mA}$	Init. Value * 0.9	Init. Value * 1.1		
Luminous Flux	Φν	I _F = 65 mA	Init. Value * 0.7	Init. Value * 1.1		

6. Soldering Conditions

a) Reflow Conditions (Pb free)

Reflow frequency: 2 times max.



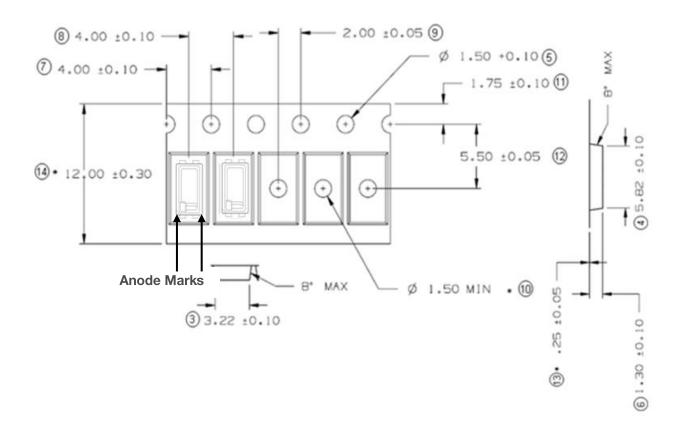
b) Manual Soldering Conditions

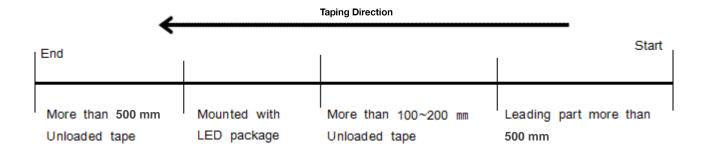
Not more than 5 seconds @ max. 300 °C, under soldering iron.

7. Tape & Reel

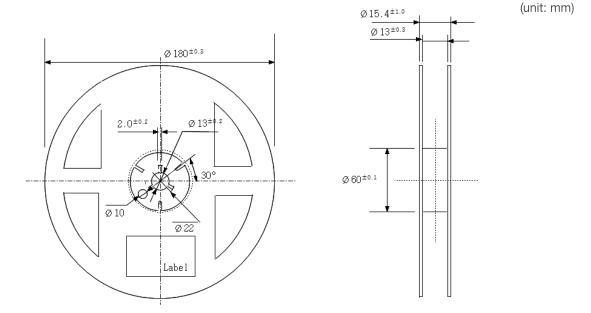
a) Taping Dimension

(unit: mm)

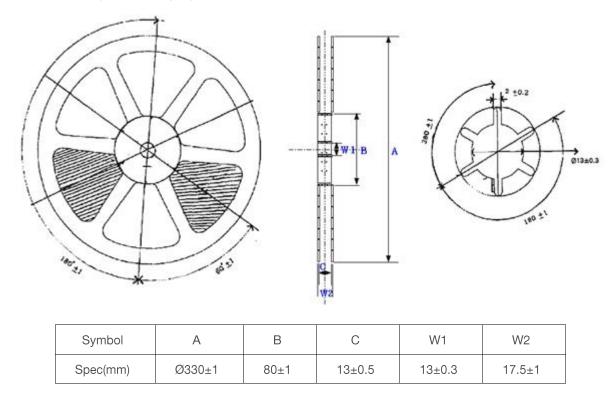




b-1) Reel Dimension (Max 2,500 pcs)



b-2) Reel Dimension (Max 10,000 pcs)

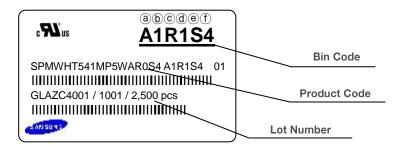


Notes:

- 1) Quantity: The quantity/reel is 2,500 or 10,000 pcs
- 2) Cumulative Tolerance: Cumulative tolerance / 10 pitches is ±0.2 mm
- 3) Adhesion Strength of Cover Tape: Adhesion strength is 0.1-0.7 N when the cover tape is turned off from the carrier tape at 10° angle to the carrier tape
- 4) Packaging: P/N, Manufacturing data code no. and quantity are indicated on the aluminum packing bag

8. Label Structure

a) Label Structure



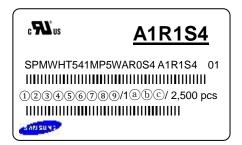
Note: Denoted bin code and product code above is only an example (see description on page 5)

Bin Code:

(a) (b): Forward Voltage bin (refer to page 11)(c) (c) (d): Chromaticity bin (refer to page 9-10)(e) (f): Luminous Flux bin (refer to page 6)

b) Lot Number

The lot number is composed of the following characters:



123456789 / 1abc / 2,500 pcs

: Production site (S: Giheung, Korea, G: Tianjin, China)

② : L (LED)

3 : Product state (A: Normal, B: Bulk, C: First Production, R: Reproduction, S: Sample)

(4) : Year (Z: 2015, A: 2016, B: 2017...)

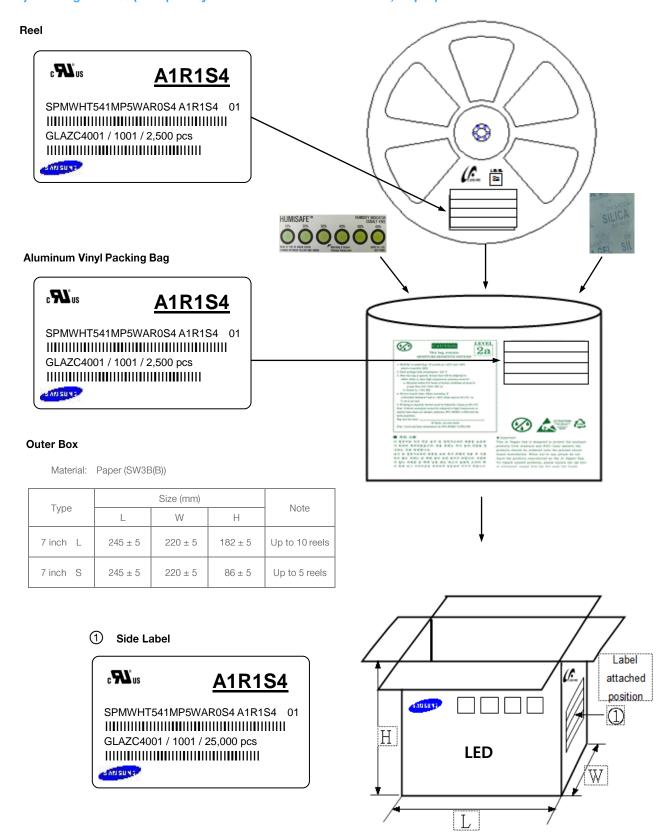
(5) : Month (1~9, A, B, C)

6789 : Day (1~9, A, B~V)

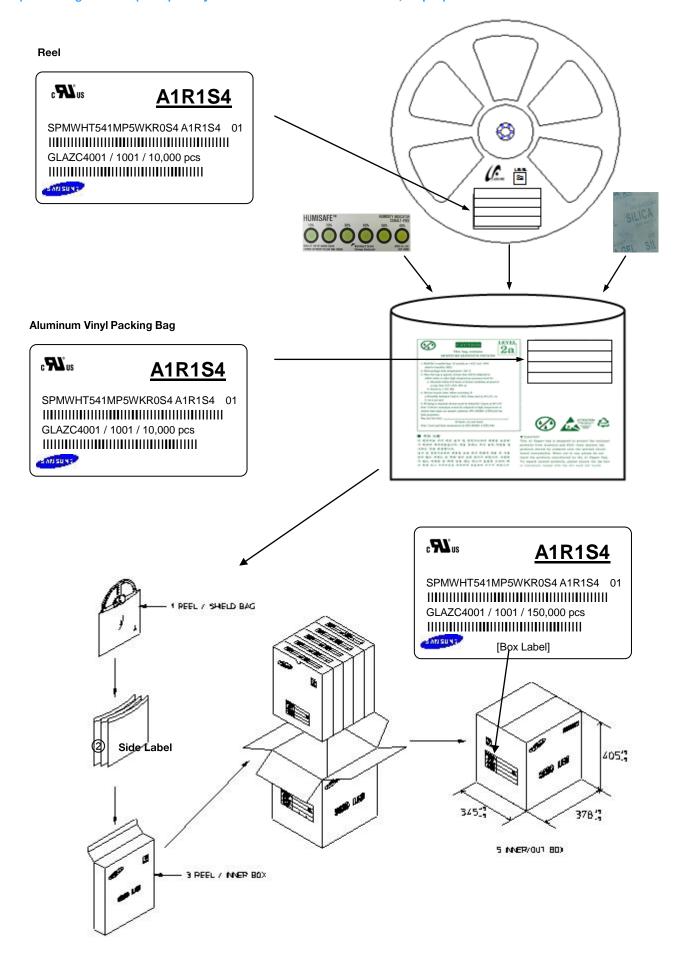
(a)b)c : Product serial number (001 ~ 999)

9. Packing Structure

a-1) Packing Process (The quantity of PKG on the Reel to be Max 2,500pcs)



a-2) Packing Process (The quantity of PKG on the Reel to be Max 10,000pcs)



b) Packing Process for kitting

Reel

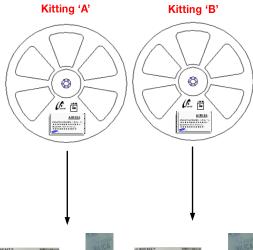
Kitting 'A'

API SU Y 2

c**R**us A1 AV

Kitting 'B'

c**%**us A1⇔Z



Aluminum Vinyl Packing Bag

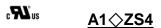
Kitting 'A'

c**W**us A1 \diamondsuit WS4

SPMWHT541MP5WA \diamondsuit KS4 A1 \diamondsuit WS4 01

GLAW94001 / 1001 / 2,500 pcs

Kitting 'B'







Outer Box

Kitting 'A'

△ A1 SWS4

[BOX Label]

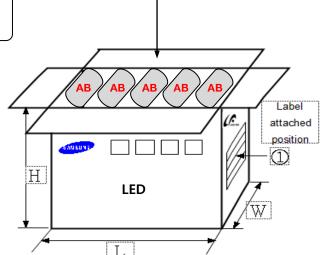
Kitting 'B'

SPMWHT541MP5WA\CKS4 A1\CZS4 01

Note: "♦" can be Nominal CCT code.

Material: Paper (SW3B(B))

Type		Size (mm)	Note	
туре	L	w	Н	Note
7 inch L	245 ± 5	220 ± 5	182 ± 5	Up to 10 reels



c) Aluminum Vinyl Packing Bag



CAUTION

2a

This bag contains MOISTURE SENSITIVE DEVICES

- Shelf life in sealed bag: 12 months at <40°C and <90% relative humidity (RH)
- 2. Peak package body temperature: 240 °C
- After this bag is opened, devices that will be subjected to reflow solder or other high temperature processes must be:
 - a. Mounted within 672 hours at factory conditions of equal to or less than 30°C /60% RH, or
 - b. Stored at < 10% RH
- Devices require bake, before mounting, if:

 a. Humidity Indicator Card is > 60% when read at 23±5°C, or
 b. 2. in order of the card is > 160% when read at 23±5°C, or
- 5. If baking is required, devices must be baked for 10 ~ 24 hours at 60±5°C Note: if device containers cannot be subjected to high temperature or shorter bake times are desired, reference IPC/JEDEC J-STD-033 for bake procedure,

Bag seal due date:

(I blank, see code label)

Note: Level and body temperature by IPC/JEDEC J-STD-020

(finance)







■ 주의 사항

이 알루미늄 지퍼 백은 습기 및 정전기로부터 제품을 보호하 기 위하여 제작되었습니다. 개봉 후에는 즉시 솔더 작업을 설 시하는 것을 권장합니다.

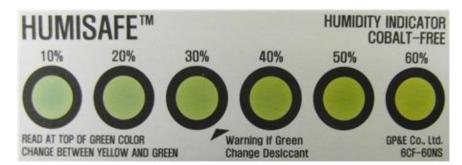
습기 및 정전기로부터 제품을 보호 하기 위해서 개봉 후 사용 하지 않는 자재는 본 팩에 넣어 보관 하시기 바랍니다. 사용하 지 않는 자재를 본 팩에 넣을 때는 반드시 동봉된 드라이 팩 과 함께 넣고 지퍼부분을 완전하게 밀봉하여 주시기 바랍니다.

■ Important

This Al Zipper bag is designed to protect the enclosed products from moisture and ESD. Once opened, the products should be soldered onto the printed circuit board immediately. When not in use, please do not leave the products unprotected by the Al Zipper Bag. To repack unused products., please ensure the zip-lock is completely sealed with the dry pack left inside.

d) Silica Gel & Humidity Indicator Card inside Aluminum Vinyl Bag







A1R1S4

PANIENAS

10. Precautions in Handling & Use

- 1) For over-current protection, users are recommended to apply resistors connected in series with the LEDs to mitigate sudden change of the forward current caused by shift of forward voltage.
- 2) This device should not be used in any type of fluid such as water, oil, organic solvent, etc. When cleaning is required, IPA is recommended as the cleaning agent. Some solvent-based cleaning agent may damage the silicone resins used in the device.
- 3) When the device is in operation, the forward current should be carefully determined considering the maximum ambient temperature and corresponding junction temperature.
- 4) LEDs must be stored in a clean environment. If the LEDs are to be stored for three months or more after being shipped from Samsung, they should be packed with a nitrogen-filled container (shelf life of sealed bags is 12 months at temperature 0~40 °C, 0~90 % RH).
- 5) After storage bag is opened, device subjected to soldering, solder reflow, or other high temperature processes must be:
 - a. Mounted within 672 hours (28 days) at an assembly line with a condition of no more than 30 °C / 60 % RH, or
 - b. Stored at <10 % RH
- 6) Repack unused devices with anti-moisture packing, fold to close any opening and then store in a dry place.
- 7) Devices require baking before mounting, if humidity card reading is >60 % at 23 ± 5 °C.
- 8) Devices must be baked for $10\sim24$ hours at 60 ± 5 °C, if baking is required.
- 9) The LEDs are sensitive to the static electricity and surge current. It is recommended to use a wrist band or antielectrostatic glove when handling the LEDs. If voltage exceeding the absolute maximum rating is applied to LEDs, it may cause damage or even destruction to LED devices. Damaged LEDs may show some unusual characteristics such as increase in leakage current, lowered turn-on voltage, or abnormal lighting of LEDs at low current.
- 10) VOCs (Volatile Organic Compounds) can be generated from adhesives, flux, hardener or organic additives used in luminaires (fixtures). Transparent LED silicone encapsulant is permeable to those chemicals and they may lead to a discoloration of encapsulant when they exposed to heat or light. This phenomenon can cause a significant loss of light emitted (output) from the luminaires. In order to prevent these problems, we recommend users to know the physical properties of materials used in luminaires and they must be carefully selected.
- 11) Risk of sulfurization (or tarnishing)
 - The LED from Samsung uses a silver-plated lead frame and its surface color may change to black (or dark colored) when it is exposed to sulfur (S), chlorine (Cl) or other halogen compound. Sulfurization of lead frame may cause intensity degradation, change of chromaticity coordinates and, in extreme cases, open circuit. It requires caution. Due to possible sulfurization of lead frame, LED should not be used and stored together with oxidizing substances made of materials such as rubber, plain paper, lead solder cream, etc.

Legal and additional information.

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