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Signature by cus	tomer:					
Product type.		800xRGBx480 RGB mode				
Samples No.	000. D	2D- 400				
Model No.	P043l	3018				
Project Size.	4.3 inch					

Email: polcd@polcd.com

Mobile: 86-136 0019 7172

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1.0 GENERAL DESCRIPTION

1.1 Introduction

Display model P043B018 is a (TM)Transmissive type color active matrix thin Film transistor(TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit, a back light system. The resolution of a 4.3" contains 800_{RGB}x480 dots and can display up to 262k colors.

Item	Specification	Unit
Screen Size	4.3`inch	Diagonal
Number of Pixel	800RGB(H)x480(V)	Pixels
Display area	95.04(H)x53.86(V)	mm
Pixel pitch	118.8(H)x112.2(V)	um
Outline Dimension	105.42x67.07x3.0	mm
Pixel arrangement	RGB Vertical Stripe	
Display mode	Normally Black	
Viewing Direction(eye)	ALL	
Gray inversion direction		
Display Color	262k	
Luminance(cd/m²)	300	nit
Contrast Ratio	1200:1	
Surface treatment		
Interface	RGB 24bit	
Back-light	LED Side-light type	
Drive IC	ST7262E43	
Operation Temperature	-20~70	$^{\circ}$
Storage Temperature	-30~80	$^{\circ}$
Weight		g

1.2 Features

n RGB 24bit interface.

1.3 Applications

- n MPOS Device.
- n Personal Navigation Device.
- n Other devices which require high quality displays.

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2.0 INPUT INTERFACE PIN ASSIGNMENT

FPC connector is used for electronics interface.

PinNo.	Symbol	Function
1	LEDK	LED back light(Cathode)
2	LEDA	LED back light(Anode)
3	GND	Ground
4	VCC	Power Supply. 2.8V
5-12	R0-R7	RGB Red data input
13-20	G0-G7	RGB green data input
21-28	B0-B7	RGB blue data input
29	GND	Ground
30	CLK	Dot clock signal for RGB interface operation
31	DISP	Display control / standby mode selection
32	HSYNC	Line synchronizing signal for RGB interface
32	HSTNC	operation
33	VSYNC	Frame synchronizing signal for RGB interface
33	VSTNC	operation
34	DE	Data enable signal for RGB interface operation
35	NC	NC
36	GND	Ground
37	XR	Touch the right end line
38	YD	Touch the lower line
39	XL	Touch the left line
40	YU	Touch the upper circuit

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3.0 ABSOLUTE MAXIMUM RATINGS

3.1 Electrical Absolute Rating

3.1.1 TFT LCD Module

Item	Symbol	Min	Max	Unit	Note
Digital supply voltage	VDDI	-0.3	+4.6	V	GND=0
Analog supply voltage	VCI	-0.3	+4.6	V	GND=0
Logic Signal Input Level	VIN	-0.3	VDDI+0.5	V	GND=0

3.1.2 Back-Light Unit

ltem	Symbol	Min	Max	Unit	Note
LED current	I _{BL}	30	40	mA	
LED voltage	V _{BL}	14	16	V	-

3.2 Environment Absolute Rating

ltem	Symbol	Min	Max	Unit	Note
Operating temperature	TOPR	-20	70	°C	-
Storage temperature	TSTG	-30	80	°C	-

Note:

Permanent damage may occur to the LCD module if beyond this specification.

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4.0 OPTICAL CHARACTERISTICS

4.1 Optical specification

Item		Symbol	Condition	Min	Type	Max	Unit	Note
White luminance (Center))	Lv	0.0		300		cd/m ²	(4)(5)(7)
Response time		Tr+Tf	Θ=0 Normal		30		ms	(6)
Contrast ratio		CR	Viewing		1200		-	(2)
Color Chromaticity	white	Wx	Angle I _{BL} =80mA	0.281	0.311	0.341		(5)
(CIE1931)	wille	Wy	IBL=00IIIA	0.308	0.338	0.368		(5)
	Hor	ΘL ΘR ΘU			80			
Viewing Angle	1 101		CR≥10		80			(1)
Viewing Angle	Ver		CN210		80			(1)
	Vei	ΘD			80			
Brightness uniformity		Avg	Θ=0	80	90		%	(5)
Color Gamut		NTSC	Θ=0		50		%	(5)
Optima View Direction			ALL				(1)	

4.2 Measuring Condition

n Measuring surrounding: dark room

n LED current IL:40mA

n Ambient temperature: 25±2℃

n 15min. warm-up time

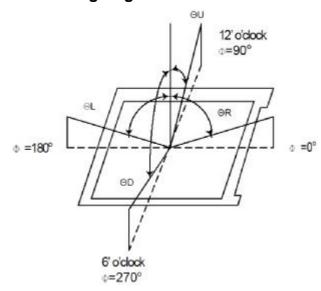
4.3 Measuring Equipment

n FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-7 for other optical characteristics.

n Measuring spot size: 20 ~ 21 mm

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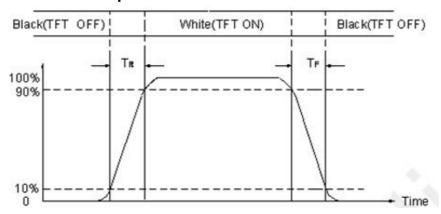
Note (1) Definition of Viewing Angle



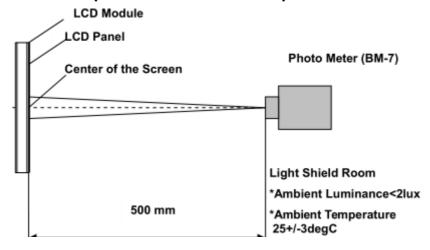
Note (2) Definition of Contrast Ratio(CR):

Measured at the center point of panel

Note (3) Definition of Response Time: Sum of TR and TF



Note (4) Definition of optical measurement setup



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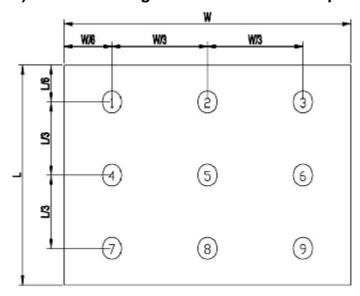
Note (5) Definition of brightness uniformity

The luminance uniformity is calculated by using following formula.

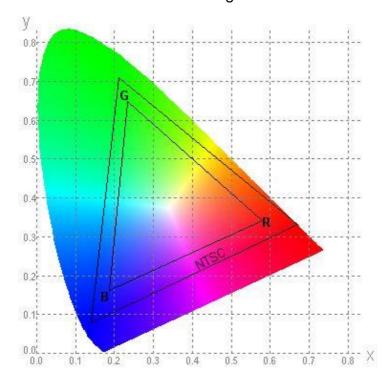
 \triangle Bp = Bp (Min.) / Bp (Max.)×100 (%)

Bp (Max.) = Maximum brightness in 9 measured spots

Bp (Min.) = Minimum brightness in 9 measured spots .



Note (6) Definition of Color of CIE1931 Coordinate and NTSC Ratio. Color gamut:

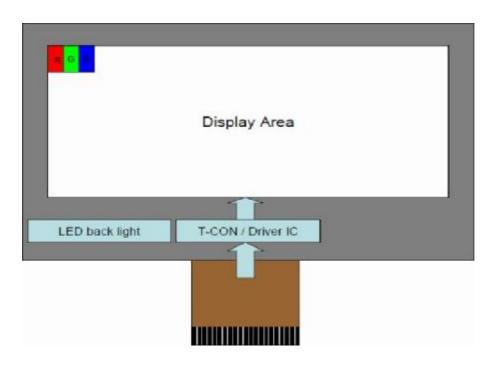


Note (7) Measured the luminance of white state at center point.

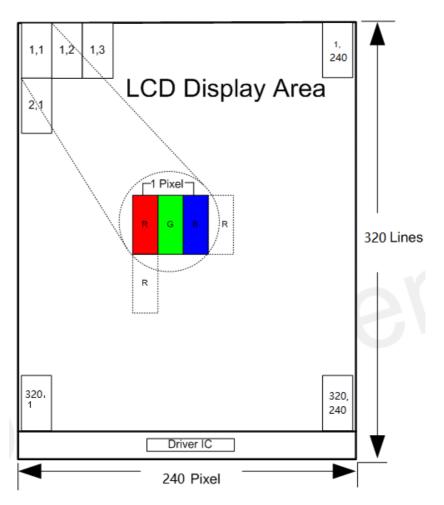
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5.0 BLOCK DIAGRAM

5.1 TFT LCD Module



5.2 Pixel Format



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6.0 ELECTRICAL CHARACTERISTICS

6.1 TFT LCD Module

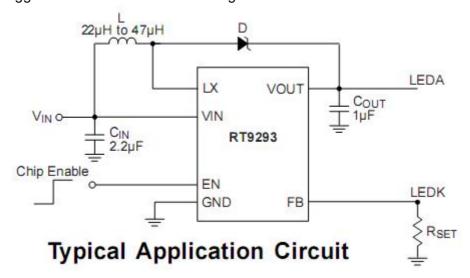
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Analog supply voltage	VDD	2.4	2.8	3.3	V	
Digital supply voltage	VDDI	1.65	1.8	3.3		
Input signal Voltage	VIH	0.7VDDI	-	VDDI	V	
Input signal Voltage	VIL	GND	-	0.3VDDI	V	

6.2 Back-Light Unit

The backlight system is an edge-lighting type with 10 LED Dies. The characteristics of the LED are shown in the following tables.

Item	Symbol	Min	Тур	Max	Unit	Note
LED current	IL	-	30	40	mA	(2)
LED voltage	VL	-	14	16	V	
Operating LED life time	Hr	-	5000	5500	Hour	(1)(2)

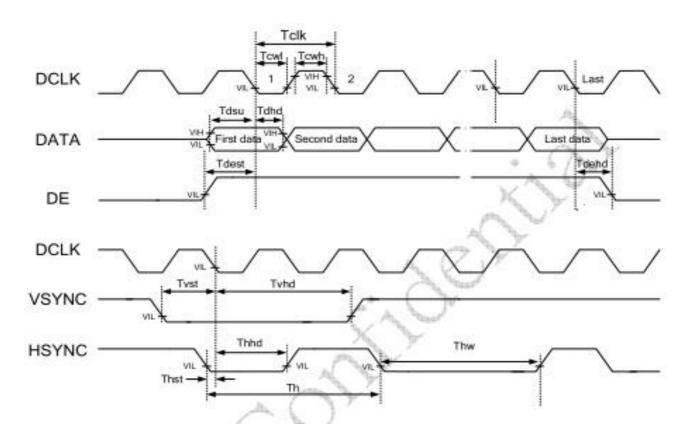
- Note (1) LED life time (Hr) can be defined as the time in which it continues to operate under the condition: $Ta=25\pm3$ °C, typical IL value indicated in the above table until the brightness becomes less than 50%.
- Note (2) The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL=80mA. The LED lifetime could be decreased if operating IL is larger than 100mA. The constant current driving method is suggested.
- Note (3) Suggested schematic of LED backlight driver



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6.3 Interface Characteristics

8080 Series RGB Interface Characteristics: 24-bit Bus



10.1.1 Parallel 24-bit RGB Timing Table

	Item	Symbol	Min.	Тур.	Max.	Unit	Remark
DCLK Frequ	iency	Fclk	8	9	12	MHz	
DCLK Perio	d	Tclk	83	111	125	ns	A
HSYNC	Period Time	Th	485	531	598	DCLK	
	Display Period	Thdisp	-	480	-	DCLK	6/14
	Back Porch	Thbp	3	43	43	DCLK	By H_Blanking setting
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	75	DCLK	7
VSYNC	Period Time	Tv	276	292	321	7 47	
	Display Period	Tvdisp	-	272	-	H-	
	Back Porch	Tvbp	2	12	12	∀ н	By V_Blanking setting
	Front Porch	Tvfp	2	8	37	Н	
	Pulse Width	Tvw	2	4	37	Н	

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

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7.0 Reliability conditions

NO	Item	Conditions	Notes
1	High Temperature Storage	Ta=80℃±2℃, 72hrs	
2	Low Temperature Storage	Ta=-30℃ ±2℃, 72hrs	
3	High Temperature Operation	Ta=70°C±2°C, 72hrs(Operation state)	
4	Low Temperature Operation	Ta=-20°C ±2°C, 72hrs(Operation state)	
5	High Temperature and High Humidity (Storage)	Ta=+60°C, 90%RH, 72hrs	
6	Thermal Cycling Test (non operation)	-20°C(30min) → +70°C(30min), 10cycles	
7	Electro static Discharge	Human Body Mode $100pF\pm10\%/1500~\Omega\pm1\%$ Air $\pm8kV$ / contact $\pm6kV$ Consecutive 10times/ Each discharge $\frac{R}{V=0}$ CLASS STRESS LEVELS (CLASS II 0.1899V) CLASS II 0.1899V (CLASS II 0.1899V) (CLASS II 0.1899V)	
8	Vibration test(with carton)	Total fixed amplitude:15mm Vibration Frequency:10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	
9	Drop (with carton)	Height: 60cm 1 corner, 3 edges, 6 surfaces	

Note: There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.

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8.0 Precautions

8.1 Operation

Burn-in sometimes happens when the same character was displayed at along time. Therefore, to prevent Burn-in, it is recommended to set up a Screen-saver function.

8.2 Safety

The liquid crystal in the LCD is poisonous, DO NOT put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

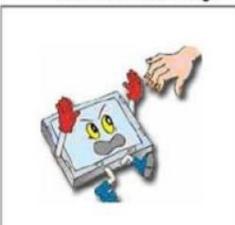
8.3 Handling

8	a. The LCD module shall be installed flat, without twisting or bending. b. COF or FPC has narrow pattern width, so easily become open circuit by external force. DO NOT apply pressure to COF or FPC especially in bending area.
	c. To avoid damage in appearance or malfunction, DO NOT subject the module to mechanical shock or to excessive force on its surface.
	d. The polarizer attached to the display is very easy to damage, handle it with care to avoid scratching.
	e. To avoid contamination on the display surface, DO NOT touch the display surface with bare hands. f. Provide a space so that the LCD module does not come into contact with other components.

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8.4 Static Electricity

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge.



- The LCD module shall be installed flat, without twisting or bending. Ground soldering iron tips, tools and testers when they operate.
- Ground your body when handling the products.
- DO NOT apply voltage to the input terminal without applying power supply.
- DO NOT apply voltage that exceeds the absolute maximum rating.
- e. Store the products in an anti-electrostatic container.
- Peel off protect tape, attached to polarizer, slowly to minimize ESD damage.

8.5 Storage



Store the products in a dark place at $+5 \sim +25$ degree C, low humidity (50%RH or less).

DO NOT store the products in an atmosphere containing organic solvents or corrosive gases.

8.6 Cleaning



- DO NOT wipe the polarizer with dry cloth, as it might cause scratch.
- Wipe the polarizer with a soft cloth soaked with petroleum IPA, other chemical might damage.

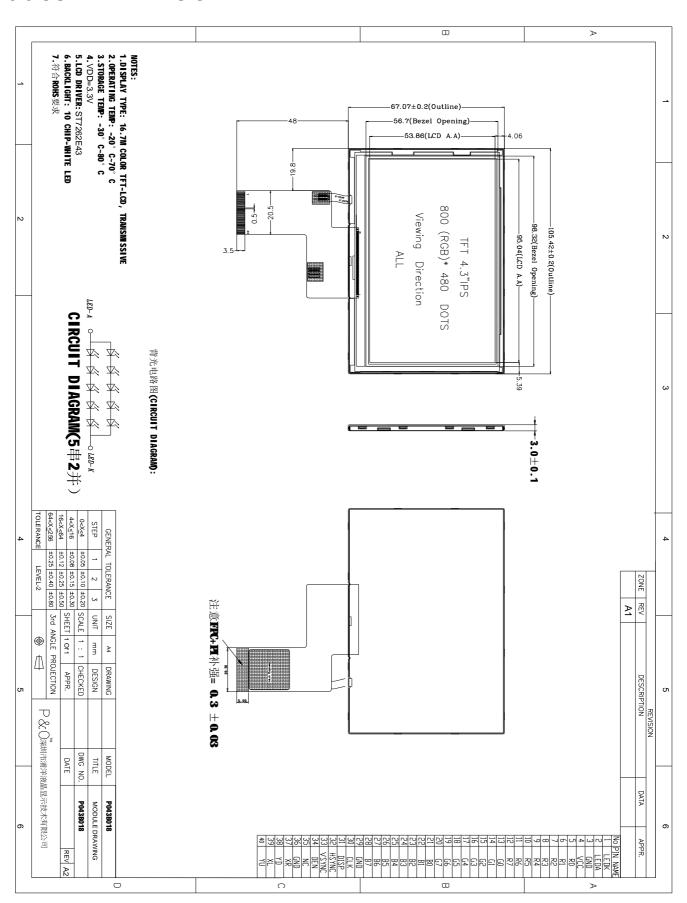
8.7 Waste



When dispose of LCD module, manage it at the production waste according to the relevant laws and regulations.

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9.0 OUTINE DIMENSION



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1 0.0 LOT MARK

10.1 Location of Lot Mark

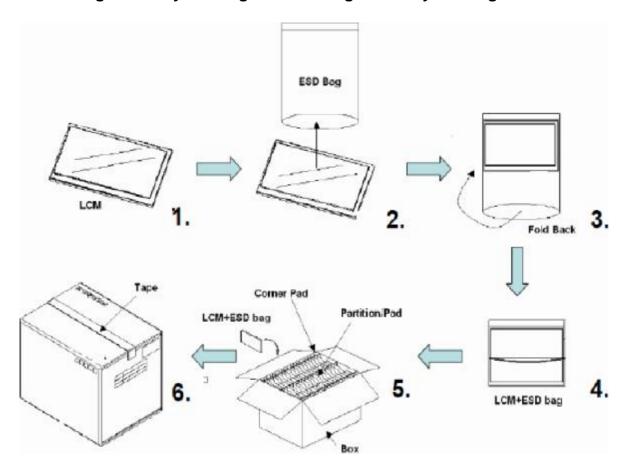
- (1) Location: The label is attached to the backside of the LCD module.
- (2) Detail of the Mark: as attached below.
- (3) This is subject to change without prior notice.

11.0 PACKAGE SPECIFICATION

11.1 Packing form

LCM Model	LCM Qty. in the box	Inner Box Size (mm)	Notice
	TDB	TDB	

11.2 Packing assembly drawings11.2 Packing assembly drawings



Items	Material	Notice
Box	Corrugated Paper Board	AB Flute
Partition/Pad	Corrugated Paper Board	A/B Flute
Corner Pad	Corrugated Paper Board	AB Flute
ESD bag	PE	

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12.0 Items and Criteria:

12.1 Guarantee

APEX warrants the quality of our products for *1 year* (from the date of delivery). If there are functional defects found during the period of warranty, the defective products would be replaced on a one-to-one backapex would not be responsible for any direct /indirect liabilities consequential to any parties.

All the products should be stored or used as specified conditions described in these sheets. If module productions are not stored or used as specified conditions, herein, it will be void the *1 year* warranty(guarantee).

12.2 Visual inspection criterion in cosmetic

(1) Glass defect

	Glass defect				
NO	Defect	Criteria	Remark		
1	Dimension(Minor)	By engineering diagram	↑ ↑		
2	Cracks(Major)	Extensive crack 【Reject】			

(2) LCM appearance defect

NO	Defect	Criteria		Remark
		Spec	Permissible Qty	1.ψ=(L+W)/2, L: Length, W: Width
		ψ≦0.10mm	Disregard	2. Disregard if out of A.A.
1	Round type(Minor)	0.10 mm< $\psi \le 0.20$ mm	3	
		0.20mm<ψ	0	₩ V
		Spec	Permissible	1. L: Length, W: Width
		·	Qty	2. Disregard if out of A.A.
	Line type(Minor)	W ≦ 0.03mm	Disregard	
0		L≦3.0mm and	2	─ └ →
2		0.03mm <w≦0.05mm< td=""><td></td><td></td></w≦0.05mm<>		
		L≦3.0mm and	1	V 7/1
		0.05mm <w≦0.10mm< td=""><td></td><td>W</td></w≦0.10mm<>		W
		W>0.10mm orL>3.0mm	0	130005
		Spec.	Permissible	1.ψ=(L+W)/2 , L: Length,
			Qty	W: Width
3		ψ≦0.20mm	Disregard	2.Disregard if out of A.A.
	Polarizer	0.20mm<ψ≦ 0.30mm	2	
	dent(Minor)	0.30mm<ψ≦ 0.50mm	1	

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(3) FPC

	Defect	Criteria	Remark
1	Copper peeling(Minor)	Copper peeling [Reject]	
2	Golden finger	FPC golden finger broken, dead fold, indentation makes FPC surface broken 【Reject】 Tin plating layer(or gold plating) scratch, but not hurt circuit 【Accept】 Except circuit, other position scratch but not expose metal wire 【Accept】	
3	Pin	metal wire 【Accept】 FPC PI layer delamination 【Reject】 Material and color are inconsistent with sample, FPC burrs 【Reject】 FPC Pin deformation but not affect function. 【Accept】 FPC Pin area is dirty 【Reject】 Other than FPC Pin area is dirty but not affect function 【Accept】	
4	Golden finger	Golden finger edge has burrs,foreign material [Reject] Golden finger oxidation (dark), uneven electroplating, pinhole, foreign material [Reject] Golden finger soldering pad crack exceeds 1/3 length of soldering pad, and soldering pad crack exceed 2 Pins [Reject] Golden finger tin plating(or gold plating)scratch, but not hurt circuit [Accept] Other than golden finger area scratch but not expose metal circuit [Accept]	
5	Ghosting, incomplete silk printing, wrong printing		
	<u> </u>	Line width deviation exceed 1/3 line width 【Reject】	

(4) Black tape

NO	Defect	Criteria	Remark
1	Shift(Minor)	IC exposed 【Reject】	
2	No black tape(Minor)	No black tape 【Reject】	

(5) Silicon

NO	Defect	Criteria	Remark
1	Amount of silicon (Minor)	ITO exposed 【Reject】	

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12.3 Visual inspection criterion in electrical display

NO	Defect	Criteria		Remark
1	No display (Major)	Not allowed		
2	Missing line (Major)	Not allowed		
3	Darker or lighter Line (Major)	Not allowed		
4	Weak line(Major)	By limited sample		
5	Bright / Dark point (Minor)	Spec. Permissi Bright 1 point	ble Qty	1:1sub-pixel: 1R or 1G or1B 2:Point defect area ≧ 1/2 sub pixel.
		Dark 2 point		
		Spec	Permissible Qty	1.ψ=(L+W)/2, L: Length, W: Width
		ψ≦0.10mm	Disregard	2. Disregard if out of A.A.
6	Round type (Minor)	0.10mm<ψ≦ 0.20mm		- ÎL
		0.20mm<ψ	0	→ W
		Spec.	Permissible Qty	L: Length, W: Width Disregard if out of A.A.
	Line type (Minor)	W≦0.03mm	Disregard	2. Distegate it out of A.A.
	3,20 (L≦3.0mm and	2	\leftarrow \leftarrow
_		0.03mm <w≦0.05mm< td=""><td></td><td>77</td></w≦0.05mm<>		77
7		L≦3.0mm and	1	V 7/1/2
		0.05mm <w≦0.10mm< td=""><td></td><td>W</td></w≦0.10mm<>		W
		W>0.10mm	or 0	
		L>3.0mm		
8	Mura (Minor)	By 5% ND filter invisib	le	

9.2.4. Others

- 1. Issues that are not defined in this document shall be discussed and agreed with both parties. (Customer and supplier)
- 2. Unless otherwise agreed upon in writing, the criteria shall be applied to both parties. (Customer and supplier)