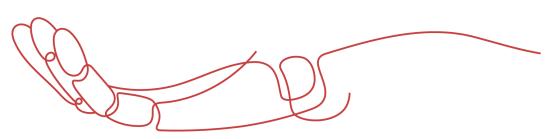


PRODUCT DATA SHEET



To learn more about JGSEMI, please visit our website at







Datasheet

ces Sami

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.



General Description

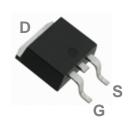
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

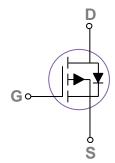
BVDSS	RDSON	ID
-40V	10m Ω	-40A

Features

- -40V, -40A, $RDS(ON) = 10m\Omega@VGS = -10V$
- Fast switching
- Green Device Available

TO252 Pin Configuration





Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	- 40	V
V_{GS}	Gate-Source Voltage	±20	V
1-	Drain Current – Continuous (Tc=25°C)	-40	А
ID	Drain Current – Continuous (Tc=100°C)	-28	А
I _{DM}	Drain Current – Pulsed ¹	-160	Α
D-	Power Dissipation (T _C =25°C)	73.5	W
P _D	Power Dissipation – Derate above 25°C	0.59	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 125	°C

Thermal Characteristics

Symbol	Symbol Parameter		Max.	Unit
R _θ JC	Thermal Resistance Junction to Case		1.7	°C/W
RθJA	Thermal Resistance Junction to Ambient		62	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D = - 250uA	- 40			V
I _{DSS}	Drain Course Leekage Current	V _{DS} =-40V , V _{GS} =0V , T _J =25°C			-1	uA
	Drain-Source Leakage Current	V _{DS} =-32V , V _{GS} =0V , T _J =125°C			-10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-10A	10	15	mΩ	
KDS(ON)	Static Drain-Source On-Resistance	V _{GS} = - 4.5V , I _D = - 8A		13	20	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA	-1.0	-1.4	- 2.5	V
gfs	Forward Transconductance	V _{DS} =-10V , I _D =-10A		13		S

Dynamic and switching Characteristics

Qg	Total Gate Charge ^{3, 4}		 22.2	
Q_{gs}	Gate-Source Charge ^{3, 4}	V _{DS} =-32V , V _{GS} =-4.5V , I _D =-10A	 8.2	 nC
Q_{gd}	Gate-Drain Charge ^{3, 4}		 8.8	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}		 23	
Tr	Rise Time ^{3, 4}	V_{DD} =-20V , V_{GS} =-10V , R_{G} =6 Ω	 10	 no
T _{d(off)}	Turn-Off Delay Time ^{3, 4}	I _D =-1A	 135	 ns
Tf	Fall Time ^{3, 4}		 46	
Ciss	Input Capacitance		 2757	
Coss	Output Capacitance	V _{DS} =-25V , V _{GS} =0V , F=1MHz	 240	 pF
Crss	Reverse Transfer Capacitance		 137	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V , Force Current			-40	Α
Ism	Pulsed Source Current	VG=VD=0V, Force Current			-80	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-1A , T _J =25°C			- 1.2	V

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. V_{DD} =25V, V_{GS} =10V,L=0.1mH, I_{AS} =51A., R_{G} =25 Ω , Starting T_{J} =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.



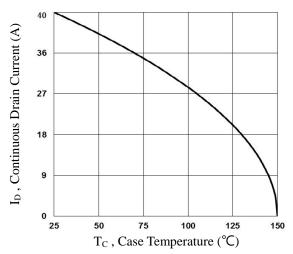


Fig.1 Continuous Drain Current vs. Tc

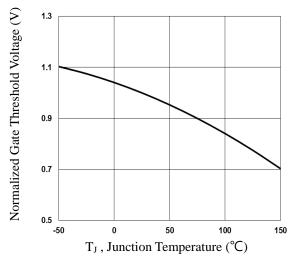


Fig.3 Normalized V_{th} vs. T_J

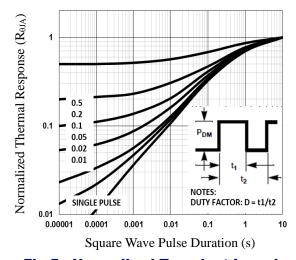


Fig.5 Normalized Transient Impedance

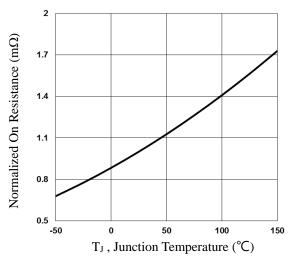


Fig.2 Normalized RDSON vs. TJ

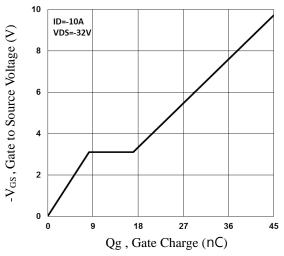


Fig.4 Gate Charge Waveform

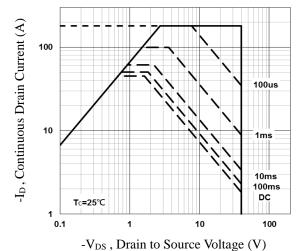
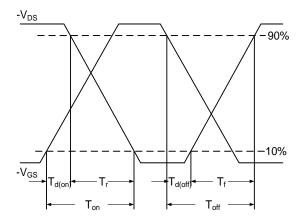


Fig.6 Maximum Safe Operation Area







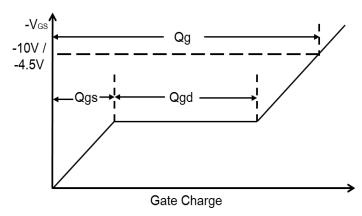
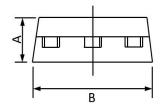
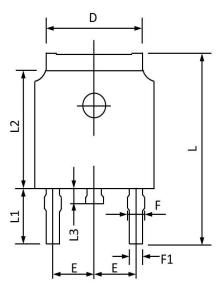


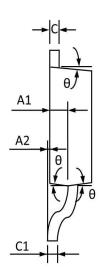
Fig.8 Gate Charge Waveform



TO252 PACKAGE INFORMATION







Crombal	Dimensions I	n Millimeters	Dimension	s In Inches
Symbol	MAX	MIN	MAX	MIN
A	2.400	2.200	0.094	0.087
A1	1.110	0.910	0.044	0.036
A2	0.150	0.000	0.006	0.000
В	6.800	6.400	0.268	0.252
C	0.580	0.450	0.023	0.018
C1	0.580	0.460	0.023	0.018
D	5.500	5.100	0.217	0.201
E	2.386	2.186	0.094	0.086
F	0.940	0.600	0.037	0.024
F 1	0.860	0.500	0.034	0.020
L	10.400	9.400	0.409	0.370
L1	3.000	2.400	0.118	0.094
L2	6.200	5.400	0.244	0.213
L3	1.200	0.600	0.047	0.024
θ	9 °	3 °	9 °	3°



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