

# **PRODUCT DATA SHEET**



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Datasheet

Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO\_questions@jgsemi.com.



### **General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

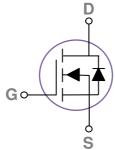
BVDSS	RDSON	ID
60V	70m $\Omega$	ЗА

#### **Features**

- 60V,3A,  $RDS(ON) = 70m\Omega@VGS = 10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

# **SOT23-3Pin Configuration**





# **Applications**

- Motor Drive
- Power Tools
- LED Lighting

#### Absolute Maximum Ratings Tc=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	60	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
<b> </b> _	Drain Current – Continuous (T <sub>C</sub> =25°C)	3.0	Α
D	Drain Current – Continuous (Tc=100°C)	2	Α
рм	Drain Current – Pulsed <sup>1</sup>	12.8	Α
D-	Power Dissipation (Tc=25°C)	1.56	W
$P_{D}$	Power Dissipation – Derate above 25°C	0.012	W/°C
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C
ΓJ	Operating Junction Temperature Range	-50 to 125	°C

## **Thermal Characteristics**

Symbol	Symbol Parameter		Max.	Unit
Reja	Thermal Resistance Junction to ambient		80	°C/W



# **Electrical Characteristics** (T<sub>J</sub>=25 °C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	60			٧
△BV <sub>DSS</sub> /△T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.05		V/°C
IDSS	Drain-Source Leakage Current	$V_{DS}$ =60 $V$ , $V_{GS}$ =0 $V$ , $T_{J}$ =25 $^{\circ}$ C	=25°C		1	uA
	Diam-Source Leakage Current	$V_{DS}$ =48V , $V_{GS}$ =0V , $T_J$ =125°C			10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm20V$ , $V_{DS}=0V$			±100	nA

#### **On Characteristics**

R <sub>DS(ON)</sub> Static Drain-Source On-Resistance	Statio Drain Source On Degistance	$V_{GS}=10V$ , $I_{D}=3A$		70	90	mΩ
	Static Dialii-Source Oil-nesistance	$V_{GS}$ =4.5 $V$ , $I_D$ =1.5 $A$		80	100	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V V I 050A	1.0	1.6	2.5	V
$\triangle V_{\text{GS(th)}}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=250uA$		-5		mV/°C
gfs	Forward Transconductance	$V_{DS}=10V$ , $I_{D}=3A$		7		S

# **Dynamic and switching Characteristics**

$Q_g$	Total Gate Charge <sup>2,3</sup>		 9.3	
Qgs	Gate-Source Charge <sup>2,3</sup>	$V_{DS}$ =48 $V$ , $V_{GS}$ =10 $V$ , $I_{D}$ =3 $A$	 2.1	 nC
$Q_gd$	Gate-Drain Charge <sup>2,3</sup>		 1.8	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2, 3</sup>		 2.9	
Tr	Rise Time <sup>2, 3</sup>	$V_{\text{DD}}{=}30\text{V}$ , $V_{\text{GS}}{=}10\text{V}$ , $R_{\text{G}}{=}3.3\Omega$	 9.5	 no
$T_{d(off)}$	Turn-Off Delay Time <sup>2,3</sup>	I <sub>D</sub> =1A	 18.4	 ns
Tf	Fall Time <sup>2,3</sup>		 5.3	
Ciss	Input Capacitance		 500	
Coss	Output Capacitance	$V_{DS}$ =15 $V$ , $V_{GS}$ =0 $V$ , $F$ =1 $MHz$	 45	 pF
Crss	Reverse Transfer Capacitance		 16	
$R_g$	Gate resistance	$V_{GS}$ =0V, $V_{DS}$ =0V, F=1MHz	 2	 Ω

# **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			3.0	Α
Ism	Pulsed Source Current	VG=VD=0V, Force Current			6.0	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1.2	V
t <sub>rr</sub>	Reverse Recovery Time <sup>2</sup>	Vgs=30V,ls=1A , dl/dt=100A/μs		23.2		ns
Qrr	Reverse Recovery Charge <sup>2</sup>	T <sub>J</sub> =25°C		14.3		nC

#### Note:

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
  The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
- 3. Essentially independent of operating temperature.



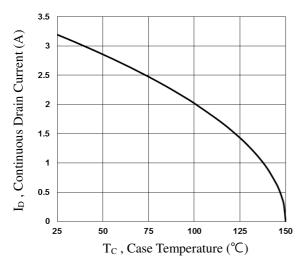


Fig.1 Continuous Drain Current vs. Tc

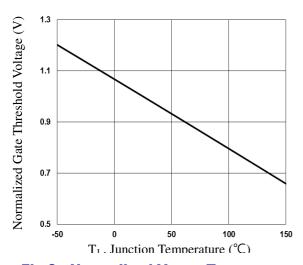


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

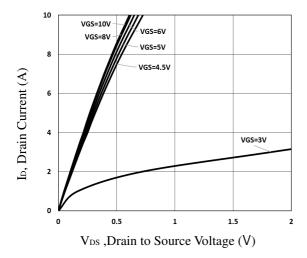


Fig.5 Typical Output Characteristics

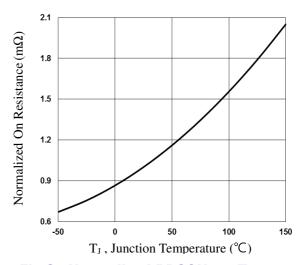


Fig.2 Normalized RDSON vs. T<sub>J</sub>

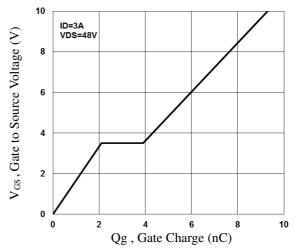


Fig.4 Gate Charge Waveform

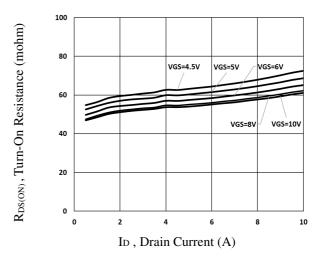


Fig.6 Turn-On Resistance vs. ID



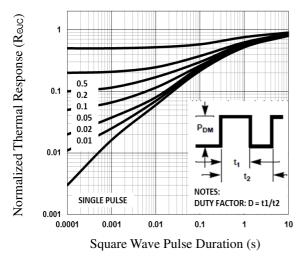


Fig.7 Normalized Transient Response

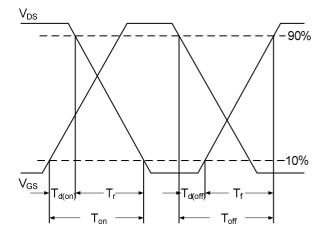


Fig.9 Switching Time Waveform

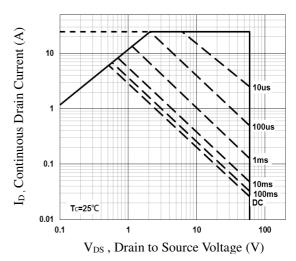
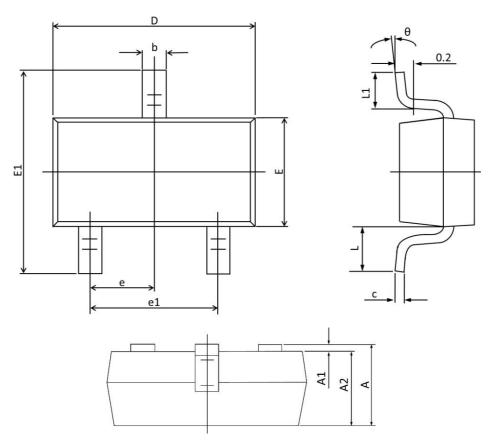


Fig.8 Maximum Safe Operation Area



# **SOT23-3 PACKAGE INFORMATION**



Symbol	Dimensions	Dimensions In Millimeters		s In Inches	
Symbol	Min	Max	Min	Max	
Α	1.050	1.450	0.041	0.057	
<b>A</b> 1		0.150		0.006	
A2	0.900	1.300	0.035	0.051	
b	0.300	0.490	0.012	0.019	
С	0.100	0.200	0.004	0.008	
D	2.820	3.050	0.111	0.120	
Е	1.500	1.750	0.059	0.069	
E1	2.600	3.000	0.102	0.118	
е	0.950 TYP.		0.03	7 TYP.	
e1	1.800	2.000	0.071	0.079	
L	0.700	REF.	0.028 REF.		
L1	0.300	0.600	0.012	0.024	
θ	0°	8°	0°	8°	



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