

PNP SILICON POWER DARLINGTON



BD650 TO-220 Plastic Package

FEATURES

- Designed for Complimentary use with BD649
- 62.5 W at 25°C Case Temperature
- 8A Continuous Collector Current
- Minimum h_{FE} of 750 at 3V, 3A

MAXIMUM RATINGS (T_a=25°C unless otherwise noted)

PARAMETER	SYMBOL	VALUE	UNIT
Collector Base Voltage	V _{CBO}	120	V
Collector Emitter Voltage	V _{CEO}	100	V
Emitter Base Voltage	V _{EBO}	5	V
Continuous collector current	Ι _c	8	А
Peak collector current ¹⁾	I _{CM}	12	А
Continuous base current	I _B	0.3	А
Continuous device dissipation at (or below) 25°C case temperature ²⁾	P _{tot}	62.5	W
Continuous device disspation at (or below) 25°C free air temperature ³⁾	P _{tot}	2	W
Unclamped inductive load energy 4)	¹ /2LIC ²	50	mJ
Operating junction temperature range	T _j	-65 to 150	٥C
Storage temperature range	T _{stg}	-65 to 150	°C
Lead temperature 3.2 mm from case for 10 seconds	TL	260	°C

NOTES:

1. This value applies for tp = 300μ s, duty cycle = 10%

2. Derate linearly to 150°C case temperature at the rate of 0.4 W/°C

3. Derate linearly to 150°C free air temperature at the rate of 16 mW/°C

4. This rating is based on the capability of the transistor to operate safely in a circuit of: L= 20mH,

 $I_{B(on)}$ =5mA, R_{BE} =100W, $V_{BE(off)}$ =0, R_{S} =0.1W, V_{CC} =20V





ELECTRICAL CHARACTERISTICS (T_a =25°C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT		
Collector-emitter breakdown voltage	V _{(BR)CEO}	I_{c} =30 mA, I_{B} =0 ⁵⁾	100		V		
	$ \begin{array}{c} $	V _{CB} =100V, I _E =0		0.2	mA		
			2.0	mA			
Collector cut-off current	I _{CEO}	V _{ce} =50V, I _b =0		0.5	mA		
Emitter cut-off current	I _{EBO}	V _{EB} =5V, I _C =0 ^{5,6)}		5	mA		
DC current gain	h _{FE}	V _{CE} =3V, I _C =3A ^{5,6)}	750				
Colector-emitter saturation voltage	$V_{CE(sat)}$	I _C =3A, I _B =12mA ^{5,6)}		2	V		
		I _c =5A, I _B =50mA ^{5,6)}		2.5	V		
Base-emitter saturation voltage	V _{BE(sat)}	I _C =5A, I _B =50mA ^{5,6)}		3	V		

NOTES:

5. These parameters must be measures using pulse techniques, tp = 300μ s, duty cycle = 2%

6. These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

MAXIMUM RATINGS (T_a=25°C unless otherwise noted)

PARAMETER	SYMBOL	VALUE	UNIT
Junction to case thermal resistance	$R_{_{ ext{ hetaJC}}}$	2.0	°C/W
Junction to free air thermal resistance	R _{eja}	62.5	°C/W







TO-220 PACKAGE OUTLINE AND DIMENSIONS

1. BASE 2. COLLECTOR 3. EMITTER

BD650 Rev_0 08122017E Continental Device India Pvt. Ltd.





Customer Notes:

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.

2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

DICLAIMER The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).



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