

BUL128

HIGH VOLTAGE FAST-SWITCHING NPN POWER TRANSISTOR

- SGS-THOMSON PREFERRED SALESTYPE
- NPN TRANSISTOR
- HIGH VOLTAGE CAPABILITY
- LOW SPREAD OF DYNAMIC PARAMETERS
- MINIMUM LOT-TO-LOT SPREAD FOR RELIABLE OPERATION
- VERY HIGH SWITCHING SPEED

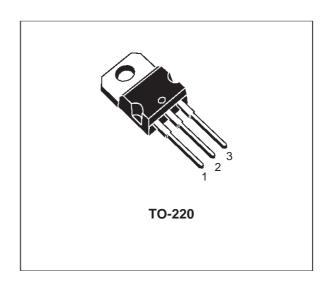
APPLICATIONS:

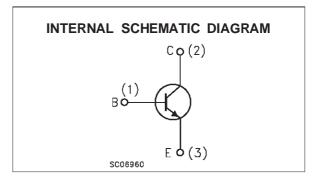
 ELECTRONIC BALLASTS FOR FLUORESCENT LIGHTING

DESCRIPTION

The device is manufactured using high voltage Multi Epitaxial Planar technology for high switching speeds and medium voltage capability. It uses a Cellular Emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The device is designed for use in lighting applications and low cost switch-mode power supplies.





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
Vces	Collector-Emitter Voltage (V _{BE} = 0)	700	V
V_{CEO}	Collector-Emitter Voltage (I _B = 0)	400	
V _{EBO}	Emitter-Base Voltage (I _C = 0)	9	V
Ic	Collector Current	4	Α
I _{CM}	Collector Peak Current (t _p < 5 ms)	8	А
I _B	Base Current	2	Α
Івм	Base Peak Current (tp < 5 ms)	4	Α
P _{tot}	Total Dissipation at T _c = 25 °C	70	W
T _{stg}	Storage Temperature	-65 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

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THERMAL DATA

R _{thj-case}	Thermal Resistance Junction-Case	Max	1.78	°C/W
R _{thj-amb}	Thermal Resistance Junction-Ambient	Max	62.5	°C/W

ELECTRICAL CHARACTERISTICS (T_{case} = 25 °C unless otherwise specified)

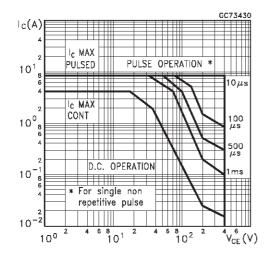
Symbol	Parameter	Test Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector Cut-off Current (V _{BE} = -1.5 V)	V _{CE} = 700 V V _{CE} = 700 V	T _j = 125 °C			100 500	μA μA
V_{EBO}	Emitter-Base Voltage	I _E = 10 mA		9			V
V _{CEO(sus)}	Collector-Emitter Sustaining Voltage	I _C = 100 mA	L = 25 mH	400			V
I _{CEO}	Collector Cut-Off Current (I _B = 0)	V _{CE} = 400 V				250	μА
V _{CE(sat)*}	Collector-Emitter Saturation Voltage	I _C = 0.5 A I _C = 1 A I _C = 2.5 A I _C = 4 A	$I_{B} = 0.1 A$ $I_{B} = 0.2 A$ $I_{B} = 0.5 A$ $I_{B} = 1 A$		0.5	0.7 1 1.5	> > >
VBE(sat)*	Base-Emitter Saturation Voltage	Ic = 0.5 A I _C = 1 A I _C = 2.5 A	$I_B = 0.1 A$ $I_B = 0.2 A$ $I_B = 0.5 A$			1.1 1.2 1.3	V V
h _{FE} *	DC Current Gain	Ic = 10 mA Ic = 2 A Group A Group B	V _{CE} = 5 V V _{CE} = 5 V	10 14 25		28 40	
t _s	RESISTIVE LOAD Storage Time Fall Time	$V_{CC} = 125 \text{ V}$ $I_{B1} = 0.4 \text{ A}$ $T_p = 30 \mu\text{s}$ (see fig.2)	Ic = 2 A I _{B2} = -0.4 A	1.5	0.2	3 0.4	μs μs
t _s t _f	INDUCTIVE LOAD Storage Time Fall Time	$I_C = 2 \text{ A}$ $V_{BEoff} = -5 \text{ V}$ $V_{clamp} = 200 \text{ V}$ (see fig.1)	$I_{B1} = 0.4 A$ $R_{BB} = 0 \Omega$		0.6 0.1	1 0.2	μs μs

* Pulsed: Pulse duration = 300 µs, duty cycle 1.5 %

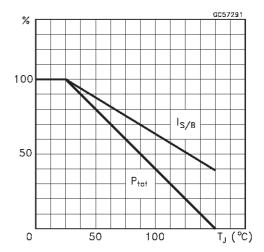
Note: Product is pre-selected in DC current gain (GROUP A and GROUP B). SGS-THOMSON reserves the right to ship either groups according to production availability. Please contact your nearest SGS THOMSON MICROELECTRONICS sales office for delivery details.

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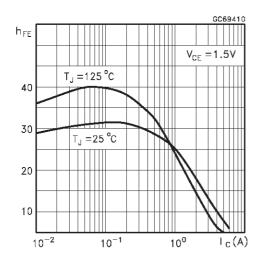
Safe Operating Areas



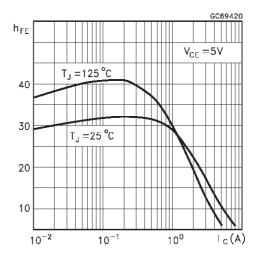
Derating Curve



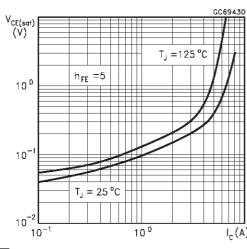
DC Current Gain



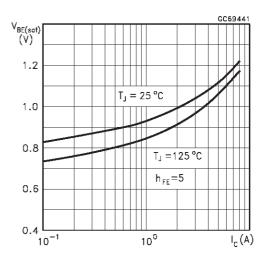
DC Current Gain



Collector Emitter Saturation Voltage

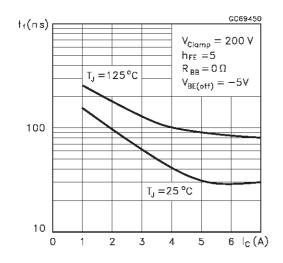


Base Emitter Saturation Voltage

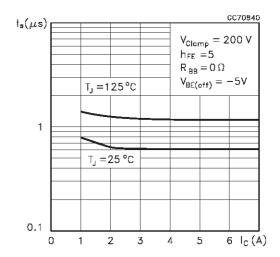


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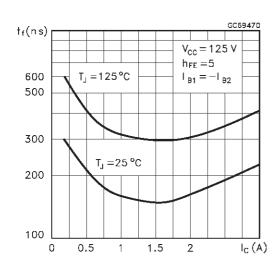
Inductive Fall Time



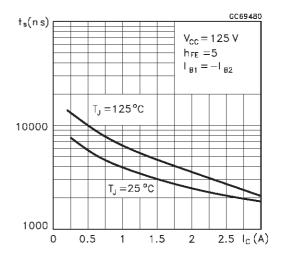
Inductive Storage Time



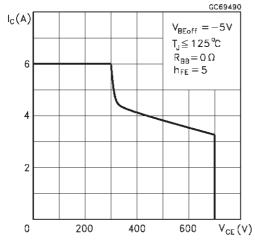
Resistive Fall Time



Resistive Load Storage Time



Reverse Biased SOA



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Figure 1: Inductive Load Switching Test Circuit.

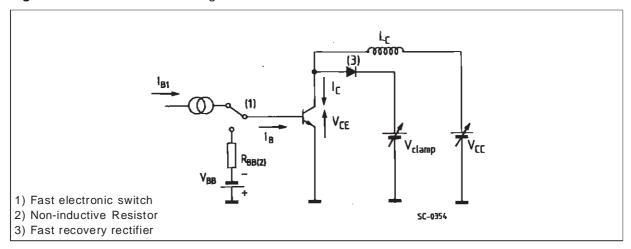
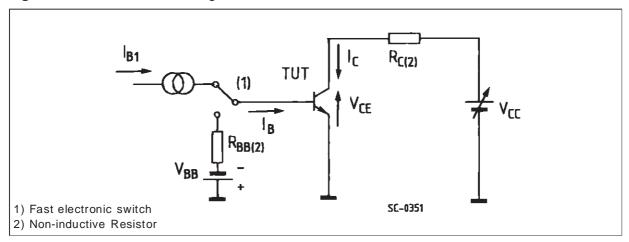
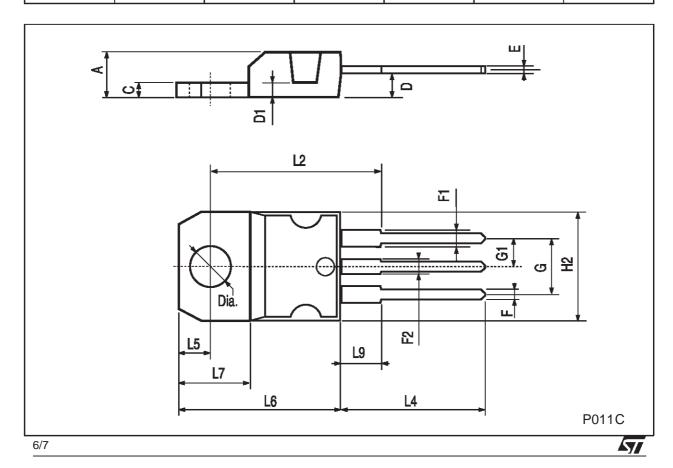


Figure 2: Resistive Load Switching Test Circuit.



TO-220 MECHANICAL DATA

DIM.	mm			inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
А	4.40		4.60	0.173		0.181	
С	1.23		1.32	0.048		0.051	
D	2.40		2.72	0.094		0.107	
D1		1.27			0.050		
E	0.49		0.70	0.019		0.027	
F	0.61		0.88	0.024		0.034	
F1	1.14		1.70	0.044		0.067	
F2	1.14		1.70	0.044		0.067	
G	4.95		5.15	0.194		0.203	
G1	2.4		2.7	0.094		0.106	
H2	10.0		10.40	0.393		0.409	
L2		16.4			0.645		
L4	13.0		14.0	0.511		0.551	
L5	2.65		2.95	0.104		0.116	
L6	15.25		15.75	0.600		0.620	
L7	6.2		6.6	0.244		0.260	
L9	3.5		3.93	0.137		0.154	
DIA.	3.75		3.85	0.147		0.151	



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