



FMMT718

Features

- Halogen free available upon request by adding suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix designates RoHS Compliant. See ordering information)
- Extremely Low Saturation Voltage
- Epoxy meets UL 94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Marking: 718

Maximum Ratings

Symbol	Rating	Rating	Unit
V_{CEO}	Collector-Emitter Voltage	-20	V
V_{CBO}	Collector-Base Voltage	-20	V
V_{EBO}	Emitter-Base Voltage	-7.0	V
I_C	Collector Current-Continuous	-1.5	A
I_B	Base Current	-0.5	A
$R_{\theta JA}$	Thermal Resistance From Junction to Ambient	357	$^{\circ}C/W$
P_C	Power Dissipation	350	mW
T_J	Operating Junction Temperature	-55 to +150	$^{\circ}C$
T_{STG}	Storage Temperature	-55 to +150	$^{\circ}C$

Electrical Characteristics @ 25°C Unless Otherwise Specified

Symbol	Parameter	Min	Max	Units
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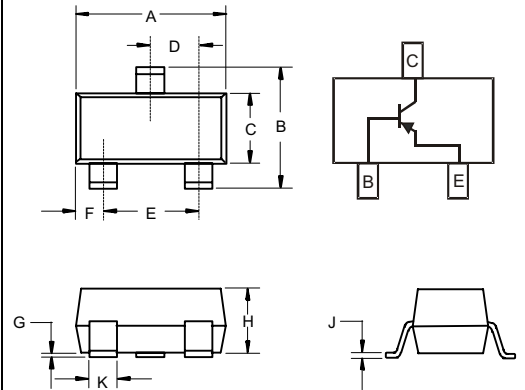
OFF CHARACTERISTICS

$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage* ($I_C=-10mA$, $I_B=0$)	-20	---	Vdc
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage ($I_C=-100\mu A$, $I_E=0$)	-20	---	Vdc
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage ($I_E=-100\mu A$, $I_C=0$)	-7.0	---	Vdc
I_{CBO}	Collector Cutoff Current ($V_{CB}=-15Vdc$, $I_E=0Vdc$)	---	-0.1	μA
I_{EBO}	Emitter Cutoff Current ($V_{EB}=-4.0Vdc$, $I_C=0$)	---	-0.1	μA
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage * ($I_C=-10mA$, $I_B=-10mA$) ($I_C=-1A$, $I_B=-20mA$) ($I_C=-1.5A$, $I_B=-50mA$)	---	-0.04 -0.20 -0.22	Vdc
$V_{BE(sat)}$	Base-Emitter Saturation Voltage* ($I_C=-1.5A$, $I_B=-50mA$)	---	-1.0	Vdc
$V_{BE(ON)}$	Base-Emitter Voltage* ($V_{CE}=-2Vdc$, $I_C=-2.0A$)	---	-1.0	Vdc
h_{FE}	DC Current Gain * ($V_{CE}=-2Vdc$, $I_C=-10mA$) ($V_{CE}=-2Vdc$, $I_C=-100mA$) ($V_{CE}=-2Vdc$, $I_C=-2A$) ($V_{CE}=-2Vdc$, $I_C=-4A$)	300 300 150 35	---	---
f_T	Transition Frequency ($I_C=-50mA$, $V_{CE}=-10Vdc$, $f=100MHz$)	150	---	MHz
$t_{(on)}$ $t_{(off)}$	Turn-on Time Turn-off Time ($V_{CC}=-10Vdc$, $I_C=-1A$, $I_{B1}=I_{B2}=-20mA$)		40(typ) 670(typ)	ns ns
C_{ob}	Output Capacitance ($V_{CB}=-10Vdc$, $f=1.0MHz$)	---	30	pF

*. Pulse test: Pul width $\leq 300\mu s$, duty cycle $\leq 2.0\%$.

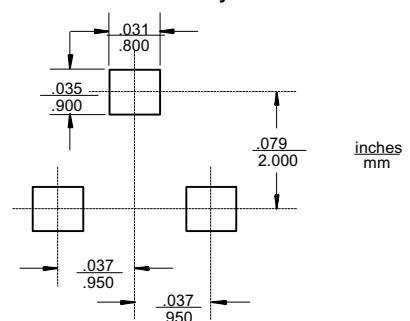
PNP Silicon Planar High Performance Transistor

SOT-23

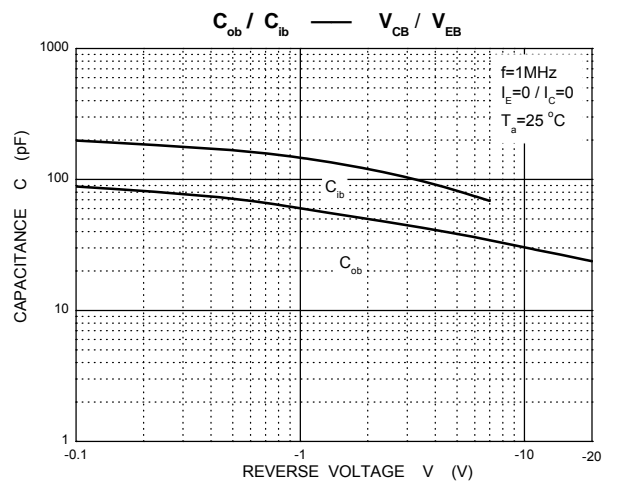
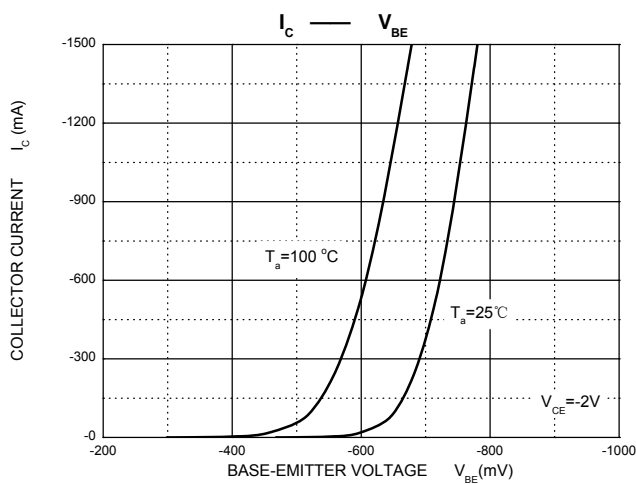
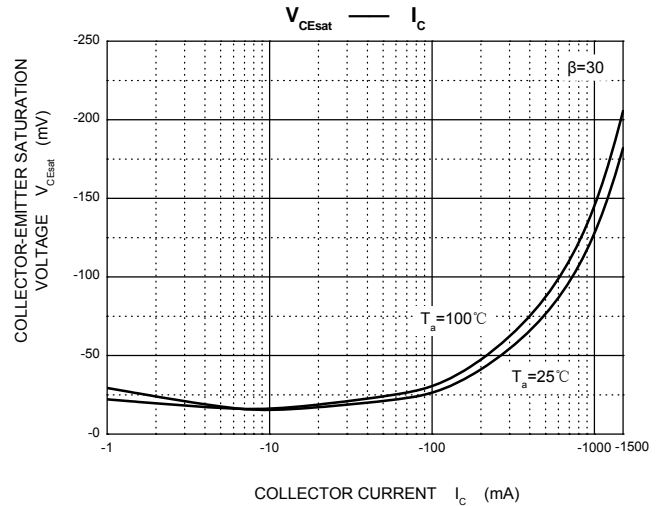
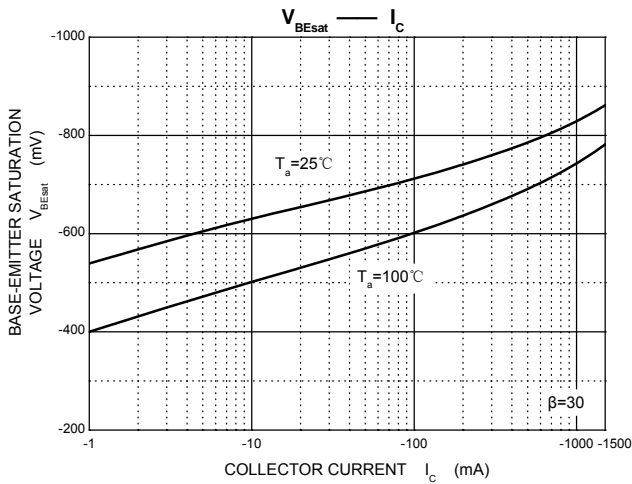
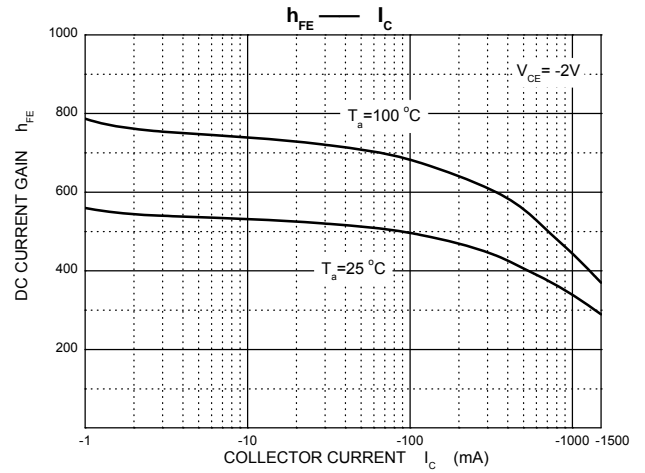
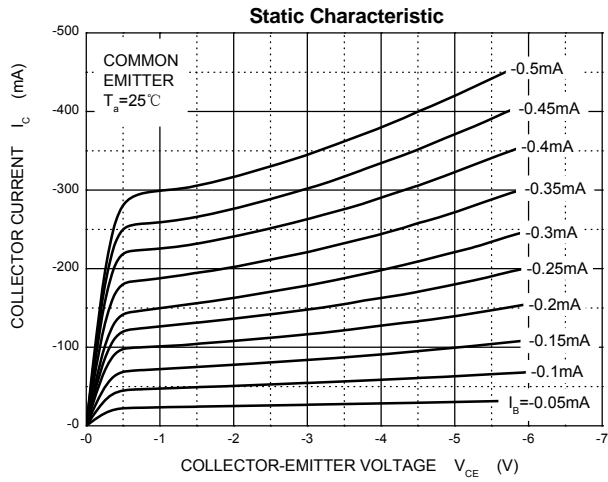


DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.110	.120	2.80	3.04	
B	.083	.104	2.10	2.64	
C	.047	.055	1.20	1.40	
D	.035	.041	.89	1.03	
E	.070	.081	1.78	2.05	
F	.018	.024	.45	.60	
G	.0005	.0039	.013	.100	
H	.035	.044	.89	1.12	
J	.003	.007	.085	.180	
K	.015	.020	.37	.51	

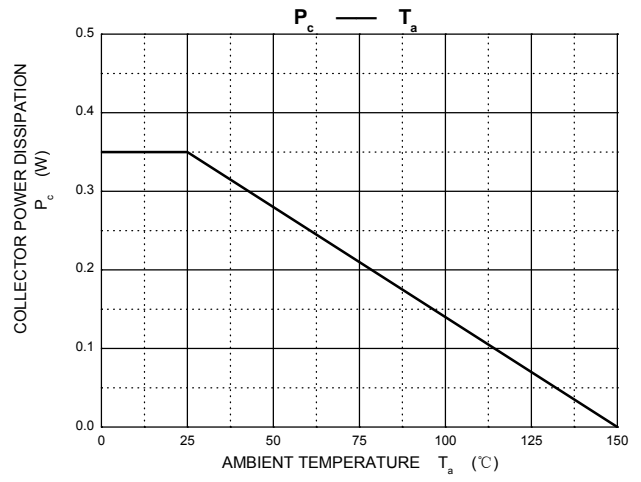
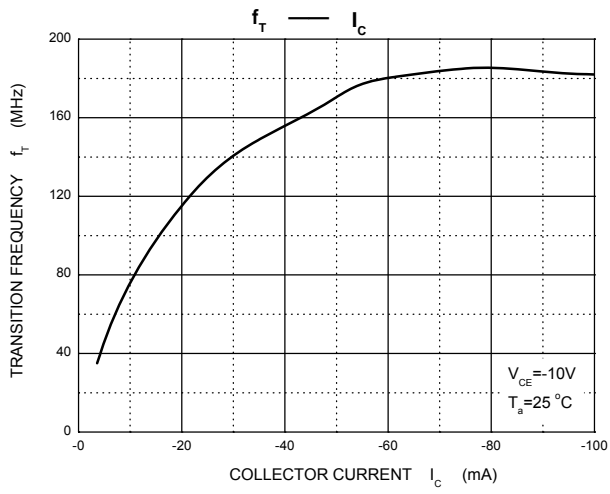
Suggested Solder Pad Layout



Typical Characteristics



Typical Characteristics





Micro Commercial Components

Ordering Information :

Device	Packing
Part Number-TP	Tape&Reel; 3Kpcs/Reel

Note : Adding "-HF" suffix for halogen free, eg. Part Number-TP-HF

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