



PRODUCT DATA SHEET



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Datasheet

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Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.



General Description

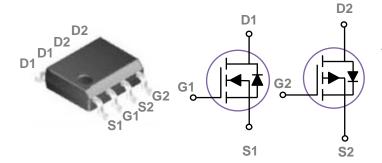
SOP8 Pin Configuration

These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
30V	16m Ω	7A
-30V	25m $Ω$	-6.5A

Features

- Fast switching
- Green Device Available
- Suit for 4.5V Gate Drive Applications



Applications

- DC Fan
- Motor Drive Applications
- Networking
- Half / Full Bridge Topology

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Symbol Parameter		Rating		
V _{DS}	Drain-Source Voltage	30	-30	V	
V _G S	Gate-Source Voltage	±20	±20	V	
lo.	Drain Current – Continuous (T _C =25°C)	7	-6.5	Α	
lD	Drain Current – Continuous (Tc=100°C)	5	-4.5	Α	
I _{DM}	Drain Current – Pulsed1	32	-22	Α	
P _D	Power Dissipation (Tc=25°C)	2.	2.5		
FU	Power Dissipation – Derate above 25°C	0.0)2	W/°C	
T _{STG}	Storage Temperature Range	-55 to 150		°C	
T_J	Operating Junction Temperature Range	-55 to	125	°C	

Thermal Characteristics

Symbol	Symbol Parameter		Max.	Unit
Reja	Thermal Resistance Junction to ambient		62.5	°C/W
Rejc	Thermal Resistance Junction to Case		50	°C/W



N-CH Electrical Characteristics (T_J=25 °C, unless otherwise) noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	BV _{DSS} Drain-Source Breakdown Voltage V _{GS} =0V , I _D =250uA		30			V
1	Drain Source Leakage Current	V _{DS} =30V , V _{GS} =0V , T _J =25°C			1	uA
IDSS	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =125°C			10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA

On Characteristics

Rosioni Static Drain-Source On-Resistance		V_{GS} =10V , I_D =7A		16	25	mΩ
1.56(6.1)	V _{GS} =4.5V , I _D =5A		23	35	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	\/\/\	1.0	1.5	2.5	V
$\Delta V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, I_{D} =250uA		-4		mV/°C
gfs	Forward Transconductance	V _{DS} =10V , I _D =3A		3		S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3,4}		 4.1	6	
Q_{gs}	Gate-Source Charge ^{3, 4}	V _{DS} =15V , V _{GS} =4.5V , I _D =8A	 1	1.4	nC
Q_{gd}	Gate-Drain Charge ^{3, 4}		 2.1	4	
T _{d(on)}	Turn-On Delay Time ^{3, 4}		 2.8	5	
Tr	Rise Time ^{3, 4}	V_{DD} =15V , V_{GS} =10V , R_{G} =6 Ω	 7.2	14	20
T _{d(off)}	Turn-Off Delay Time ^{3, 4}	I _D =1A	 15.8	30	ns
T _f	Fall Time ^{3, 4}		 4.6	9	
C _{iss}	Input Capacitance		 345	500	
Coss	Output Capacitance	V _{DS} =25V , V _{GS} =0V , F=1MHz	 55	80	pF
C _{rss}	Reverse Transfer Capacitance		 32	55	
Rg	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	 3.2	6.4	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions		Тур.	Max.	Unit
Is	Continuous Source Current	Va_Va_OV Force Current			7	Α
Isм	Pulsed Source Current	V _G =V _D =0V , Force Current			14	Α
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A , T _J =25°C			1.3	V

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. $V_{DD}=25V$, $V_{GS}=10V$, L=0.1 mH, $I_{AS}=17A$., $R_{G}=25\Omega$, Starting $T_{J}=25^{\circ}C$.
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.



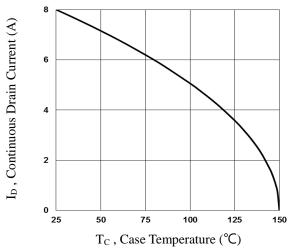


Fig.1 Continuous Drain Current vs. T_c

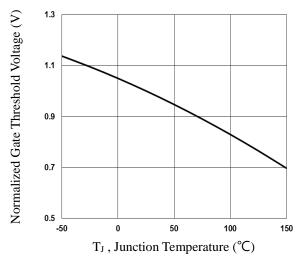


Fig.3 Normalized V_{th} vs. T_J

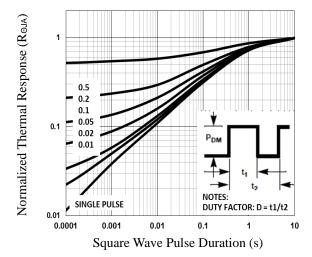


Fig.5 Normalized Transient Response

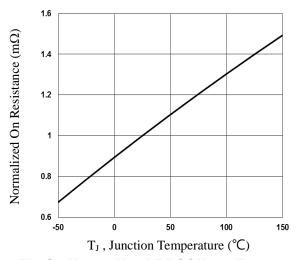


Fig.2 Normalized RDSON vs. TJ

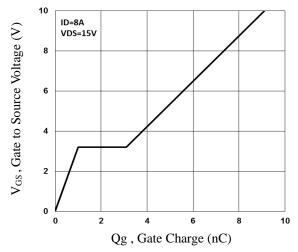


Fig.4 Gate Charge Waveform

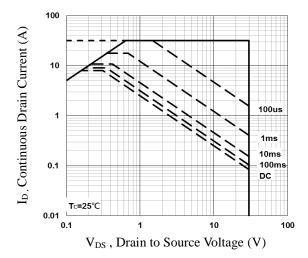


Fig.6 Maximum Safe Operation Area



P-CH Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D = - 250uA				V
△BV _{DSS} /△T _J	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =-1mA		-0.03		V/°C
I	Drain Source Leakage Current	V _{DS} =-30V , V _{GS} =0V , T _J =25°C			-1	uA
IDSS	Drain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =125°C			-10	uA
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$			±100	nA

On Characteristics

Present	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-6A		25	35	mΩ
1.20(0.1)	V _{GS} = - 4.5V , I _D = - 3A		35	55	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA		- 1.6	- 2.5	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient			4		mV/°C
gfs	Forward Transconductance	V _{DS} =-10V , I _D =-3A		3.5		S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{7,8}		 5.1	7	
Q_{gs}	Gate-Source Charge ^{7,8}	V _{DS} =-15V , V _{GS} =-4.5V , I _D =-3A	 2	3	nC
Q_{gd}	Gate-Drain Charge ^{7,8}		 2.2	4	
$T_{d(on)}$	Turn-On Delay Time ^{7,8}		 3.4	6	
Tr	Rise Time ^{7,8}	V_{DD} =-15V , V_{GS} =-10V , R_G =6 Ω	 10.8	21	20
$T_{d(off)}$	Turn-Off Delay Time ^{7,8}	I _D =-1A	 26.9	51	ns
T _f	Fall Time ^{7,8}		 6.9	13	
Ciss	Input Capacitance		 560	810	
C_{oss}	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , F=1MHz	 55	80	pF
Crss	Reverse Transfer Capacitance		 40	60	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V- V- OV Force Current			- 6.5	Α
Ism	Pulsed Source Current	V _G =V _D =0V , Force Current			- 13	Α
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-1A , T _J =25°C			-1.3	V

Note

- 5. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 6. V_{DD} =-25V, V_{GS} =-10V,L=0.1mH, I_{AS} =-10A., R_{G} =25 Ω , Starting T_{J} =25 $^{\circ}$ C
- 7. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 8. Essentially independent of operating temperature.

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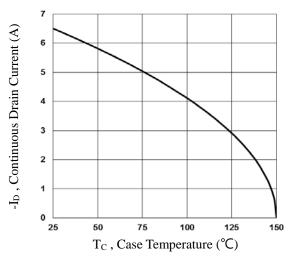


Fig.1 Continuous Drain Current vs. Tc

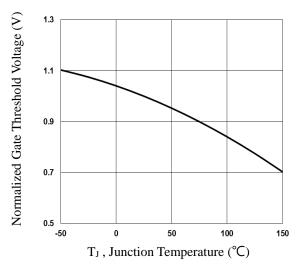


Fig.3 Normalized V_{th} vs. T_J

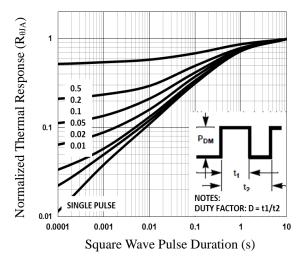


Fig.5 Normalized Transient Impedance

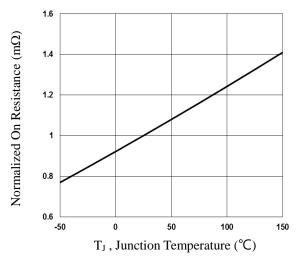


Fig.2 Normalized RDSON vs. TJ

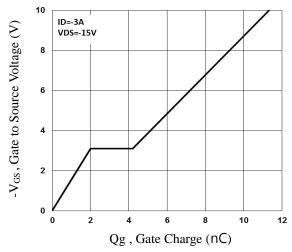


Fig.4 Gate Charge Waveform

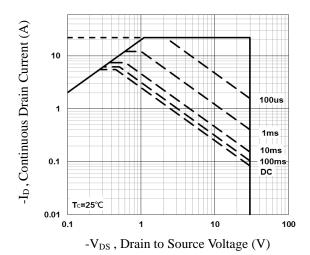
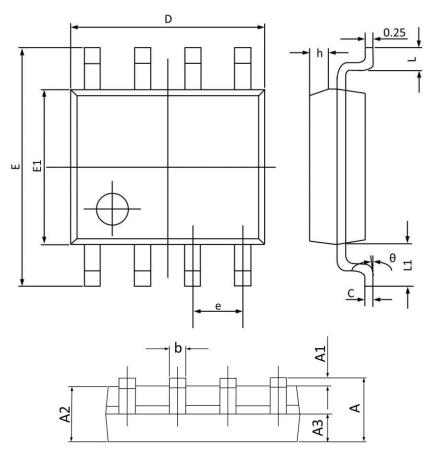


Fig.6 Maximum Safe Operation Area



SOP8 PACKAGE INFORMATION



Crumbal	Dimensions I	n Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.250	1.650	0.049	0.065
A3	0.500	0.700	0.020	0.028
b	0.380	0.510	0.015	0.020
c	0.170	0.260	0.007	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.700	4.100	0.146	0.161
e	1.270	(BSC)	0.050	(BSC)
h	0.250	0.500	0.010	0.020
L	0.400	0.800	0.016	0.031
L1	1.050	(BSC)	0.041(BSC)	
θ	0°	8°	0°	8°



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