

## PRODUCT DATA SHEET



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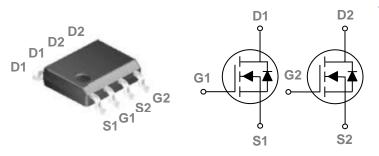
Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO\_questions@jgsemi.com.

# JG Techology

#### **General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

#### **SOP8 Dual Pin Configuration**



BVDSS	RDSON	ID
60V	$28 \text{m}\Omega$	6A

#### **Features**

- 60V,6A,RDS(ON) =28mΩ@VGS = 10V
- Excellent CdV/dt effect decline
- Super Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available

#### **Applications**

- Motor Drive
- Power Tools
- LED Lighting

#### Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	60	V
Vgs	Gate-Source Voltage	±20	V
	Drain Current – Continuous (T <sub>A</sub> =25°C)	6	A
ID	Drain Current – Continuous (T <sub>A</sub> =70°C)	4.5	A
Ідм	Drain Current – Pulsed <sup>1</sup>	22	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	22	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	23	A
D	Power Dissipation (T <sub>A</sub> =25°C)	1.5	W
Po	Power Dissipation – Derate above 25°C	0.016	W/°C
Тѕтс	Storage Temperature Range	-50 to 150	°C
TJ	Operating Junction Temperature Range	-50 to 125	°C

#### **Thermal Characteristics**

Symbol	ol Parameter		Max.	Unit
R <sub>0JA</sub>	Thermal Resistance Junction to ambient		85	°C/W

## **IRF7341**



#### Electrical Characteristics (TJ=25 °C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	60			V
	Drain-Source Leakage Current	V <sub>DS</sub> =60V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
IDSS		V <sub>DS</sub> =42V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm20V$ , $V_{DS}=0V$			±100	nA

#### **On Characteristics**

R <sub>DS(ON)</sub> Static Drain-Source On-Resistance	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =8A	28	28	40	mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =4A		36	50	mΩ
V <sub>GS</sub> (th)	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	1.6	2.5	V
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =1A		5		S

#### **Dynamic and switching Characteristics**

Qg	Total Gate Charge <sup>2,3</sup>		 20.3	
Q <sub>gs</sub>	Gate-Source Charge <sup>2,3</sup>	V <sub>DS</sub> =30V , V <sub>GS</sub> =10V , I <sub>D</sub> =2.5A	 3.7	 nC
Q <sub>gd</sub>	Gate-Drain Charge <sup>2,3</sup>		 5.3	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2 , 3</sup>		 7.6	
Tr	Rise Time <sup>2 , 3</sup>	$V_{DD}$ =15V, $V_{GS}$ =10V , $R_G$ =3.3 $\Omega$	 20	 
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2,3</sup>	I <sub>D</sub> =1A	 15	 ns
Tf	Fall Time <sup>2,3</sup>		 25	
Ciss	Input Capacitance		 1148	
Coss	Output Capacitance	$V_{DS}$ =25V , $V_{GS}$ =0V , F=1MHz	 58.5	 pF
Crss	Reverse Transfer Capacitance		 49.4	
Rg	Gate resistance	$V_{GS}$ =0V, $V_{DS}$ =0V, F=1MHz	 2.6	 Ω

#### **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			5	А
I <sub>SM</sub>	Pulsed Source Current	vg=vD=0v, Force Current			20	А
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , Is=1A , TJ=25°C			1.2	V

Note :

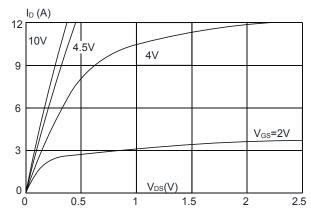
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : TJ=25 °C ,V DD=30V,VG=10V, L=0 .5mH,Rg =25Ω,IAS=8.7A

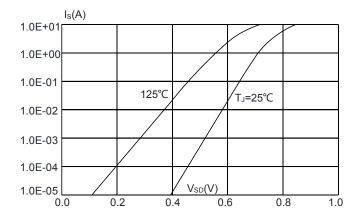
3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



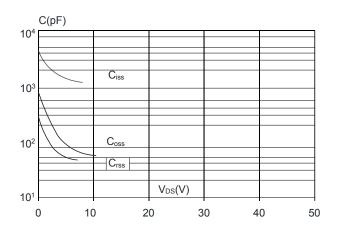
## **IRF7341**



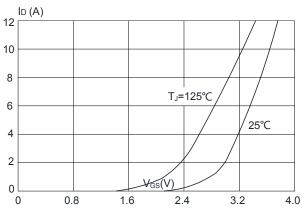
#### Fig.1 Output Characteristics



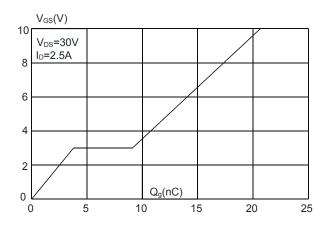
#### Fig.3 Body Diode Characteristics



#### Fig.5 Normalized Transient Impedance



#### Fig.2 Typical Transfer Characteristics



#### Fig.4 Gate Charge Characteristics

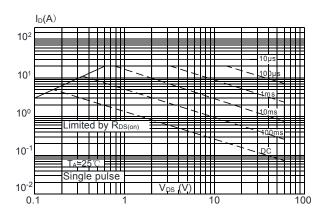
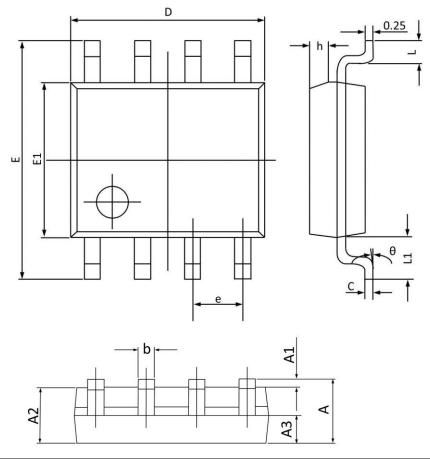


Fig.6 Maximum Safe Operation Area



# SOP8 PACKAGE INFORMATION



Symbol	Dimensions l	In Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
Α	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
A3	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
с	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
<b>E</b> 1	3.700	4.100	0.146	0.161
e	1.270	1.270(BSC)		(BSC)
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050	(BSC)	0.041	(BSC)
θ	0°	<b>8°</b>	<b>0°</b>	<b>8</b> °



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