



# **PRODUCT DATA SHEET**



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Datasheet Re

Sample

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO\_questions@jgsemi.com.



### **General Description**

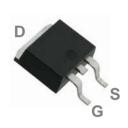
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

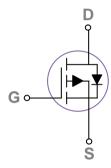
BVDSS	RDSON	ID
-55V	110mΩ	-8A

#### **Features**

- -55V, -8A,  $RDS(ON) = 110m\Omega@VGS = -10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

### **TO252 Pin Configuration**





# **Applications**

- Motor Drive
- Power Tools
- LED Lighting

### Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-55	V
$V_{GS}$	Gate-Source Voltage	±20	V
	Drain Current – Continuous (T <sub>C</sub> =25°C)	-8	А
ID	Drain Current – Continuous (T <sub>C</sub> =100°C)	-4.4	А
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	-28	А
EAS	Single Pulse Avalanche Energy <sup>2</sup>	32	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	-8	А
D	Power Dissipation (T <sub>C</sub> =25°C)	15.6	W
$P_D$	Power Dissipation – Derate above 25°C	0.125	W/°C
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C
TJ	Operating Junction Temperature Range	-50 to 125	°C

### **Thermal Characteristics**

Symbol Parameter		Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case		8	°C/W



# Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise

# noted) Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-55			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =-1mA		-0.05		V/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =-55V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}$ C			-1	uA
	Diam-Source Leakage Current	V <sub>DS</sub> =-45V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$			±10	uA

#### **On Characteristics**

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-10V , I <sub>D</sub> =-4.5A		110	150	mΩ
	Static Dialif-Source Off-Resistance	$V_{GS}$ =-4.5V , $I_{D}$ =-4.5A		155	180	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA		-2.1	-3.0	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient			5		mV/°C
gfs	Forward Transconductance	V <sub>DS</sub> =-10V , I <sub>D</sub> =-4.5A		3		S

## **Dynamic and switching Characteristics**

•	3			
$Q_g$	Total Gate Charge <sup>3, 4</sup>		 12	
$Q_gs$	Gate-Source Charge <sup>3, 4</sup>	$V_{DS}$ =-25V , $V_{GS}$ =-5V , $I_{D}$ =-4.5A	 2.2	 nC
$Q_{gd}$	Gate-Drain Charge <sup>3,4</sup>		 2.2	
$T_{d(on)}$	Turn-On Delay Time <sup>3,4</sup>		 8	
T <sub>r</sub>	Rise Time <sup>3,4</sup>	$V_{DD}$ =-25 $V$ , $V_{GS}$ =-10 $V$ , $R_{G}$ =10 $\Omega$	 8	 no
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>	I <sub>D</sub> =-2A	 35	 ns
$T_f$	Fall Time <sup>3, 4</sup>		 8	
C <sub>iss</sub>	Input Capacitance		 560	
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ =-10V , $V_{GS}$ =0V , F=1MHz	 90	 pF
$C_{rss}$	Reverse Transfer Capacitance		 45	

# **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions		Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			-10	Α
I <sub>SM</sub>	Pulsed Source Current	V <sub>G</sub> =V <sub>D</sub> =UV , Force Current			20	Α
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-4.5A , T <sub>J</sub> =25°C			-1.2	V

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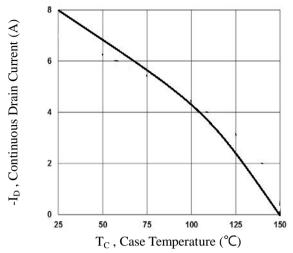


Fig.1 Continuous Drain Current vs. T<sub>c</sub>

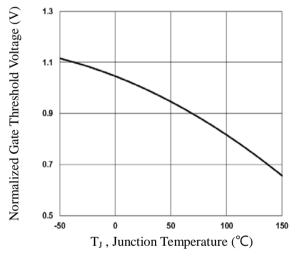


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

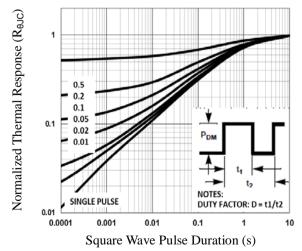


Fig.5 Normalized Transient Impedance

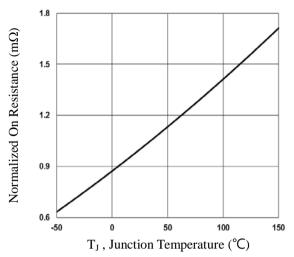


Fig.2 Normalized RDSON vs. T<sub>J</sub>

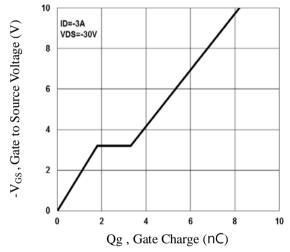


Fig.4 Gate Charge Waveform

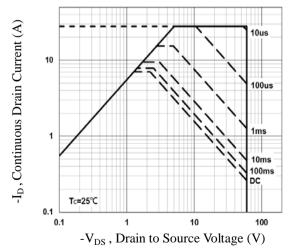
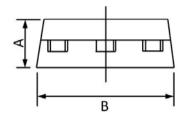
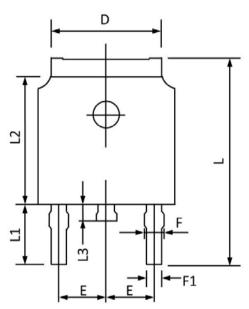


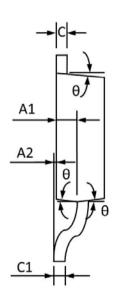
Fig.6 Maximum Safe Operation Area



# **TO252 PACKAGE INFORMATION**







Crumbal	Dimensions 1	In Millimeters	Dimension	s In Inches	
Symbol	Min	Max	Min	Max	
A	2.20	2.40	0.087	0.094	
<b>A1</b>	0.91	1.11	0.036	0.044	
A2	0.00	0.15	0.000	0.006	
В	6.50	6.70	0.256	0.264	
С	0.46	0.580	0.018	0.230	
<b>C</b> 1	0.46	0.580	0.018	0.030	
D	5.10	5.46	0.201	0.215	
E	2.186	2.386	0.086	0.094	
F	0.74	0.94	0.029	0.037	
F1	0.660	0.860	0.026	0.034	
L	9.80	10.40	0.386	0.409	
L1	2.9	REF	0.114REF		
L2	6.00	6.20	0.236	0.244	
L3	0.60	1.00	0.024	0.039	
θ	<b>3</b> °	<b>9</b> °	<b>3</b> °	<b>9</b> °	



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