

### **General Description**

The IRLR120NTRPBF use advanced SGT MOSFET technology

to provide low RDS(ON), low gate charge, fast switching

and excellent avalanche characteristics.

This device is specially designed to get better ruggedness

and suitable to use in.



TO-252-2L

#### **General Features**

 $V_{DS} = 100V I_{D} = 12 A$ 

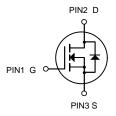
 $R_{DS(ON)}$  < 120m $\Omega$  @  $V_{GS}$ =10V

### **Applications**

Consumer electronic power supply

Motor control

Synchronous-rectification



N-Channel MOSFET

### **Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
IRLR120NTRPBF	TO-252-2L	12N10 XXXXX	2500

## **Absolute Maximum Ratings** at $T_j$ =25°C unless otherwise noted

Symbol	Parameter	Value	Unit
VDS	Drain source voltage	100	V
VGS	Gate source voltage	±20	V
ID	Continuous drain current <sup>1)</sup>	12	А
ID, pulse	Pulsed drain current <sup>2)</sup>	24	А
P <sub>D</sub>	Power dissipation <sup>3)</sup>	17	W
EAS	Single pulsed avalanche energy <sup>4)</sup>	1.2	mJ
Tstg, Tj	Operation and storage temperature	-55 to 150	°C
RθJC	Thermal resistance, junction-case	6.6	°C/W
RÐJA	Thermal resistance, junction-ambient <sup>5)</sup>	62	°C/W



# **Electrical Characteristics** Tc=25℃ unless otherwise specified

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
Off Chara	cteristic					•
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250µA	100	110	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	-	-	1	μA
I <sub>GSS</sub>	Gate to Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA
On Chara	cteristics note3					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0	1.8	3.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance note2	V <sub>GS</sub> = 10V, I <sub>D</sub> = 3A	-	95	120	mΩ
Dynamic	Characteristics note4			ı	l .	l.
Ciss	Input Capacitance	., 50/// 0//	-	196	-	pF
Coss	Output Capacitance	$V_{DS} = 50V, V_{GS} = 0V,$ f = 1.0MHz	-	25.9	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	21.4	-	pF
Qg	Total Gate Charge	V <sub>DS</sub> = 50V, I <sub>D</sub> = 3A,	-	4.3	-	nC
Qgs	Gate-Source Charge		-	3.5	-	nC
Q <sub>gd</sub>	Gate-Drain("Miller") Charge	V <sub>GS</sub> = 10V	-	3.1	-	nC
Switching	Characteristics note4					
t <sub>d(on)</sub>	Turn-On Delay Time	., 50,/ 1 0.4	-	14.7	-	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{DD} = 50V$ , $I_{DS} = 3A$	-	3.5	-	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	$R_G = 2\Omega$ , $V_{GEN} = 10V$	-	20.9	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	2.7	-	ns
Drain-Sou	rce Diode Characteristics and Maximum Rati	ngs		ı	l .	l.
ls	Maximum Continuous Drain to Source Diode Forward Current note2		-	-	4.5	Α
Ism	Maximum Pulsed Drain to Source Diode Forward Current		-	-	12	Α
V <sub>SD</sub>	Drain to Source Diode Forward Voltage note3	V <sub>GS</sub> = 0V, I <sub>S</sub> =3A	-	-	1.3	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time	\/ - 0\/ I - 2A	-	32.1	-	ns
Qrr	Body Diode Reverse Recovery Time Charge	$V_{GS} = 0V, I_F = 3A,$	-	39.4	-	nC
I <sub>rrm</sub>	Peak Reverse Recovery Current	- di/dt =100A/μs	-	2.1	-	Α

#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤2%.
- 4. Guaranteed by design, not subject to production
- 5.  $V_{DD}$ =50 V, R<sub>G</sub>=50  $\Omega$ , L=0.3 mH, starting T<sub>j</sub>=25 °C



# **Typical Performance Characteristics**

Figure1: Output Characteristics

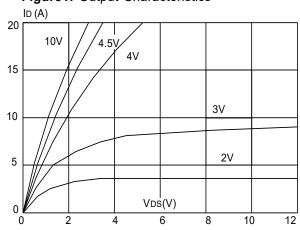


Figure 2: Typical Transfer Characteristics

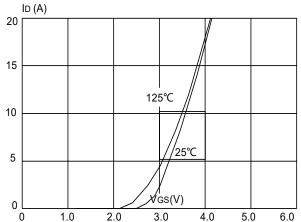


Figure 3:On-resistance vs. Drain Current

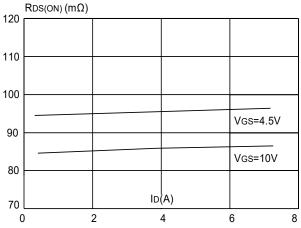


Figure 4: Body Diode Characteristics

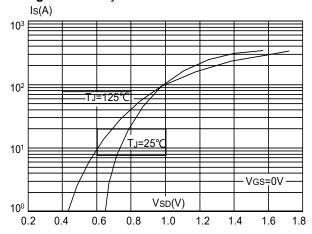
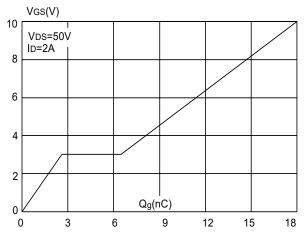
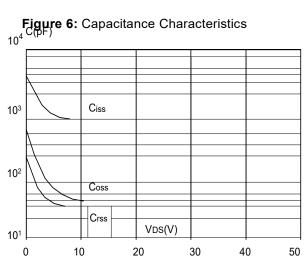


Figure 5: Gate Charge Characteristics







**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

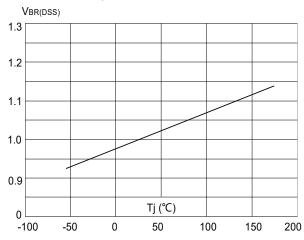
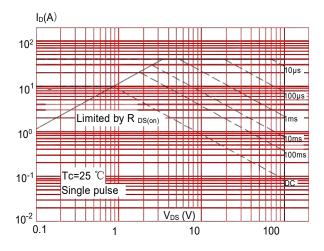
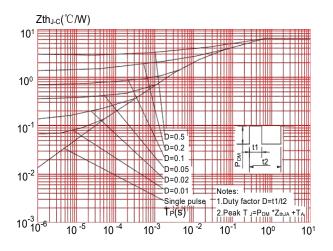


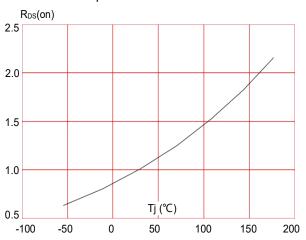
Figure 9: Maximum Safe Operating Area



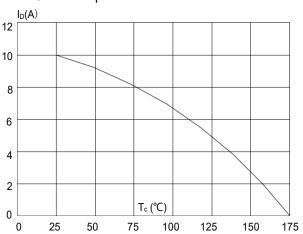
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



**Figure 8:** Normalized on Resistance vs. Junction Temperature

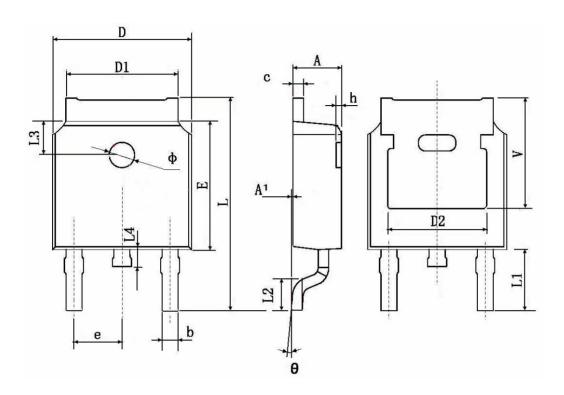


**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature





# **TO-252-2L Package Information**



Comple at	Dimensions In Millimeters		Dimensions In Inches			
Symbol	Min.	Max.	Min.	Max.		
А	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	0.483 TYP.		0.190 TYP.			
E	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.900	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067		
L3	1.600	TYP.	0.063 TYP.			
L4	0.600	1.000	0.024	0.039		
Ф	1.100	1.300	0.043	0.051		
θ	0°	8°	0°	8°		
h	0.000	0.300	0.000	0.012		
V	5.350	TYP.	0.211 TYP.			



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