

TRIAC series

1 Description

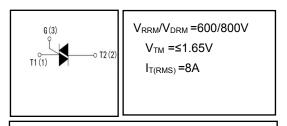
BT137 series triacs with low holding and latchingcurrent are especially recommended for use onmiddle and small resistance type power load.

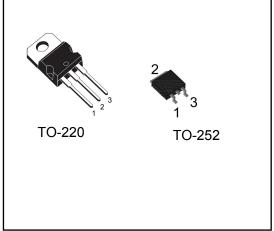
2 Features

- High current output up to 8A
- Low Peak on-state voltage drop
- High voltage
- High reliability

3 Applications

- jet pumps of dishwashers
- fans of air-conditioner
- power charger
- AC Motor control





4 Electrical Characteristics

4.1 Absolute Maximum Ratings (Tc=25 °C,unless otherwise noted)

PARAMETER			UNIT	
Repetitive peak off-state voltage (Tj=25°C)		600/800	V	
epetitive peak reverse voltage (Tj=25°C)		600/800	V	
	V _{DSM}	+ 100	V	
	V_{RSM}	+ 100	V	
	I _{T(RMS)}	8	Α	
tp=8.3ms		84	Α	
tp=10ms	- I _{TSM}	80		
I ² t value for fusing (tp=10ms)			Α	
	d _{IT/dt}	50	A/us	
Peak gate current		1	Α	
Peak gate power		5	W	
Average gate power dissipation		0.5	W	
Operating junction temperature range		- 40 ~ 150	$^{\circ}\mathbb{C}$	
Storage junction temperature range		- 40 ~ 150	$^{\circ}$ C	
		V _{RSM} I _{T(RMS)} tp=8.3ms tp=10ms I _{TSM} I ² t	VDRM 600/800 VRRM 600/800 VDSM + 100 VRSM + 100 IT(RMS) 8 tp=8.3ms 84 tp=10ms 80 I2t 21 dIT/dt 50 IGM 1 PGM 5 PG(AV) 0.5 TJ - 40 ~ 150	

4.2 Thermal Characteristics

PARAMETER	SYMBOL	VAI	LUE	UNIT	
PARAIVIETER	STWIDOL	TO-220	TO-252	CIVIT	
Thermal Resistance, Junction to Case-sink	R _{thJC}	3.0	3.7	°C/W	



4.3 Electrical Characteristics	(Tc=25 [°] C,unless otherwise noted)
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SYMBOL	PARAMETER	Test Conditions		Min	Тур	Max	Unit
			I - II -III	-	7.5	10	
I _{GT}	Triggering gate current	V_D =12V R _L =33 Ω	IV	-	-	-	mA
V_{GT}	Triggering gate voltage		ALL	-	0.85	1.5	V
V_{GD}	Non-triggering gate voltage	$V_D = V_{DRM} T_j = 125 ^{\circ} CR_L = 3.3 K\Omega$		0.2	-	-	V
			I -III	-	-	15	
I∟	Latching Current	I _G =1.2I _{GT}	II	-	-	20	mA
I _H	Holding Current	I _T =100mA		-	5.8	15	mA
d _{V/dt}	Critical Rate of Rise of Off-state Voltage	V _D =2/3V _{DRM} Gate Open T _j =125℃		50	-	-	V/us
V _{TM}	Peak Forward On-State Voltage	I _{TM} =10A tp=380us		-	1.35	1.65	V
I _{DRM}	Maximum forward or reverse leakage current		Tj=25℃	-	-	10	uA
I _{RRM}	Maximum reverse leakage current	$V_D = V_{DRM} V_R = V_{RRM}$	Tj=125℃	-	-	500	uA

5 Typical characteristics diagrams

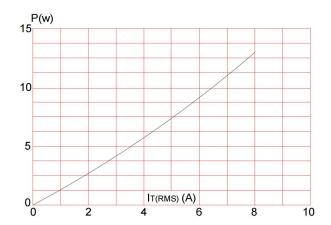


FIG.1: Maximum power dissipation versus RMS on-state current

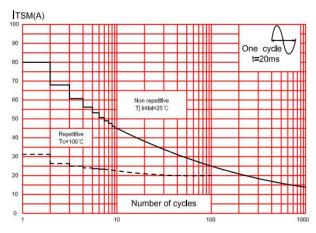


FIG.3: Surge peak on-state current versus number of cycles

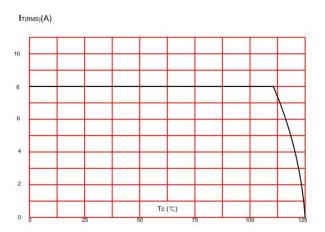


FIG.2: RMS on-state current versus case temperature

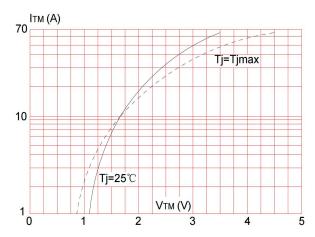
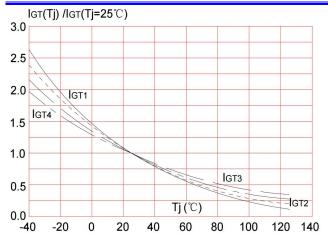


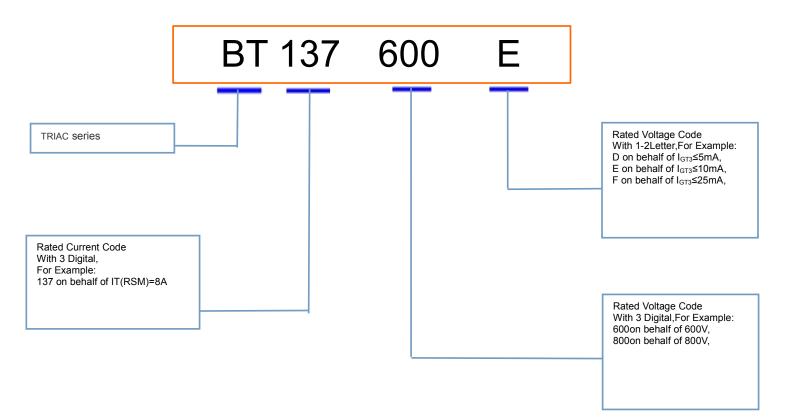
FIG.4: On-state characteristics (maximum values)





and latching current versus junction temperature

6 Product Names Rules



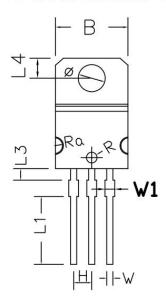
7 Product Specifications and Packaging Models

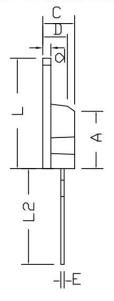
Product Model	Package Type	Mark Name	RoHS	Package	Quantity
BT137	TO-220	BT137	Pb-free	Tube	1000//box
BT137	TO-252	BT137	Pb-free	Braid	3000//disc



8 Dimensions

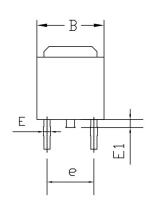
TO-220M PACKAGE OUTLINE DIMENSIONS

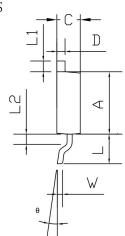




Symbol	Dimensions	In Millimeters	Dimensions	In Inches
Symbol	min.	max.	min.	max.
	MIN	MAX	MIN	MAX
A	8. 03	8.05	0. 316	0.317
В	10. 13	10. 23	0.399	0. 403
С	4. 42	4. 52	0. 174	0. 178
D	3. 42	3. 52	0. 135	0. 139
Е	0.44	0.46	0.017	0.018
L	15. 25	15. 45	0.601	0.609
Н	2. 52	2. 56	0.099	0. 101
W	0.85	0.87	0.033	0.034
Φ	3.78	3. 82	0.149	0. 151
R	0.74	0.76	0.029	0.030
Ra	9.44	9.48	0.372	0.374
d	1. 28	1.32	0.050	0.052
L1	9. 4	9.6	0.370	0.378
L2	13. 22	13.62	0. 521	0. 537
L3	1. 52	1.72	0.060	0.068
L4	2.7	2.9	0. 106	0.114
W1	1.32	1.42	0. 052	0.056

TO-252 PACKAGE OUTLINE DIMENSIONS





C	DimensionsIn Millimeters		DimensionsIn Inches	
Symbol	min.	max.	min.	max.
А	5.70	6.30	0.224	0.248
В	6.30	6.90	0.248	0.272
С	2.05	2.55	0.081	0.100
D	0.70	0.90	0.028	0.035
E	0.40	0.60	0.016	0.024
E1	0.60	1.00	0.024	0.039
е	4.50	4.65	0.177	0.183
L	2.75	3.05	0.108	0.120
L1	0.75	1.15	0.030	0.045
L2	0.75	1.25	0.030	0.049
W	0.40	0.60	0.016	0.024
θ	0	8	0	8



9 Attentions

- ROUM Semiconductor Technology CO.,LTD. reserves the right to change the specification without prior notice! The customer should obtain the latest version of the information before making the order and verify that the information is complete and up to date.
- It is the responsibility of the purchaser for any failure or failure of any semiconductor product under certain conditions. It is the responsibility of the purchaser to comply with safety standards and to take safety measures in the system design and machine manufacturing of Roma products in order to avoid potential risk of failure. Injury or property damage.
- Product promotion is endless, our company will be dedicated to provide customers with better products.

10 Appendix

Revision history:

Date	REV.	Description	Page
2017.08.14	1.0	Original	