

TDA2030A 22W Hi-Fi AUDIO AMPLIFIER

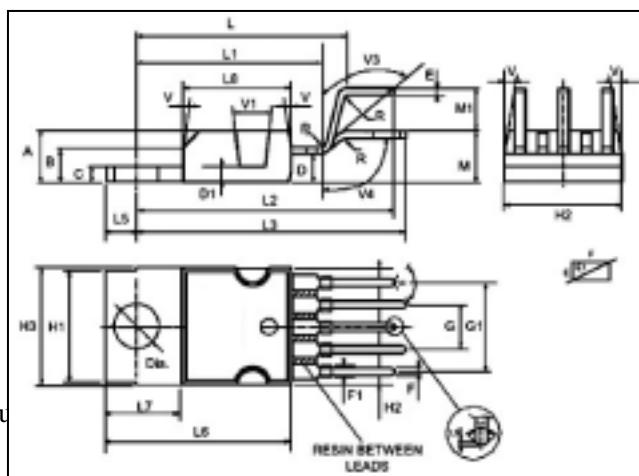
GENERAL DESCRIPTION

The TDA2030A is a monolithic integrated circuit in Pentawatt package, intended for use as a low frequency class AB amplifier. Typically it provides 22W output power ($d = 0.5\%$) at $\pm 18V$ or $36V$

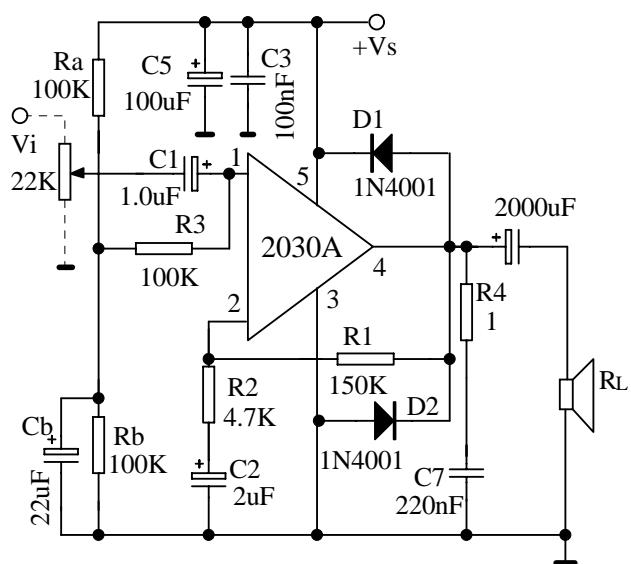
The TDA2030A provides high output current and has very low harmonic and cross-over distortion.

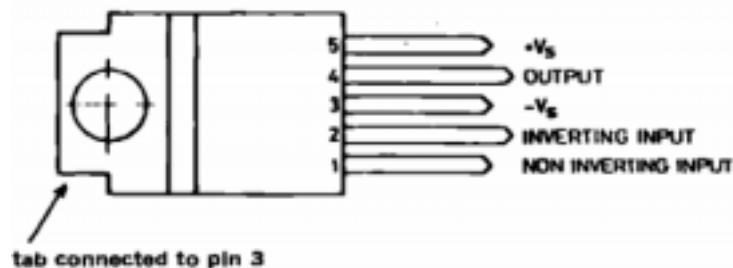
Further the device incorporates an original (and patented) short circuit protection system comprising an arrangement for automatically limiting the dissipated power so as to keep the working point of the output transistors within their safe operating area. A conventional thermal shut-down system is also included.

Outline Drawing



TYPICAL APPLICATION



PIN CONNECTION**ABSOLUTE MAXIMUM RATINGS (Ta=25°C)**

Characteristics	Symbol	Value	Units
Supply Voltage	V _s	±21	V
Input Voltage	V _i	V _s	V
Differential input Voltage	V _{di}	±15	V
Peak output Current (Internally limited)	I _o	3.5	A
Power Dissipation at T _{case} =90°C	P _{tot}	20	W
Storage Temperature	T _{stg}	-40~+150	°C
Junction Temperature	T _j	-40~+150	°C

THERMAL DATA

Characteristics	Symbol	Value	Units
Thermal Resistance Junction-case Max	R _{th(j-case)}	3	°C/W

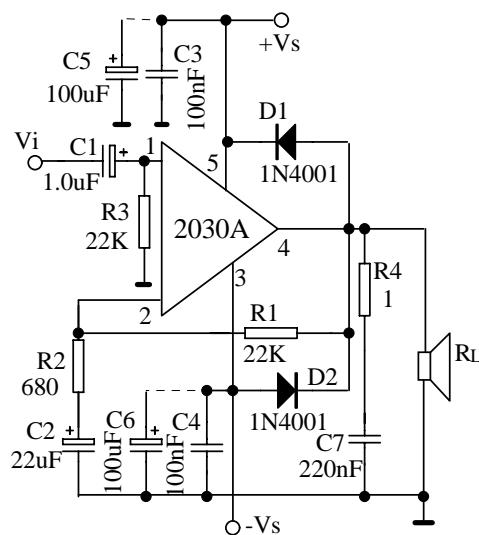
ELECTRICAL CHARACTERISTICS(Refer to the test circuit, V_s=±16V, Ta=25°C, unless otherwise specified)

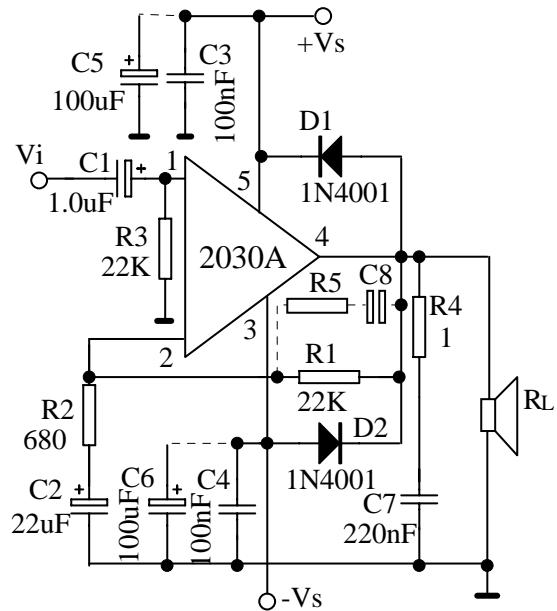
Characteristics	Test conditions	Symbol	Min	Typ	Max	Unit
Supply Voltage		V _s	±6		±21	V
			12		42	
Quiescent Drain Current	Id V _s =±16V(V _s =32V)			50	80	mA
Input Bias Current		I _b		0.2	2	μA
Input Offset Voltage		V _{os}		±2	±20	mA
Input Offset Current		I _{os}		±20	±200	nA

ELECTRICAL CHARACTERISTICS

Continue

Characteristics	Test conditions	Symbol	Min	Typ	Max	Unit
Output Power Vs=±18V	THD=0.5%, Gv=30dB, RL=4Ω, f=40 to 15000Hz	Po	18	22		W
	RL=8Ω		13	15		
	THD=10%, Gv=30dB, f=1kHz			25		
	RL=8Ω			18		
Power Bandwidth (-3dB)	Po=12W, RL=4Ω, Gv=30dB	BW	10~140000			kHz
Open Loop Voltage Gain		Gv		90		dB
Closed Loop Voltage Gain	f=1kHz	Gv	29.5	30	30.5	dB
Total Harmonic Distortion	Po=0.1 to 12W, RL=4Ω, Gv=30dB, f=40 to 15000kHz	THD		0.2	0.5	%
	Po=0.1 to 8W RL=8Ω Gv=30dB, f=40 to 15000kHz			0.1	0.5	
Input Noise Voltage	B=22Hz to 22kHz	e _N		3	10	µV
Input Noise Current		i _N		80	200	pA
Input Resistance (pin1)		Ri	0.5	5		MΩ
Supply Voltage Rejection	RL=4Ω, Rg=22kΩ, Gv=30dB, Vripple=0.5Veff, fripple=100Hz	SVR	40	50		dB
Drain current	Po=14W, RL=4Ω	Id		900		mA
	Po=8W, RL=8Ω			500		

TEST CIRCUIT

APPLICATION CIRCUIT**1. Typical amplifier with split power supply****2. Typical amplifier with single power supply**