# UTC UNISONIC TECHNOLOGIES CO., LTD

# **TDA7052A**

# LINEAR INTEGRATED CIRCUIT

# **1W BTL MONO AUDIO** AMPLIFIER WITH DC VOLUME CONTROL

#### DESCRIPTION

The UTC TDA7052A is mono BTL output amplifier with DC volume control. It is designed for use in TV and monitors, additionally it is suitable for portable recorders and radios.

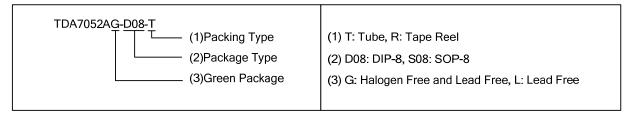
In the IC, a Missing Current Limiter (MCL) is built-in. This function is activated when the difference of current between the OUT+ and OUT- exceed 100mA (typical 300mA). This level of 100mA suit for headphone applications (single-ended).

#### **FEATURES**

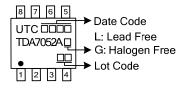
- \* Low power consumption
- \* DC volume control
- \* Mute mode
- \* No switch-on and off clicks
- \* Short-circuit proof
- \* Good overall stability
- \* Low HF radiation
- \* Few external components
- \* Thermal protection
- \* ESD protected on all pins
- \* Missing Current Limiter (MCL)

#### ORDERING INFORMATION

Ordering Number		Daakaga	Dooking	
Lead Free	Halogen Free	Package	Packing	
TDA7052AL-D08-T	TDA7052AG-D08-T	DIP-8	Tube	
TDA7052AL-S08-T	TDA7052AG-S08-T	SOP-8	Tube	
TDA7052AL-S08-R	TDA7052AG-S08-R	SOP-8	Tape Reel	



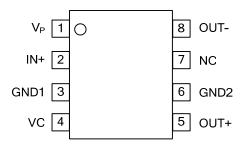
#### **MARKING**



DIP-8 SOP-8

www.unisonic.com.tw 1 of 5

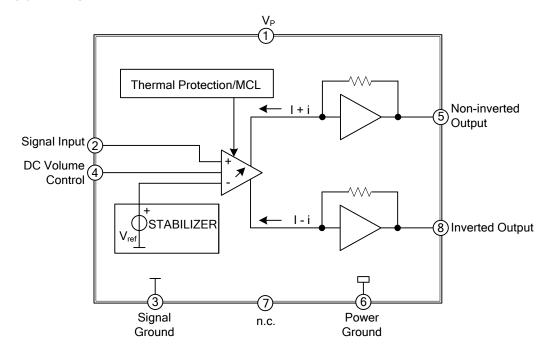
# **■ PIN CONFIGURATION**



## **■ PIN DESCRIPTION**

PIN NO.	PIN NAME	DESCRIPTION
1	$V_P$	Power Supply
2	IN+	Signal Input Terminal
3	GND1	Signal Ground
4	VC	DC Volume Control Terminal
5	OUT+	Non-inverted Output Terminal
6	GND2	Power Ground
7	NC	Not Connected
8	OUT-	Inverted Output Terminal

## ■ BLOCK DIAGRAM



#### ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	RATINGS	UNIT	
Supply Voltage Range		$V_{P}$	18	V
Input Voltage Pin 2		$V_2$	8	V
Input Voltage Pin 4		$V_4$	8	V
Repetitive Peak Output Current		I <sub>ORM</sub>	1.25	Α
Non-Repetitive Peak Output Current		I <sub>OSM</sub>	1.5	Α
Short-Circuit Time		T <sub>SC</sub>	1	hr
Total Dower Dissination (T <25%)	DIP-8	Ь	1.25	W
Total Power Dissipation (T <sub>A</sub> ≤25%)	SOP-8	P <sub>D</sub>	0.8	W
Operating Ambient Temperature Range		T <sub>A</sub>	-40 ~ +85	°C
Junction Temperature		$T_J$	+150	°C
Storage Temperature Range		T <sub>STG</sub>	-55 ~ <b>+</b> 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

#### **■ ELECTRICAL CHARACTERISTICS**

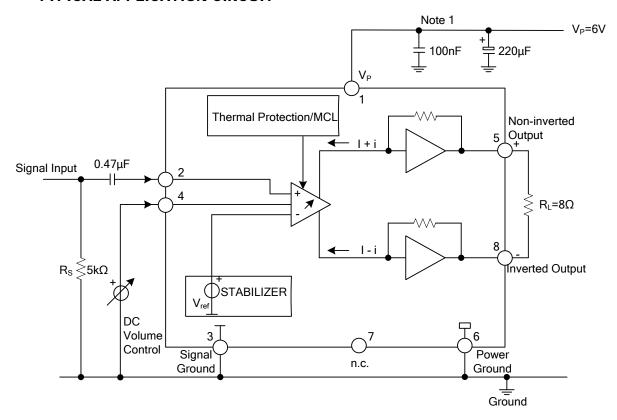
 $V_P=6V$ ,  $T_{amb}=25$ °C, f=1kHz;  $R_L=8\Omega$ , unless otherwise specified.

noi moo opoo	illou.							
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT			
$V_P$		4.5		18	V			
l <sub>P</sub>	V <sub>P</sub> =6V, R <sub>L</sub> =∞, Note 1		7	12	mA			
DC Volume Control								
φ		75	80		dB			
l <sub>4</sub>	V <sub>4</sub> =0.4V		70	80	μA			
Vo	V <sub>4</sub> ≤0.3V, V <sub>I</sub> =600mV			30	μV			
$G_V$			-44		dB			
$V_{NO(RMS)}$	Note 2		20	30	μV			
Characteristics In Maximum Gain, V₄=1.4V								
Po	THD=10%	1.0	1.1		W			
THD	P <sub>0</sub> =0.5W		0.3	1	%			
$G_V$		34.5	35.5	36.5	dB			
VI	V <sub>4</sub> =0.8V, THD<1%	0.5	0.65		V			
$V_{NO(RMS)}$	f=500kHz, Note 3		210		μV			
В	−1dB		0.02~300		kHz			
SVRR	Note 4	38	46		dB			
$ V_{OFF} $			0	150	mV			
$Z_{l}$		15	20	25	kΩ			
	SYMBOL  VP  IP  Q  I4  Vo  GV  VNO(RMS)  PO  THD  GV  VI  VNO(RMS)  B  SVRR   VOFF	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$			

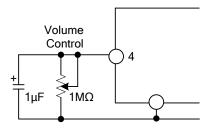
Notes: 1. With a load connected to the outputs the quiescent current will increase, the maximum value of this increase being equal to the DC output offset voltage dividend by R<sub>L</sub>.

- 2. The noise output voltage (RMS value) is measured with  $R_S$ =5k $\Omega$  unweighted.
- 3. The noise output voltage (RMS value) at f=500kHz is measured with  $R_S$ =0 $\Omega$  and bandwidth=5kHz.
- 4. The ripple rejection is measured with  $R_S=0\Omega$  and f=100Hz~10kHz. The ripple voltage of 200mV, (RMS value) is applied to the positive supply rail.

# **■ TYPICAL APPLICATION CIRCUIT**

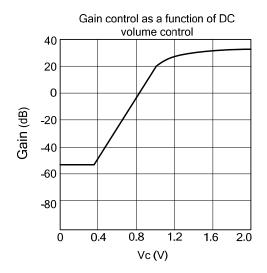


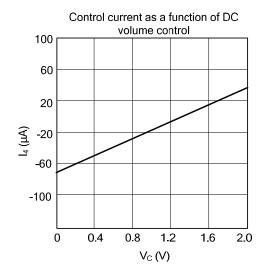
Note 1. This capacitor can be omitted if the  $220\mu F$  electrolytic capacitor is connected close to pin 1.



Application with potentiometer as volume control; maximum gain=30dB

## **■ TYPICAL CHARACTERISTICS**





UTC assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all UTC products described or contained herein. UTC products are not designed for use in life support appliances, devices or systems where malfunction of these products can be reasonably expected to result in personal injury. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice.