

Wide input voltage , non-isolated & regulated single output



FEATURES

- High efficiency up to 93%
- No-load input current as low as 0.2mA
- Operating temperature range: -40°C to +85°C
- Support the negative output
- Output short circuit protection
- Pin-out compatible with LM78XX linear regulators
- UL60950,EN60950 approval

K78xx-500R3 series are high efficiency switching regulators and ideal substitutes of LM78xx series three-terminal linear regulators. The product is featured with high efficiency, low loss, short circuit protection, support the negative output and no heat sink requirement. They are widely used in industrial control, instrumentation, and electric power applications.

	Part	Input Voltage (VDC) Output			Efficiency (Nominal	Max. Capacitive Load(µF)
Certification			Output Voltage Max. Output (VDC) Current (mA)		Input Voltage) (%, Min./Typ.) @Full Load	
UL/CE	K7803-500R3	24 (4.75-36)	3.3	500	78/81	680
III (05	24 (6.5-36)	5.0	500	82/85	680	
UL/CE	UL/CE K7805-500R3 12 (7-31)		-5.0	-300	78/81	330
UL/CE	K7809-500R3	24 (12-36)	9	500	87/90	680
UI /OF	1/7010 F00D0	24 (15-36)	12	500	89/92	680
UL/CE	K7812-500R3	12 (8-24)	-12	-150	82/85	330
	K7815-500R3	24 (19-36)	15	500	90/93	680
UL/CE		12 (8-21)	-15	-150	82/85	330

Input Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
No-load Input Current	Positive output		0.2	1.5	mA
Reverse Polarity Input			Forbi	dden	
Input Filter			Capac	itor filter	

Output Specifications						
Item	Operating Conditions	Operating Conditions		Тур.	Max.	Unit
Output Voltage Assuracy	Full lead to whom the control	K7803-500R3	_	±2	±4	%
Output Voltage Accuracy	Full load, input voltage range	Others		±2	±3	
Line Regulation	Full load, input voltage range	Full load, input voltage range		±0.2	±0.4	/6
Load Regulation	Nominal input,10% -100% load	Nominal input, 10% - 100% load		±0.4	±0.6	
Ripple & Noise*	20MHz bandwidth, nominal inpu	20MHz bandwidth, nominal input, 10%-100% load		20	75	mVp-p
Temperature Drift Coefficient	Operating temperature -40°C ~	Operating temperature -40 $^\circ$ C ~ +85 $^\circ$ C			±0.03	%/℃
Transient response deviation	Nominal input, 25% load step change			50	250	mV
Transient recovery time			-	0.2	1	ms
Output short circuit protection	Nominal input		Continuous	, self-recovery	•	

Note: *1. Ripple and noise tested with "parallel cable" method, please refer to *DC-DC Converter Application Notes* for specific operation methods; *2.With the load lower than 10%, the maximum ripple and noise of 3.3V/5V output products will be 150mVp-p, 9V/12V/15V output products will be 2%Vo.

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General Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	单位
Operating Temperature	Derating if the temperature ≥71°C (see Fig. 1)	-40		85	
Storage Temperature		-55		125	\mathbb{C}
Pin Welding Resistance Temperature	Welding time: 10s (Max.)			260	
Storage Humidity	Non-condensing	5		95	%RH
Switching Frequency	Full load, nominal input	550		850	KHz
MTBF	MIL-HDBK-217F@25°C	2000			K hours

Physical Specifications				
Casing Material	Black flame-retardant and heat-resistant plastic (UL94-V0)			
Package Dimensions	11.60*7.55*10.16 mm			
Weight	1.8g (Typ.)			
Cooling Method	Free air convection			

EMC S	EMC Specifications						
EMI	Conducted Disturbance	CISPR22/EN55022	CLASS B (see Fig. 5-2) for recommended circuit)				
CIVII	Radiated Emission	CISPR22/EN55022	CLASS B (see Fig. 5-2) for recommended circuit	·)			
	Electrostatic Discharge	IEC/EN 61000-4-2	Contact ±4KV	perf. Criteria B			
EN 4C	Radiation Immunity	IEC/EN 61000-4-3	10V/m	perf. Criteria A			
EMS	EFT	IEC/EN 61000-4-4	±1KV (see Fig. 5-① for recommended circuit)	perf. Criteria B			
	Conducted Disturbance Immunity	IEC/EN 61000-4-6	3Vr.m.s	perf. Criteria A			

Product Characteristic Curve

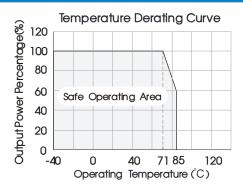
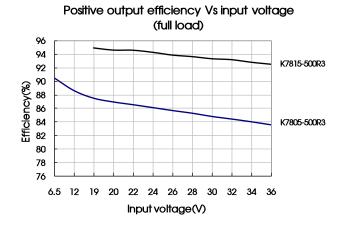
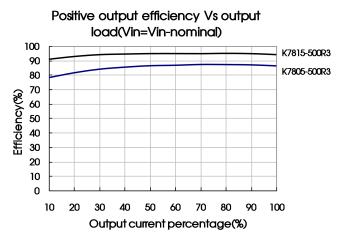
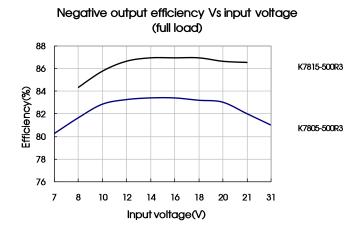


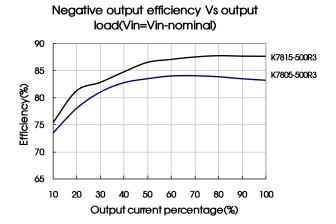
Fig. 1





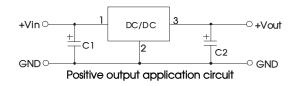
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Design Reference

1. Typical application circuit



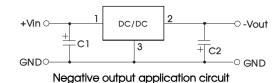
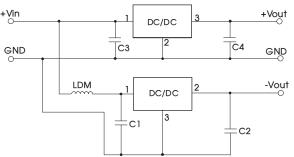


Fig. 2 Typical application circuit

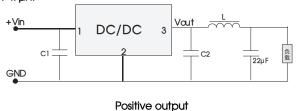


Sheet 1						
Part No.	C1/C3 (ceramic capacitor)	C2/C4 (ceramic capacitor)				
K7803-500R3	-	22µF/10V				
K7805-500R3	10μF/50V	22μF/10V				
K7809-500R3		22μF/16V				
K7812-500R3		22µF/25V				
K7815-500R3		22μF/25V				

Fig. 3 Positive and Negative output parallelling application circuit Note:

- 1. C1 and C2 (C3 and C4) are required and should be connected close to the pin terminal of the module.
- 2. The capacitance of C1 and C2 (C3 and C4) refer to Sheet 1, it can be increased properly if required, and tantalum or low ESR electrolytic capacitors may also suffice.
- 3. When the products used as the circuit like figure 3, an inductor named as LDM up to 10µH is recommended in the circuit to reduce the mutual interference.
- 4. Cannot be used in parallel for output and hot swap.

To reduce the output ripple furtherly, it is suggested to connect a "LC" filter at the output terminal, and recommended value of L is 10μ H-47 μ H.



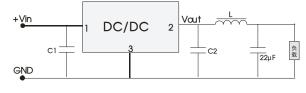


Fig. 4 "LC" filter application circuit

Negative output

2. EMC solution-recommended circuit

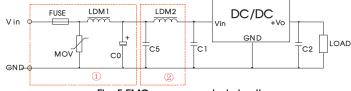


Fig. 5 EMC recommended circuit

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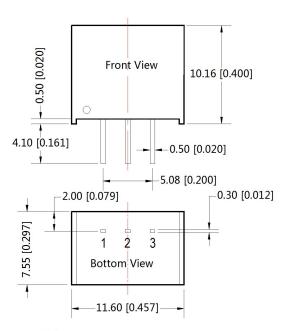


FUSE	MOV	LDM1	C0	C1/C2	C5	LDM2
Selected based on the actual input current from the customer	S14K35	82µH	680µF /50V	Refer to Sheet	4.7µF /50V	12µH

Note: Part ① in the Fig. 5 is for EMS test, part ② is for EMI filtering; parts ① and ② can be added based on actual requirement.

3. For more information please find the application notes on www.mornsun-power.com

Dimensions and Recommended Layout



THIRD ANGLE PROJECTION

\$\phi 1.00 [\$\phi 0.039]\$

Note: Grid 2.54*2.54mm

	Pin-Out					
Pin	Positive Output	Nagetive Output				
1	Vin	Vin				
2	GND	-Vo				
3	+Vo	GND				

Note:

Unit:mm[inch]

Pin section tolerances: ±0.10[±0.004] General tolerances: ±0.25[±0.010]

Notes:

- 1. Packing information please refer to Product Packing Information. Packing bag number: 58200003;
- 2. The max. capacitive load should be tested within the input voltage range and under full load conditions;
- 3. Without any special statement, all indexes are only specific to positive output application;
- 4. Unless otherwise specified, data in this datasheet should be tested under the conditions of Ta=25°C, humidity<75% when inputting nominal voltage and outputting rated load;
- 5. All index testing methods in this datasheet are based on our Company's corporate standards;
- The performance indexes of the product models listed in this manual are as above, but some indexes of non-standard model products will exceed the above-mentioned requirements, and please directly contact with our technician for specific information;
- 7. Specifications of this product are subject to changes without prior notice.