



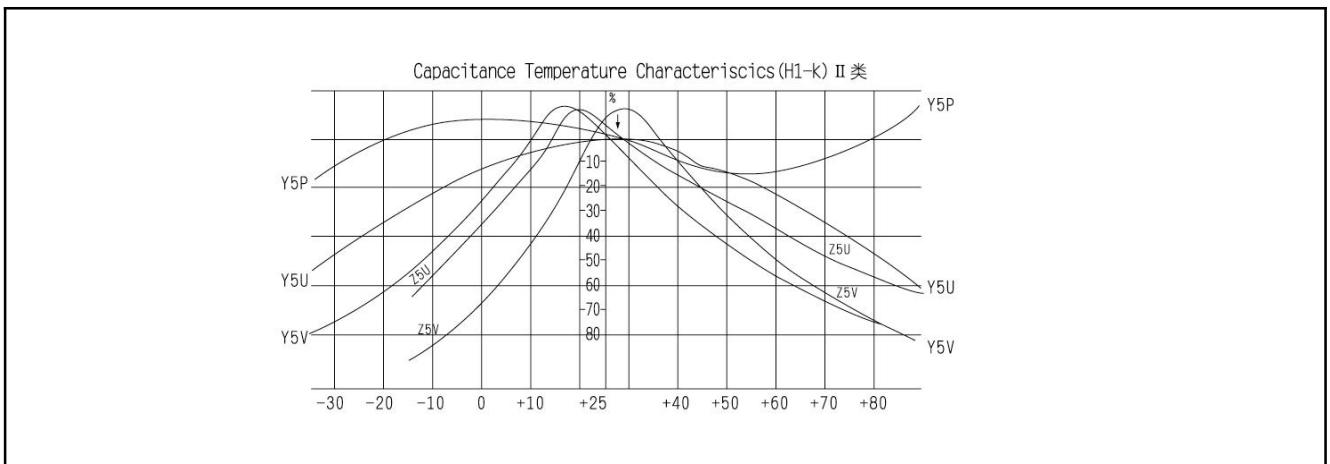
Appearance Size and Structure

Part No	Model	L Min(m m)	D Max(m m)	T Max(m m)	F (mm)	d (mm)	Appearance Size
	Y2-250VAC-10 2M	25	7.0	4.5	7.5±0.5	0.6±0.05	

Temperature Coefficient

Code	T.R.	Cap change
Y5V	-25°C ~ +85°C	+22% ~ -82%

Capacitance and Temperature Curve





According to:Specification

- GB/T 2693 《Fixed capacitors for use in electronic equipment
Part1: Generic specification》
- GB/T 5966 《Fixed capacitors for use in electronic equipment
Part8: Sectional specification
Fixed capacitors of ceramic dielectric, Class I 》
- GB/T 5968 《Fixed capacitors for use in electronic equipment
Part8: Sectional specification
Fixed capacitors of ceramic dielectric, Class II 》
- GB 11305 《Fixed capacitors for use in electronic equipment
Sectional specification
Fixed capacitors of ceramic dielectric, Class III》
- GB/T 14472 《Fixed capacitors for use in electronic equipment
Part14: Sectional specification
Fixed capacitors for electromagnetic interference suppression and
connection to the supply mains》
- GB2828 《Sampling procedures and tables for lot-by-lot inspection by attributes》
- GB2829 《Sampling procedures and tables for periodic inspection by attributes》

Quality Assurance(OQC)and Test

Check item (lot)	Check level	
	IL	AQL
1. Appearance 2. Size	S--4	2.5
1. Capacitance 2. DF 3. Voltage proof 4. Insulation resistance	II	0.25
1. Solder ability of leads	S--3	2.5



□ **Specification and Testing Method**

Item	Specification	Testing Method
1.Operating Temperature Range	-25~+85℃	
2.Capacitance	M: +20%-20%	Temperature: 25±2℃ Voltage: 1.0±0.2Vrms Frequency: 1.0±0.2KHz
3.DF	5.0%max	Temperature: 25±2℃ Voltage: 1.0±0.2Vrms Frequency: 1.0±0.2KHz
4.Insulation Resistance (IR)	5000MΩmin	Apply voltage: 500VDC Apply current: I≤0.05A Test time: 1min
5.Dielectric Strength	No failure	Rated voltage: Y2: 1500VAC Apply current: I≤2π×f×C×U Test time: 1min



6. Temperature Characteristic	Y5V: +22%~ -82%	<p>The capacitance measurement shall be made at each step:</p> <p>Before Test: Set the capacitor for 1hour at 85±2℃,after 24±2 hour at room temperature,then can be measured.</p> <table border="1" data-bbox="933 577 1262 884"> <thead> <tr> <th>Step</th> <th>Temperature</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>20±2℃</td> </tr> <tr> <td>2</td> <td>-25±3℃</td> </tr> <tr> <td>3</td> <td>20±2℃</td> </tr> <tr> <td>4</td> <td>85±2℃</td> </tr> <tr> <td>5</td> <td>20±2℃</td> </tr> </tbody> </table>	Step	Temperature	1	20±2℃	2	-25±3℃	3	20±2℃	4	85±2℃	5	20±2℃
Step	Temperature													
1	20±2℃													
2	-25±3℃													
3	20±2℃													
4	85±2℃													
5	20±2℃													

Item		Specification	Testing Method
7. Vibration resistance	Appearance	No marked defect	The capacitor shall firmly be soldered to the supporting terminal and vibration which is 10HZ to 55HZ in the vibration frequency range 1.5mm in total amplitude and about 1min. in the rate of vibration change from 10HZ to 55HZ and back to 10HZ is applied for a total of 6 hours, 2hours each in there mutually perpendicular direction.
	Capacitance	Y5V: $\Delta C/C \leq 30\%$	
	DF	Y5V: 7.5%max	
8. Soldering effect	Appearance	No marked defect	The lead wire shall be immersed into the melted solder of 260±5℃ up to about 1.5to 2.0mm from the main body for 3.5±0.5sec. Pre-treatment: capacitor shall be stored 1hour at 85±2℃ after at normal temperature for 24±2 hour before initial measurements. Post-treatment: capacitor shall be measured after 24±2 hours at normal temperature.
	Capacitance change	Y5V: $\Delta C/C \leq 30\%$	
	Dielectric strength (between lead wires)	No failure	



9.Humidity (under steady state)	Appearance	No marked defect	Set the capacitor for 500 hours at 40±2°C in 90 to 95%RH Pre-treatment: capacitor shall be stored 1hour at 85±2°C after at normal temperature for24±2 hour before initial measurements. Post-treatment: capacitor shall be measured after 24±2 hours at normal temperature.
	Capacitance change	Y5V: $\Delta C/C \leq 30\%$	
	DF	Y5V: 7.5%max	
	Insulation Resistance (IR)	1000MΩmin	

Item		Specification	Testing Method
10. Humidity loading	Appearance	No marked defect	Apply rated voltage for 500 hours at 40±2°C in 90 to 95%RH Pre-treatment: capacitor shall be stored 1hour at 85±2°C after at normal temperature for24±2 hour before initial measurements. Post-treatment: capacitor shall be measured after 24±2 hours at normal temperature.
	Capacitance change	Y5V: $\Delta C/C \leq 30\%$	
	DF	Y5V: 7.5%max	
	Insulation Resistance (IR)	3000MΩmin	
11.Life Test	Appearance	No marked defect	Apply 150% of the rated voltage for 1000 hours at 85±2°C Pre-treatment: capacitor shall be stored 1hour at 85±2°C after at normal temperature for24±2 hour before initial measurements. Post-treatment: capacitor shall be measured after 24±2 hours at normal temperature.
	Capacitance change	Y5V: $\Delta C/C \leq 30\%$	
	DF	Y5V: 7.5%max	
	Insulation Resistance (IR)	3000MΩmin	



12.Strength of lead	Dielectric strength	Lead wire shall not cut off, Capacitor shall not be broken.	As a figure fix the body of capacitor, apply a tensile weight gradually to each lead in the radial direction of capacitor up to 10N and keep it for 10±15 sec.
	Bending		Each lead wire shall be subjected to 5N weight and then ±45°bend twice.
	Turn back strength		Each lead wire shall be turn back twice at 180°.
13.Solderability of leads	Lead wire shall be soldered with coated over 95% of the circumferential direction.		The lead wire of a capacitor shall be dipped into flax and then into molten solder of 235±5°C for 2±0.5sec.