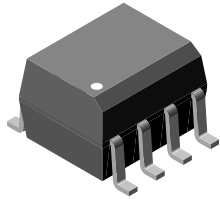
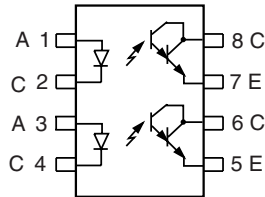


## Optocoupler, Photodarlington Output, Dual Channel, SOIC-8 Package



I179042



### DESCRIPTION

The ILD223T is a high current transfer ratio (CTR) optocoupler. It has a gallium arsenide infrared LED emitter and silicon NPN photodarlington transistor detector.

This device has CTRs tested at an LED current of 1.0 mA. This low drive current permits easy interfacing from CMOS to LSTTL or TTL.

The ILD223T is constructed in a standard SOIC-8 foot print which makes it ideally suited for high density applications. In addition to eliminating through hole requirements, this package conforms to standards for surface mounted devices.

### FEATURES

- Two channel optocoupler
- High current transfer ratio at  $I_F = 1.0$  mA, 500 % minimum
- Isolation test voltage, 4000  $V_{RMS}$
- Electrical specifications similar to standard 6-pin coupler
- Compatible with dual wave, vapor phase and IR reflow soldering
- SOIC-8 surface mountable package
- Standard lead spacing, 0.05"
- Available only on tape and reel (conforms to EIA standard 481-2)
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC


**RoHS**  
COMPLIANT

### AGENCY APPROVALS

- UL1577, file no. E52744 system code Y
- CUL - file no. E52744, equivalent to CSA bulletin 5A
- DIN EN 60747-5-2 (VDE 0884) available with option 1

### ORDER INFORMATION

PART	REMARKS
ILD223T	CTR > 500 %, SOIC-8

### ABSOLUTE MAXIMUM RATINGS (1)

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
<b>INPUT</b>				
Peak reverse voltage		$V_R$	6.0	V
Peak pulsed current	1.0 $\mu$ s, 300 pps		3.0	A
Continuous forward current per channel			30	mA
Power dissipation		$P_{diss}$	45	mW
Derate linearly from 25 °C			0.4	mW/°C
<b>OUTPUT</b>				
Collector emitter breakdown voltage		$BV_{CEO}$	30	V
Emitter collector breakdown voltage		$BV_{ECO}$	5.0	V
Power dissipation per channel		$P_{diss}$	75	mW
Derate linearly from 25 °C			3.1	mW/°C

Vishay Semiconductors Optocoupler, Photodarlington Output,  
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ABSOLUTE MAXIMUM RATINGS <sup>(1)</sup>				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
<b>COUPLER</b>				
Isolation test voltage	t = 1.0 s	V <sub>ISO</sub>	4000	V <sub>RMS</sub>
Total package dissipation (2 LEDs and 2 detectors, 2 channels)		P <sub>tot</sub>	250	mW
Derate linearly from 25 °C			2.0	mW/°C
Storage temperature		T <sub>stg</sub>	- 55 to + 150	°C
Operating temperature		T <sub>amb</sub>	- 55 to + 100	°C
Soldering temperature <sup>(2)</sup>		T <sub>slid</sub>	260	°C

**Notes**

<sup>(1)</sup> T<sub>amb</sub> = 25 °C, unless otherwise specified

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(2)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SOP/SOIC).

ELECTRICAL CHARACTERISTICS							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>INPUT</b>							
Forward voltage	I <sub>F</sub> = 10 mA		V <sub>F</sub>			1.3	V
Reverse current	V <sub>R</sub> = 6.0 V		I <sub>R</sub>		0.1	100	μA
Capacitance	V <sub>F</sub> = 0 V, f = 1.0 MHz		C <sub>O</sub>		25		pF
<b>OUTPUT</b>							
Collector emitter breakdown voltage	I <sub>C</sub> = 10 μA		BV <sub>CEO</sub>	30			V
Emitter collector breakdown voltage	I <sub>C</sub> = 10 μA		BV <sub>CEO</sub>	5.0			V
Collector emitter leakage current	V <sub>CE</sub> = 50 V, I <sub>F</sub> = 0 A		I <sub>CEO</sub>			50	nA
Collector emitter capacitance	V <sub>CE</sub> = 5.0 V		C <sub>CE</sub>		3.4		pF
<b>COUPLER</b>							
Capacitance (input to output)		ILD223T	C <sub>IO</sub>	0.5			pF
Saturation voltage, collector emitter	I <sub>F</sub> = 1.0 mA, I <sub>CE</sub> = 0.5 mA	ILD223T	V <sub>CEsat</sub>			1.0	V
Resistance, input to output		ILD223T	C <sub>IO</sub>	100			GΩ

**Note**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
DC current transfer ratio	I <sub>F</sub> = 1.0 mA, V <sub>CE</sub> = 5.0 V	CTR <sub>DC</sub>	500			%

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	V <sub>CC</sub> = 10 V, R <sub>L</sub> = 100 Ω, I <sub>F</sub> = 5.0 mA	ILD223T	t <sub>on</sub>	15			μs
Turn-off time	V <sub>CC</sub> = 10 V, R <sub>L</sub> = 100 Ω, I <sub>F</sub> = 5.0 mA	ILD223T	t <sub>off</sub>	30			μs



Optocoupler, Photodarlington Output, Vishay Semiconductors  
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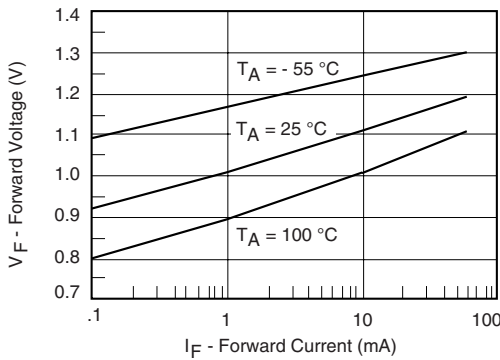
SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)				55/100/21		
Comparative tracking index		CTI	175		399	
$V_{IOTM}$			6000			V
$V_{IORM}$			560			V
$P_{SO}$					350	mW
$I_{SI}$					150	mA
$T_{SI}$					165	°C
Creepage distance			4			mm
Clearance distance			4			mm
Insulation thickness, reinforced rated	per IEC 60950 2.10.5.1		0.2			mm

**Note**

As per IEC 60747-5-2, §7.4.3.8.1, this optocoupler is suitable for “safe electrical insulation” only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

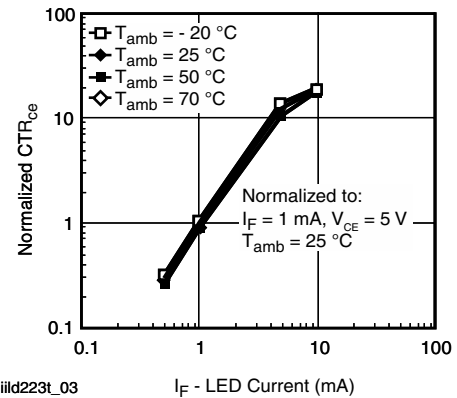
**TYPICAL CHARACTERISTICS**

$T_{amb} = 25\text{ °C}$ , unless otherwise specified



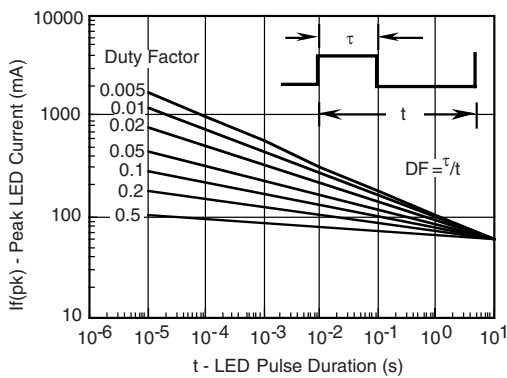
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Fig. 3 - Forward Voltage vs. Forward Current



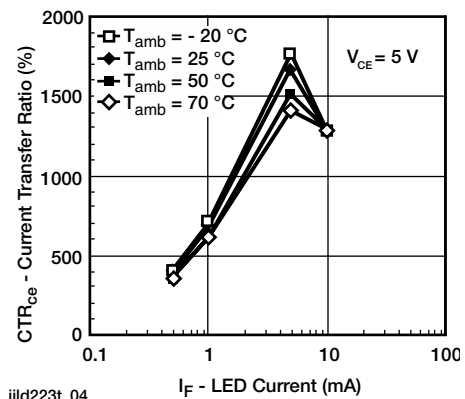
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Fig. 5 - Normalized  $CTR_{ce}$  vs. LED Current



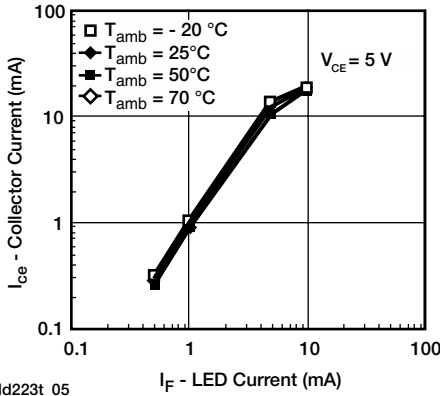
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Fig. 4 - Peak LED Current vs. Duty Factor,  $\tau$



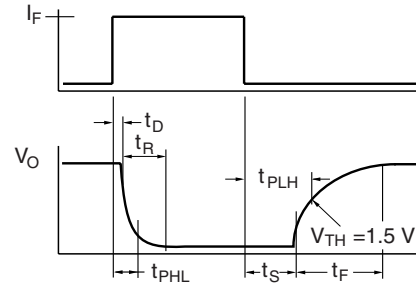
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Fig. 6 -  $CTR_{ce}$  vs. LED Current



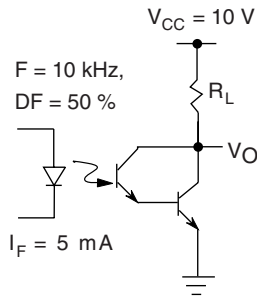
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Fig. 7 - Collector Current vs. LED Current



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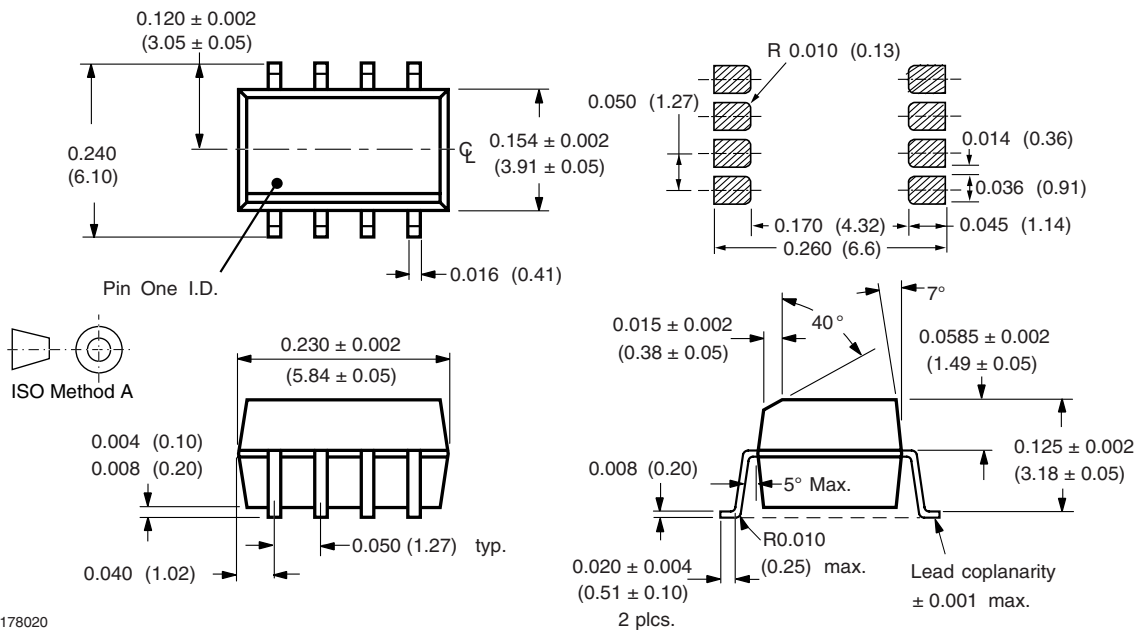
Fig. 9 - Switching Timing



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Fig. 8 - Switching Schematic

**PACKAGE DIMENSIONS** in inches (millimeters)



ii178020



Optocoupler, Photodarlington Output, Vishay Semiconductors  
Dual Channel,  
SOIC-8 Package

**OZONE DEPLETING SUBSTANCES POLICY STATEMENT**

It is the policy of Vishay Semiconductor GmbH to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design  
and may do so without further notice.

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Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany



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