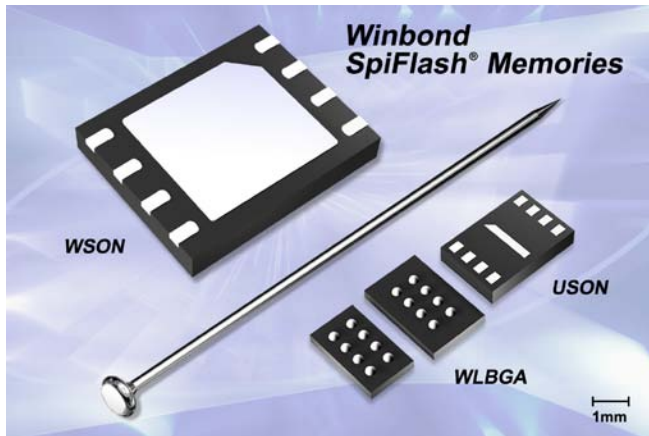
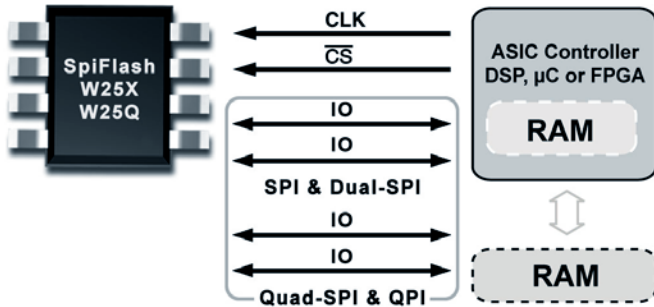


Winbond's W25X and W25Q SpiFlash® Multi-I/O Memories feature the popular Serial Peripheral Interface (SPI), densities from 512K-bit to 256M-bit, small erasable sectors and the industry's highest performance. The W25X family supports Dual-SPI effectively doubling standard SPI clock rates. The W25Q family is a "superset" of the 25X family with Dual-I/O and Quad-I/O SPI for even higher performance. Clock rates up to 104MHz achieve an equivalent of 416MHz (50M-Byte/S transfer rate) when using Quad-SPI. This even surpasses asynchronous Parallel Flash memories while using fewer pins and less space. Faster transfer rates mean controllers can execute code (XIP) directly from the SPI interface or further improve boot time when shadowing code to RAM. Some SpiFlash devices offer the new Quad Peripheral Interface (QPI) supporting true Quad Commands for improved XIP performance and simpler controller circuitry. Additionally, new ultra-small form-factor packages are ideal for space constrained mobile and handheld applications.



W25X SpiFlash Family

- 512K-bit to 4M-bit
- Serial Peripheral Interface (SPI), Dual Output SPI
- Uniform 4KB, 32KB & 64KB erase

W25Q SpiFlash Family

- 2M-bit to 256M-bit, superset compatible with 25X
- SPI, Dual-SPI, Quad-SPI and QPI (for many devices)
- Uniform 4KB, 32KB & 64KB erase
- Erase and Program Suspend/Resume
- Quad Page Program
- Security: Lock-down, ID#, OTP Registers
- Serial Flash Discoverable Parameters (SFDP)

High Performance

- 104MHz Clock, 416MHz Quad-SPI (50MB/S)
- Fast-boot or execute code (XIP) from SPI

Voltage & Package Options

- 3V (2.7~3.6V), 2.5V (2.3~3.6V) and 1.8V (1.65~1.95V)
- Space saving packages: 8-pin SOIC, WSON, VSOP, USON, WLBGA (CSP), 16-pin SOIC, 24-ball TFBGA
- Known Good Die (KGD) Wafers

Wide Range of Applications

- PCs, DVD, BluRay, WLAN, DSL/Cable Modem, Printers,
- Hard Drives, Set Top Box, LCD-TV, Mobile Phones,
- Bluetooth, GPS, MP3, Meters, DSP, FPGAs and more

Refer to Winbond Automotive SpiFlash Memory Selection Guide for details of Automotive products.

Winbond also offers the W29GL family of Parallel Flash products from 32Mb through 512Mb densities compatible to industry standard x29GL products.

Density	Winbond Part # ²	Quad SPI	Clock MHz	Features ³	Voltage	Package ⁴	Sample Availability
512M-bit	W25M512JVxIG/Q	•	104	QPI, Enhanced, DTR	3V	x=(F,E,C ⁶ ,B ⁶)	Q4-2014
256M-bit	W25Q256FVxIG/Q/F	•	80/104	QPI, Enhanced	3V	x=(F,E,C ⁶ ,B ⁶)	Now
	W25Q257FVxIG/Q ⁵	•	80/104	QPI, Enhanced	3V	x=(F,E)	Q1-2014
	W25Q256JVxIG/Q	•	80/104	QPI, Enhanced, DTR	3V	x=(F,E,C ⁶ ,B ⁶)	Q4-2014
128M-bit	W25Q128FVxIG/Q/F	•	104	QPI, Enhanced	3V	x=(S,T ⁶ ,F,P,E,C ⁶ ,B ⁶)	Now
	W25R128FVxIQ	•	104	RPMC, Enhanced	3V	x=(S,T ⁶ ,F ⁶ ,P ⁶)	Now
	W25Q128JVxIG/Q	•	104	QPI, Enhanced, DTR	3V	x=(S,T ⁶ ,F,P,E,C ⁶ ,B ⁶)	Q4-2014
	W25Q128FWxIG	•	104	QPI, Enhanced	1.8V	x=(S,T ⁶ ,F ⁶ ,P)	Now
64M-bit	W25Q64FVxxIG/Q/F	•	104	QPI, Enhanced	3V	xx=(SS,ST ⁶ ,SF,ZP,ZE,TC,TB ⁶)	Now
	W25R64FVxxIQ	•	104	RPMC, Enhanced	3V	xx=(SS,ST ⁶ ,SF ⁶ ,ZP ⁶)	Now
	W25Q64JVxxIG/Q	•	104	QPI, Enhanced, DTR	3V	xx=(SS,ST ⁶ ,SF,ZP,ZE,TC,TB ⁶)	Q3-2014
	W25Q64FWxxIG	•	104	QPI, Enhanced	1.8V	xx=(SS,ST ⁶ ,SF ⁶ ,ZP,BY)	Now
32M-bit	W25Q32FVxxIG/Q/F	•	104	QPI, Enhanced	3V	xx=(SS,ST ⁶ ,SF,ZP,ZE,TC ⁶ ,TB ⁶)	Now
	W25Q32JVxxIG/Q	•	104	QPI, Enhanced, DTR	3V	xx=(SS,ST ⁶ ,SF,ZP,ZE,TC ⁶ ,TB ⁶)	Q3-2014
	W25Q32DWxxIG	•	104	QPI, Enhanced	1.8V	xx=(SS,ST ⁶ ,SF ⁶ ,ZP,ZE ⁶)	Now
	W25Q32FWxxIG	•	104	QPI, Enhanced	1.8V	xx=(SS,ST ⁶ ,SF ⁶ ,ZP,ZE ⁶ ,XG)	Q1-2014
16M-bit	W25Q16DVxxIG/Q	•	80/104	Enhanced	3V	xx=(SN,SV ⁶ ,SS,ST ⁶ ,ZP,TC ⁶ ,TB ⁶)	Now
	W25Q16CLxxIG	•	50/80	Enhanced	2.5V/3V	xx=(SN,SV ⁶ ,SS,ZP)	Now
	W25Q16DWxxIG	•	104	QPI, Enhanced	1.8V	xx=(SN,SV ⁶ ,SS,ST ⁶ ,SF ⁶ ,ZP,TC ⁶ ,TB ⁶ ,UU,BY ⁶)	Now
	W25Q16FWxxIG	•	104	QPI, Enhanced	1.8V	xx=(SN,SV ⁶ ,SS,ST ⁶ ,SF ⁶ ,ZP,TC ⁶ ,TB ⁶ ,UU,BY ⁶)	Q3-2014
8M-bit	W25Q80BVxxIG	•	80/104	Fast Write, Enhanced	3V	xx=(SN,SS,ZP,UX)	Use W25Q80DV
	W25Q80DVxxIG	•	80/104	Fast Write, Enhanced	3V	xx=(SN,SV,SS,ZP,UX)	Now
	W25Q80BLxxIG	•	50/80	Fast Write, Enhanced	2.5V	xx=(SN,SV,SS,ZP,UX)	Now
	W25Q80DLxxIG	•	80/104	Fast Write, Enhanced	2.5V	xx=(SN,SV,SS,ZP,UX)	Q2-2014
	W25Q80BWxxIG ⁷	•	80	Fast Write, Enhanced	1.8V	xx=(SN,SV,SS,ZP,UU ⁶ ,BY ⁶)	Now
	W25Q80EWxxIG	•	80	Fast Write, Enhanced	1.8V	xx=(SN,SV,SS,ZP,UU ⁶ ,BY ⁶)	Q4-2014
4M-bit	W25X40CLxxIG		80/104	Fast Write	2.5V/3V	xx=(SN,SV,SS,ZP,UX)	Now
	W25Q40CLxxIG	•	104	Fast Write, Enhanced	2.5V/3V	xx=(SN,SS,UX ⁶)	Now
	W25Q40BWxxIG	•	80	Fast Write, Enhanced	1.8V	xx=(SN,SV,SS ⁶ ,ZP,UX,UU ⁶)	Now
	W25Q40EWxxIG	•	80	Fast Write, Enhanced	1.8V	xx=(SN,SV,SS ⁶ ,ZP,UX,UU ⁶)	Q4-2014
2M-bit	W25X20CLxxIG		80/104	Fast Write	2.5V/3V	xx=(SN,SV,ZP,UX)	Now
	W25Q20CLxxIG	•	80/104	Fast Write	2.5V/3V	xx=(SN,SV,ZP,UX)	Now
	W25Q20BWxxIG	•	80	Fast Write, Enhanced	1.8V	xx=(SN,SV,ZP,UX ⁶)	Now
	W25Q20EWxxIG	•	80	Fast Write, Enhanced	1.8V	xx=(SN,SV,ZP,UX ⁶)	Q2-2014
1M-bit	W25X10CLxxIG		80/104	Fast Write	2.5V/3V	xx=(SN,SV ⁶ ,ZP ⁶ ,UX)	Now
	W25Q10EWxxIG	•	80/104	Fast Write, Enhanced	1.8V	xx=(SN,SV ⁶ ,ZP ⁶ ,UX)	Q2-2014
512K-bit	W25X05CLxxIG		80/104	Fast Write	2.5V/3V	xx=(SN,SD ⁶ ,UX)	Now

1. See data sheet for further technical information. This is subject to change without notice. 2. At the end of the part number, letter "G" represents "Green", Halogen Free and RoHS compliant packaging; letter "Q" represents Green packaging and Quad Enabled as shipping default & fast sector erase time (tSE); letter "F" represents Fast Sector Erase time (tSE); letter "I" represents Industrial Temperature (-40°C to +85°C). 3. Enhanced=SFDP¹, Security Registers, Program/Erase Suspend/Resume, Burst Read with Wrap, Non-Volatile & Volatile Status Registers, Complement Array Protection. 4. SN=SO8 150mil, SV=VSOP8 150mil, SS or S=SO8 208mil, ST or T=VSOP8 208mil, SF or F=SO16 300mil, SD=TSSOP8 173mil, ZP or P=WSO8 6x5mm, ZE or E=WSO8 8x6mm, TC or C=TFBGA24 8x6mm (4x6 Matrix), TB or B=TFBGA24 8x6mm (5x5 Matrix), UX=USO8 2x3mm, UU=USO8 4x3mm, XG=XSON8 4x4mm, BY=WLPGA. KGD Wafer available. 5. Default 4-byte addressing for W25W257FV. 6. Special Order. 7. Contact Winbond for Suspend/Resume feature support on W25Q80BW.