

FGH40N60SMD 600 V, 40 A Field Stop IGBT

Features

- Maximum Junction Temperature : T_J = 175^oC
- Positive Temperaure Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)}$ = 1.9 V(Typ.) @ I_C = 40 A
- High Input Impedance
- Fast Switching: E_{OFF} = 6.5 uJ/A
- Tighten Parameter Distribution
- RoHS Compliant

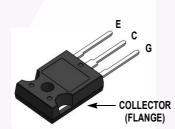
Applications

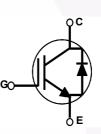
· Solar Inverter, UPS, Welder, PFC, Telecom, ESS

October 2014

General Description

Using novel field stop IGBT technology, Fairchild's new series of field stop 2nd generation IGBTs offer the optimum performance for solar inverter, UPS, welder, telecom, ESS and PFC applications where low conduction and switching losses are essential.





Absolute Maximum Ratings

Symbol	Description		Ratings	Unit
V _{CES}	Collector to Emitter Voltage	600	V	
V _{GES}	Gate to Emitter Voltage	± 20	V	
	Transient Gate to Emitter Voltage	± 30	V	
I _C	Collector Current	@ T _C = 25°C 80		A
'U	Collector Current	@ T _C = 100 ^o C	40	A
I _{CM (1)}	Pulsed Collector Current	@ T _C = 25°C	120	A
IF	Diode Forward Current	@ T _C = 25°C	40	A
'F	Diode Forward Current	@ T _C = 100 ^o C	20	A
I _{FM (1)}	Pulsed Diode Maximum Forward Cur	120	A	
P _D	Maximum Power Dissipation	@ T _C = 25°C	349	W
. D	Maximum Power Dissipation	@ T _C = 100 ^o C	174	W
TJ	Operating Junction Temperature		-55 to +175	°C
T _{stg}	Storage Temperature Range	-55 to +175	°C	
TL	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 secor	nds	300	°C

Notes:

1: Repetitive rating: Pulse width limited by max. junction temperature

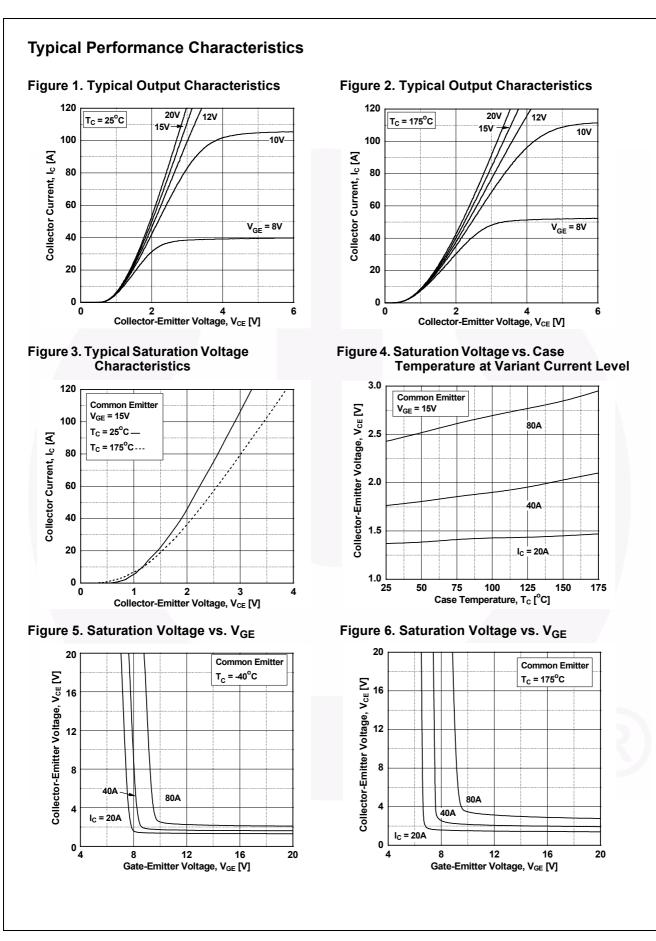
Symbo	I	Par	ramete	r		Тур.		Max.		Unit	
R _{0JC} (IGBT)	Thern	nal Resistance, Junct	ise		-		0.43		°C/W		
R _{0JC} (Diode)				ise		-		1.5		°C/W	
R _{θJA}	Thern	nal Resistance, Junct	ion to An	nbient		-		40		°C/W	
Package	e Marki	ng and Order	ing In	form	ation		- I		-		
	lumber Top Mark Pack			Packing Method	Reel Size		Tape Wid	th Qu	Quantity		
FGH40N60SMD FGH40N60SMD TO-2		247	Tube	N/A		N/A		30			
Electric	al Char	acteristics of	the lo	GBT	T _C = 25°C unless othe	rwise noted					
Symbol		Parameter		Test Conditions		ns	Min.	Тур.	Max.	Unit	
Off Charact		o Emittor Prockdown	Voltage	V	0 \/ _ = 250 \		600			V	
BV _{CES} ∆BV _{CES}		o Emitter Breakdown ure Coefficient of Brea		-	V _{GE} = 0 V, I _C = 250 μA			-	-	V	
ΔT_J	Voltage	are obtained in bree		V_{GE} = 0 V, I _C = 250 μ A			-	0.6	-	V/ºC	
I _{CES}	Collector (ollector Cut-Off Current			V _{CE} = V _{CES} , V _{GE} = 0 V			-	250	μA	
I _{GES}	G-E Leakage Current			V _{GE} =	V _{GES} , V _{CE} = 0 V		-	-	± 400	nA	
On Charact	eristics										
V _{GE(th)}	G-E Thres	G-E Threshold Voltage			I _C = 250 μA, V _{CE} = V _{GE}			4.5	6.0	V	
		I _C = 40 A, V _{GE} = 15 V			- 1.9		2.5	V			
V _{CE(sat)}	Collector to Emitter Saturation Voltage				I _C = 40 A, V _{GE} = 15 V, T _C = 175°C			2.1	-	V	
Dynamic C	haracteris	tics							1		
C _{ies}	Input Cap						-	1880	-	pF	
C _{oes}		Output Capacitance Reverse Transfer Capacitance			V _{CE} = 30 V, V _{GE} = 0 V, f = 1 MHz			180	-	pF	
C _{res}	Reverse T							50	-	pF	
	Charactori	stics								/	
Switching (Delay Time					· ·	12	16	ns	
t _{d(on)} t _r	Rise Time			-			_	20	28	ns	
t _{d(off)}		Delay Time		Voc =	400 V, I _C = 40 A,		-	92	120	ns	
t _f	Fall Time	,		$R_G = 6$	Ω, V _{GE} = 15 V,		-	13	17	ns	
E _{on}		Switching Loss		Inducti	ve Load, $T_C = 25^\circ$	d, T _C = 25ºC		0.87	1.30	mJ	
E _{off}		Switching Loss					-	0.26	0.34	mJ	
E _{ts}		ching Loss					-	1.13	1.64	mJ	
t _{d(on)}		Delay Time					-	15	-	ns	
t _r	Rise Time						-	22	-	ns	
t _{d(off)}	Turn-Off D	elay Time		$V_{CC} = $	400 V, I _C = 40 A,		-	116	-	ns	
t _f	Fall Time				R _G = 6 Ω, V _{GE} = 15 V,			16	-	ns	
E _{on}	Turn-On S	Switching Loss		Inducti	ve Load, T _C = 17	5°C	-	0.97	-	mJ	
E _{off}	Turn-Off S	Switching Loss		1			-	0.60	-	mJ	
E _{ts}	Tatal Curit	ching Loss		1			-	1.57	_	mJ	

Electrical Characteristics of the IGBT (Continued)

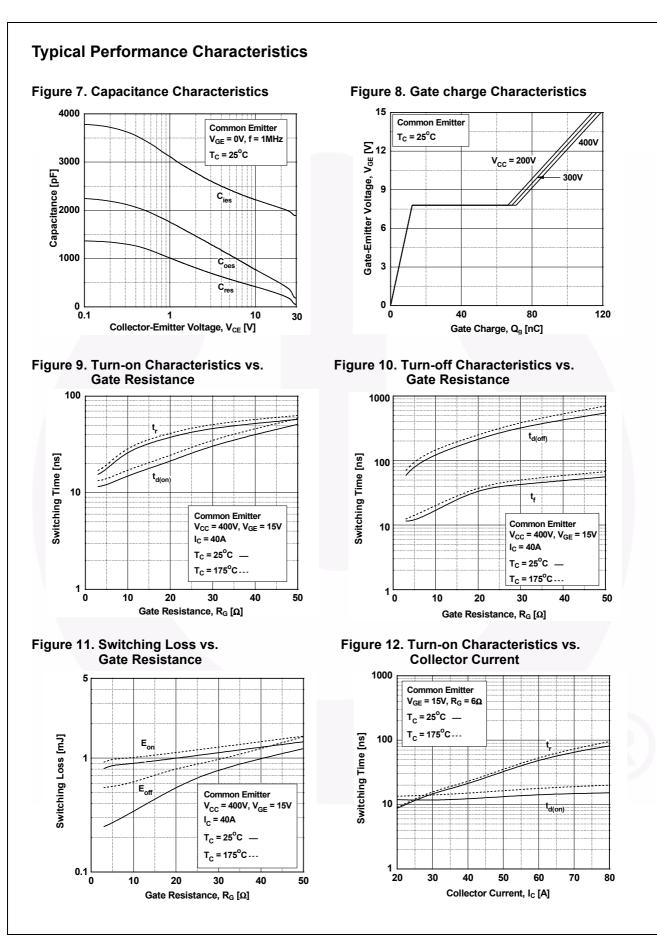
Symbol	Parameter	Test Conditions	Min.	Тур.	Max	Unit
Qg	Total Gate Charge		-	119	180	nC
Q _{ge}	Gate to Emitter Charge	V _{CE} = 400 V, I _C = 40 A, V _{GE} = 15 V	-	13	20	nC
Q _{gc}	Gate to Collector Charge	VGE - 13 V	-	58	90	nC

Electrical Characteristics of the Diode T_C = 25°C unless otherwise noted

Symbol	Parameter		Test Condition	on	s	Min.	Тур.	Мах	Unit
V _{FM}	Diode Forward Voltage		20 A		T _C = 25°C	-	2.3	2.8	V
FIN			F 207		T _C = 175 ^o C	-	1.67	-	1
E _{rec}	Reverse Recovery Energy				T _C = 175 ^o C	-	48.9	-	uJ
t.	Diode Reverse Recovery Time	L = 20 A dL	20 A, dI _F /dt = 200 A/µs		T _C = 25°C	-	36	-	ns
۲r	blode Reverse Recovery fille		$1F = 20 A$, $0F at = 200 A \mu 3$		T _C = 175 ^o C	-	110	-	110
Q _{rr}	Diode Reverse Recovery Charge				T _C = 25°C	-	46.8	-	nC
~11	2.000 Hororor (000 for y charge				T _C = 175 ^o C	-	445	-	

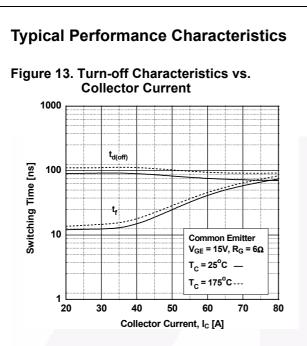


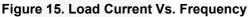
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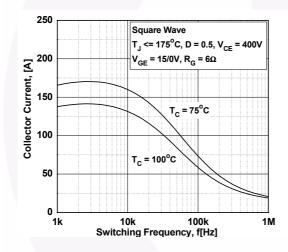
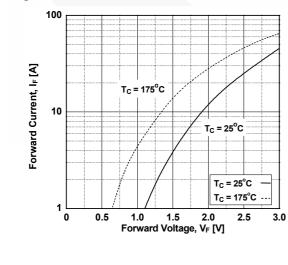


Figure 17. Forward Characteristics



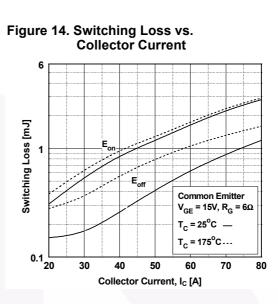


Figure 16. SOA Characteristics

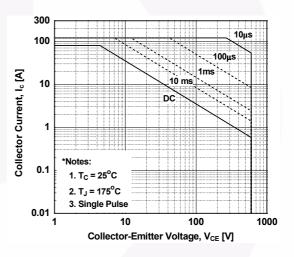
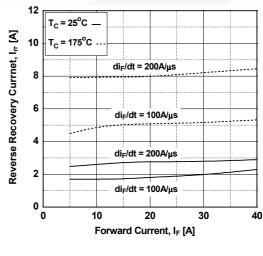
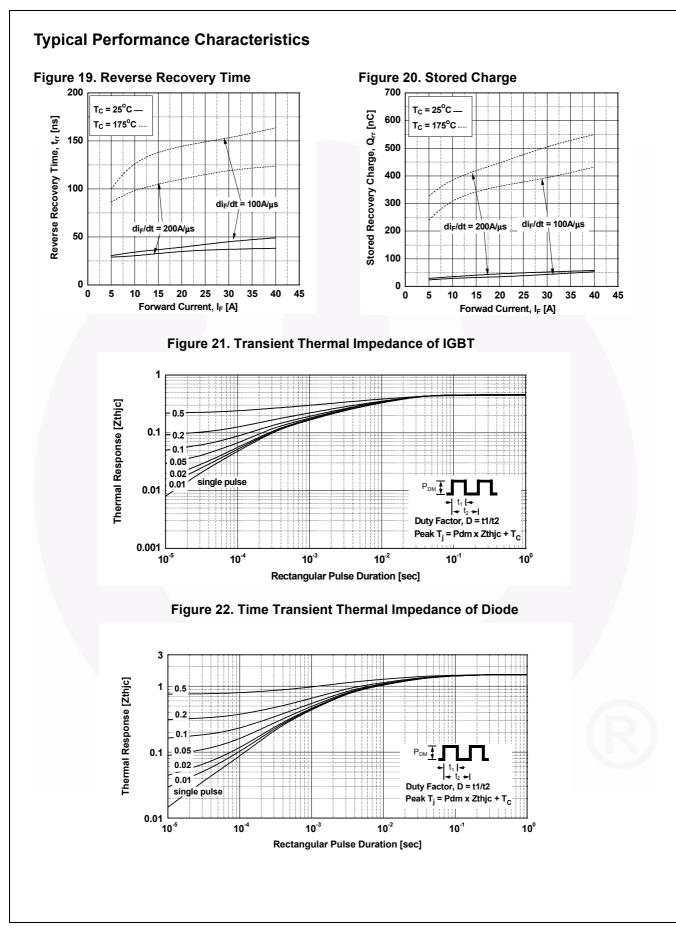
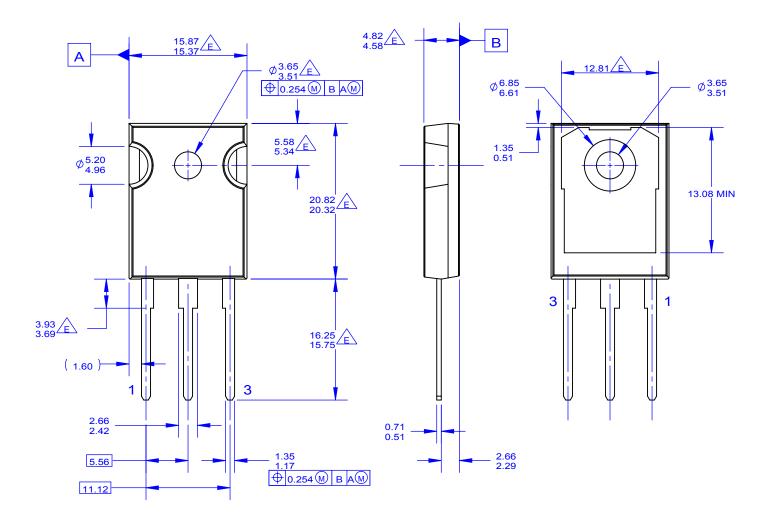


Figure 18. Reverse Recovery Current



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