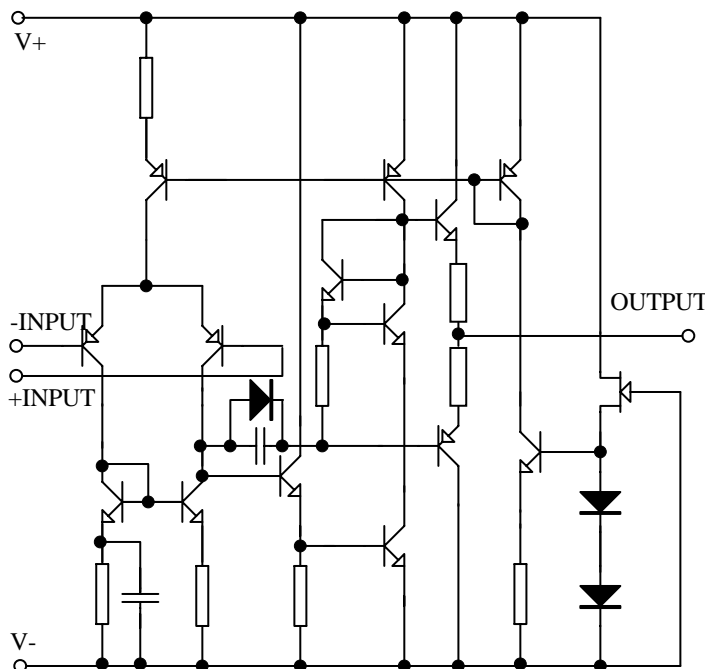
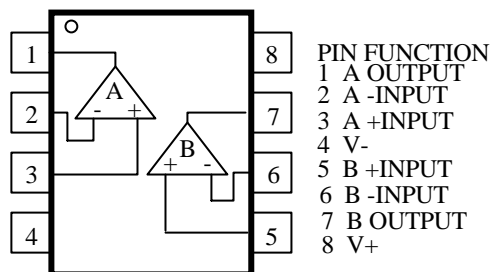


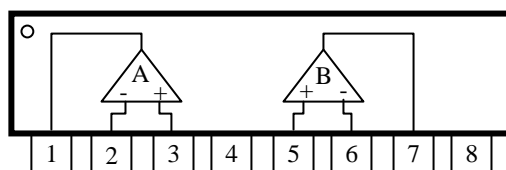
EQUIVALENT CIRCUIT



PIN CONFIGURATION



- PIN FUNCTION**
 1 A OUTPUT
 2 A -INPUT
 3 A +INPUT
 4 V-
 5 B +INPUT
 6 B -INPUT
 7 B OUTPUT
 8 V+



ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Characteristic	Symbol	Value	Unit
Supply Voltage	V+/V-	±16	V
Input Voltage	V _{IC}	±15	V
Differential Input Voltage	V _{ID}	±30	V
Output Current	I _c	±50	mA
Power Dissipation	P _D	800	mW
Operating Temperature Range	T _{amb}	-40~85	°C
Storage Temperature Range	T _{stg}	-40~125	°C

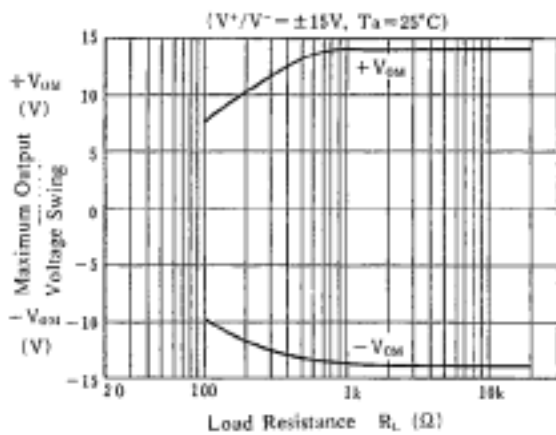
ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: Ta=25°C, V+/V- = ±15V)

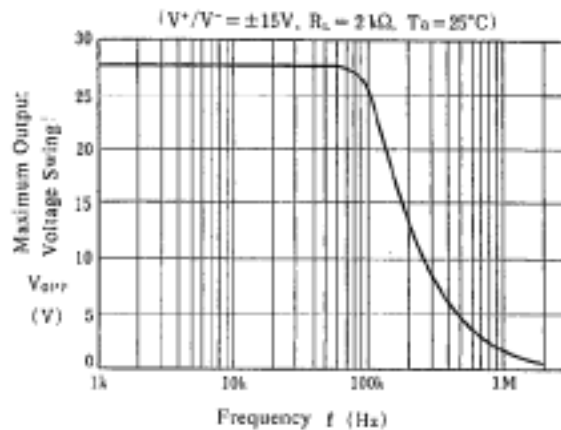
Parameter	Symbol	Test condition	Min	Typ	Max	Unit
Input Offset Voltage	V _{IO}	R _s ≤ 10kΩ		0.5	3	mV
Input Offset Current	I _{IO}			5	200	nA
Input Bias Current	I _B			100	500	nA
Large Signal Voltage Gain	A _v	R _L ≥ 2kΩ , V _o = ±10V	90	110		dB
Output Voltage Swing	V _{OM}	R _L ≥ 2kΩ	±12	±13.5		V
Input Common Mode Voltage Range	V _{ICM}		±12	±13.5		V
Common Mode Rejection Ratio	CMR	R _s ≤ 10kΩ	80	110		dB
Supply Voltage Rejection Ratio	SVR	R _s ≤ 10kΩ	80	110		dB
Operating Current	I _{cc}			6	9	mA
Slew Rate	SR	R _L ≥ 2kΩ		5		V/μA
Gain Bandwidth Product	GB	f=10kHz		15		MHz
Total Harmonic Distortion	THD	A _v = 20dB , V _o = 5V , f = 1kHz , R _L = 2kΩ		0.0005		%
Input Noise Voltage	V _{NI}	RIAA R _s = 2.2kΩ , 30kHz LPF		0.8		μV _{rms}

CHARACTERISTICS CURVES

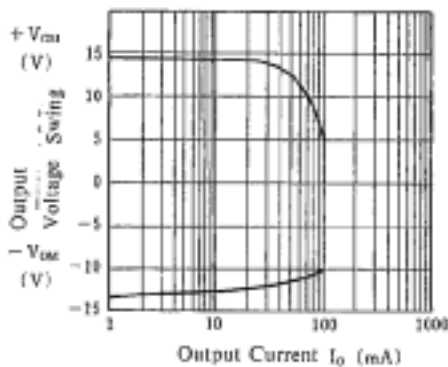
Maximum Output Voltage Swing vs. Load Resistance



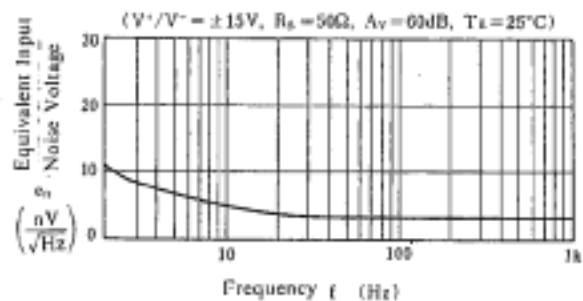
Maximum Output Voltage Swing vs. Frequency



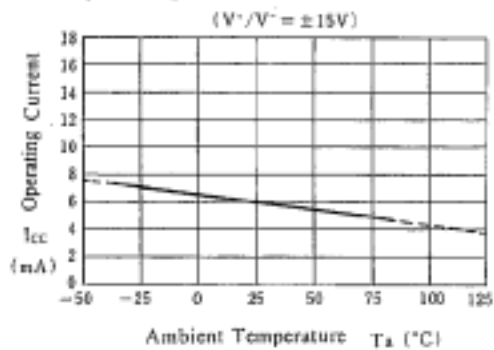
Output Voltage Swing vs. Output Current



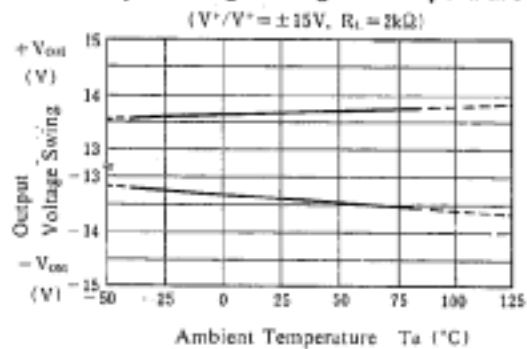
Equivalent Input Noise Voltage vs. Frequency



Operating Current vs. Temperature

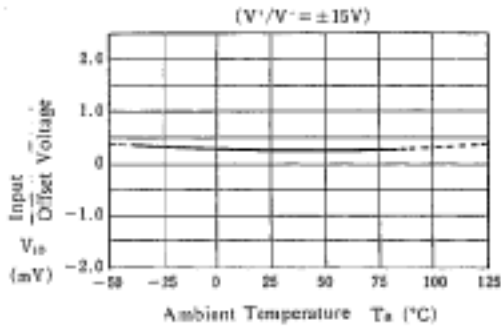


Output Voltage Swing vs. Temperature

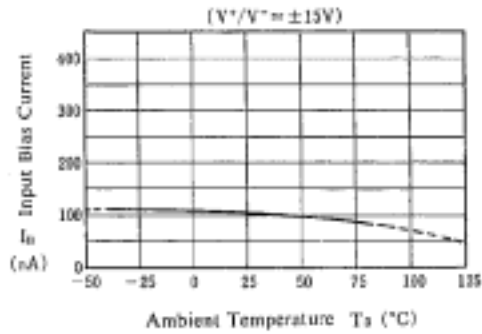


■ TYPICAL CHARACTERISTICS

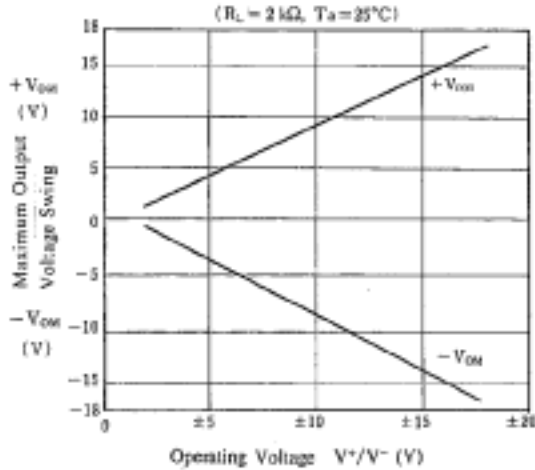
Input Offset Voltage vs. Temperature



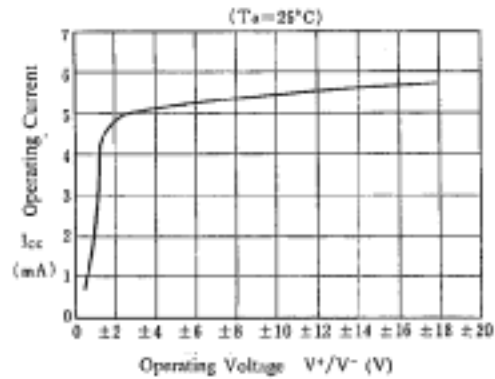
Input Bias Current vs. Temperature



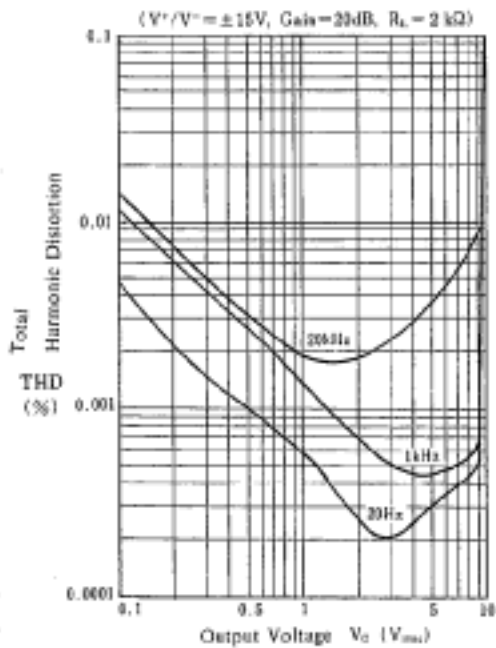
Maximum Output Voltage Swing vs. Operating Voltage



Operating Current vs. Operating Voltage



Total Harmonic Distortion vs. Output Voltage



Voltage Gain, Phase vs. Frequency

